



# POORNIMA

## COLLEGE OF ENGINEERING

Promoted by Shanti Education Society, Affiliated to Rajasthan Technical University & Approved by AICTE

### A REPORT ON EXPERT LECTURES

**TITLE AND DURATION:** A Seminar on Workshop on “Circuit Maker Simulation

" on 8<sup>th</sup> March, 2019 by Mr.Kuldeep Kaushik ,Alumni 2007-2011 at 11:30 AM.

**SPONSORS & SUPPORTERS:** Poornima College of Engineering, Jaipur

**ORGANIZER(S):** Department of Electronics & Communication Engineering, Jaipur.

### **EXPECTED OUTCOMES:**

Activity-1.1	Student will be able to enhance the knowledge of Semiconductor Industry.
Activity-1.2	Student will be able to recognize the different scopes of Electronics Engineering in Semiconductor Industry.
Activity-1.3	Student will be able to analyze their future perspective in the field of Electronics Industry.

## CO-PO-PSO Mapping:

Mapping Levels: 1-Low, 2- Moderate, 3-Strong

CO	PO												PSO		
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
CO1	-	-	-	-	-	3	-	-	-	-	-	-	2	3	2
CO2	-	-	-	-	-	-	-	-	3	-	-	-	2	3	2
CO3	-	-	-	-	-	-	-	-	-	-	-	3	2	3	2

## FLYER/POSTER:



# POORNIMA

## COLLEGE OF ENGINEERING

Affiliated to RTU, Kota • Approved by AICTE & UGC under 2(f) • Accredited by NBA

## WORKSHOP ON CIRCUIT MAKER SIMULATION



**Mr. Kuldeep Kaushik**  
Management Trainee at VVDN Technologies

**Organized by**  
**Department of Electronics and Communication Engineering**

 **Friday, March 8 2019**

 **Time 11:30 AM – 01:30 PM**

 **Venue:- PCE- CS-05**

RSVP  
 Dr. Garima Mathur  
[drg.mathur@poornima.org](mailto:drg.mathur@poornima.org)  
 9829393517

## **BRIEF BIODATA OF RESOURCE PERSON:**

Speaker: Mr. Kuldeep Kaushik, Alumni 2007-2011, Department of Electronics and Communication Engineering, Poornima College of Engineering, Jaipur Raj., Presently working in VVDN Technologies.

**ABOUT THE SESSION:** The main objective of this Workshop was to demonstrate all the necessary aspect to design a high quality printed circuit board .Dr.Mahesh Bundeale, Director, PCE,Jaipur and Mr. Pankaj Dhemla, Vice Principal, PCE,Jaipur gave welcome address and brief description about the Workshop. Speaker took an interactive session with participants. He Explained Workshop participants will learn about the schematic circuit then the PCB layout and then the 3d visualization. This workshop provides basic knowledge of electronics components, designing and simulation of basic circuits using software Proteus ISIS and development of PCB design using ARES software, which is beneficial for students in their projects development. Student will be able to recognize the different scopes of Electronics Engineering in Semiconductor Industry. They will also learn to apply the knowledge in practical circuits and systems and can enhance their fundamentals. Some hand-on session ns will give practical exposure. It is necessary for Electronics and Communication engineers, students and researchers to understand the basic designing of PCBs.The Session was so informative and nicely presented. After this wonderful session query session was conducted to resolve the doubts of participants. At last vote of thanks was delivered by Dr. Garima Mathur.

## **COURSE OBJECTIVES:**

At the end of this course, the students should be able to:

- Understand some basic concepts of Workshop
- Understanding the nature of problem to be studied and identifying the related area of knowledge.
- Reviewing literature to understand how others have approached or dealt with the problem.
- Collecting data in an organized and controlled manner so as to arrive at valid decisions.
- Analyzing data appropriate to the problem.

## GLIMPSES:





## LIST OF PARTICIPANTS:

S.No.	Name	Registration No.	Year	Branch
1	AADYA GUPTA	PCE15EC001	2rd	EC
2	ABHINAV NAVEEN	PCE15EC002	2rd	EC
3	ABHINAV SAXENA	PCE15EC003	2rd	EC
4	AKASH SHARMA	PCE15EC013	2rd	EC
5	AKASH SOLANKI	PCE15EC014	2rd	EC
6	AMAN JAIN	PCE15EC015	2rd	EC
7	BINAY PRATAP SINGH	PCE15EC029	2rd	EC
8	CHARVISHARMA	PCE15EC030	2rd	EC
9	CHIRAG GOYAL	PCE15EC031	2rd	EC
10	SUVITAMEENA	PCE15EC109	2rd	EC
11	TARUN SHARMA	PCE15EC110	2rd	EC
12	SHUBHAM SINGH	PCE15EC126	2rd	EC
13	VAISHALISHARMA	PCE16EC701	3rd	EC
14	VIVEKSHARMA	PCE15EC119	3rd	EC
15	YATHANSHARORA	PCE15EC121	3rd	EC
16	RITWIKBHOWMICK	PCE15EC085	3rd	EC
17	ROHITKUMARSHARMA	PCE15EC086	3rd	EC
18	RUPESHKUMARPRASAD	PCE15EC087	3rd	EC
19	NIKITAJEPH	PCE15EC067	3rd	EC
20	PANKAJ KUMARSHARMA	PCE15EC068	3rd	EC
21	PARAMVEERSINGH	PCE15EC069	3rd	EC
22	PARTH KAMRA	PCE15EC070	3rd	EC
23	AADITYASRIVASTAVA	PCE/EC/14/001	4th	EC
24	AYUSHKHANNA	PCE/EC/14/002	4th	EC
	AAYUSHI	PCE/EC/14/003	4th	EC
26	CHHAYASINGH	PCE/EC/14/031	4th	EC
27	DEEPAK KUMARJHA	PCE/EC/14/032	4th	EC
28	DEEPAK KUMARPANDEY	PCE/EC/14/033	4th	EC
29	JIGYASA	PCE/EC/14/046	4th	EC
30	JITENDRASHARMA	PCE/EC/14/047	4th	EC
31	KAREENABISHT	PCE/EC/14/048	4th	EC
	KRISHANGURJAR	PCE/EC/14/049	4th	EC
33	MANIK JAIN	PCE/EC/14/050	4th	EC
34	MAYANKDADHEECH	PCE/EC/14/051	4th	EC
35	PARIKSHITKUMAR	PCE/EC/14/074	4th	EC
36	POOJASHARMA	PCE/EC/14/075	4th	EC
37	SHIVANI KUMAWAT	PCE/EC/14/105	4th	EC

38	SHIVANI VYAS	PCE/EC/14/106	4th	EC
39	TARUN SINGH BHATI	PCE/EC/14/116	4th	EC
40	VAISHALIPANWAR	PCE/EC/14/117	4th	EC
41	venu GOPAL	PCE/EC/14/118	4th	EC
42	VIVEK KUMAR	PCE/EC/14/126	4th	EC
43	ARPITA SHUKLA	PCE/EC/14/033	4th	EC

## FEEDBACK ANALYSIS:

"Session was fantastic overall." "Openness and appreciation of students' questions and comments made a relatively large class feel like a seminar."

Are you willing to attend these type of webinars/FDPs in future from us:

127 responses

