POORNIMA
COLLEGE OF ENGINEERING

Approved by AICTE
Affiliated to Rajasthan Technical University, Kota
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Scheme and Syllabus
Rajasthan Technical University, Kota
(Session 2018-19)

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Scheme and Syllabus

Rajasthan Technical University, Kota

B. Tech. in 1st Year

(Session 2018-19)

Teaching and Examination Scheme

I Semester: B.Tech
Common to all branches of UG Engineering & Technology

SN	Category	Course Code	Course Title	Hours			Marks			Cr
				L	T	P	IA	ETE	Total	
1	BSC	1FY2-01	Engineering Mathematics-I	3	1	-	40	160	**200**	**4**
2	BSC	1FY2-02/ 1FY2-03	Engineering Physics/ Engineering Chemistry	3	1	-	40	160	**200**	**4**
3	HSMC	1FY1-04/ 1FY1-05	Communication Skills/ Human Values	2	-	-	20	80	**100**	**2**
4	ESC	1FY3-06/ 1FY3-07	Programming for Problem Solving/ Basic Mechanical Engineering	2	-	-	20	80	**100**	**2**
5	ESC	1FY3-08/ 1FY3-09	Basic Electrical Engineering/ Basic Civil Engineering	2	-	-	20	80	**100**	**2**
6	BSC	1FY2-20/ 1FY2-21	Engineering Physics Lab/ Engineering Chemistry Lab	-	-	2	30	20	**50**	**1**
7	HSMC	1FY1-22/ 1FY1-23	Language Lab/ Human Values Activities	-	-	2	30	20	**50**	**1**
8	ESC	1FY3-24/ 1FY3-25	Computer Programming Lab/ Manufacturing Practices Workshop	-	-	3	45	30	**75**	**1.5**
9	ESC	1FY3-26/ 1FY3-27	Basic Electrical Engineering Lab/ Basic Civil Engineering Lab	-	-	2	30	20	**50**	**1**
10	ESC	1FY3-28/ 1FY3-29	Computer Aided Engineering Graphics/ Computer Aided Machine Drawing	-	-	3	45	30	**75**	**1.5**
11	SODECA	1FY8-00							**25**	**0.5**
								Total	**1025**	**20.5**

L = Lecture, **T =** Tutorial,

P = Practical, **IA=**Internal Assessment**,**
ETE=End Term Exam, **Cr**=Credits

Teaching and Examination Scheme

II Semester: B.Tech.
Common to all branches of UG Engineering & Technology

SN	Category	Course Code	Course Title	Hours			Marks			Cr
				L	T	P	IA	ETE	Total	
1	BSC	2FY2-01	Engineering Mathematics-II	3	1	-	40	160	**200**	**4**
2	BSC	2FY2-03/ 2FY2-02	Engineering Chemistry/ Engineering Physics	3	1	-	40	160	**200**	**4**
3	HSMC	2FY1-05/ 2FY1-04	Human Values/ Communication Skills	2	-	-	20	80	**100**	**2**
4	ESC	2FY3-07/ 2FY3-06	Basic Mechanical Engineering/ Programming for Problem Solving	2	-	-	20	80	**100**	**2**
5	ESC	2FY3-09/ 2FY3-08	Basic Civil Engineering/ Basic Electrical Engineering	2	-	-	20	80	**100**	**2**
6	BSC	2FY2-21/ 2FY2-20	Engineering Chemistry Lab/Engineering Physics Lab	-	-	2	30	20	**50**	**1**
7	HSMC	2FY1-23/ 2FY1-22	Human Values Activities/ Language Lab	-	-	2	30	20	**50**	**1**
8	ESC	2FY3-25/ 2FY3-24	Manufacturing Practices Workshop/ Computer Programming Lab	-	-	3	45	30	**75**	**1.5**
9	ESC	2FY3-27/ 2FY3-26	Basic Civil Engineering Lab/Basic Electrical Engineering Lab	-	-	2	30	20	**50**	**1**
10	ESC	2FY3-29/ 2FY3-28	Computer Aided Machine Drawing/ Computer Aided Engineering Graphics	-	-	3	45	30	**75**	**1.5**
11	SODECA	1FY8-00							**25**	**0.5**
								Total	**1025**	**20.5**

L = Lecture, **T =** Tutorial,
P = Practical,**IA=**Internal Assessment**,**
ETE=End Term Exam, **Cr**=Credits

SYLLABUS
I Semester
Common to all branches of UG Engineering & Technology

1FY2-01: Engineering Mathematics-I

Credit: 4 **Max. Marks: 200 (IA:40, ETE:160)**

3L+1T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	**Calculus:** Improper integrals (Beta and Gamma functions) and their properties; Applications of definite integrals to evaluate surface areas and volumes of revolutions.	8
2	**Sequences and Series:** Convergence of sequence and series, tests for convergence; Power series, Taylor's series, series for exponential, trigonometric and logarithm functions.	6
3	**Fourier Series:** Periodic functions, Fourier series, Euler's formula, Change of intervals,Half range sine and cosine series, Parseval's theorem.	6
4	**Multivariable Calculus (Differentiation):** Limit, continuity and partial derivatives, directional derivatives, total derivative; Tangent plane and normal line; Maxima, minima and saddle points; Method of Lagrange multipliers; Gradient, curl and divergence.	10
5	**Multivariable Calculus (Integration):** Multiple Integration: Double integrals (Cartesian), change of order of integration in double integrals, Change of variables (Cartesian to polar), Applications: areas and volumes, Centre of mass and Gravity (constant and variable densities); Triple integrals (Cartesian), Simple applications involving cubes, sphere and rectangular parallelepipeds; Scalar line integrals, vector line integrals, scalar surface integrals, vector surface integrals, Theorems of Green, Gauss and Stokes.	10
	TOTAL	**40**

I & II Semester

Common to all branches of UG Engineering & Technology

1FY2-02/ 2FY2-02: Engineering Physics

Credit: 4 **Max. Marks: 200 (IA:40, ETE:160)**

3L+1T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	**Wave Optics:** Newton's Rings, Michelson's Interferometer, Fraunhofer Diffraction from a Single Slit. Diffraction grating: Construction, theory and spectrum, Resolving power and Rayleigh criterion for limit of resolution, Resolving power of diffraction grating, X-Ray diffraction and Bragg's Law.	9
2	**Quantum Mechanics:** Introduction to quantum Mechanics, Wave-particle duality, Matter waves, Wave function and basic postulates, Time dependent and time independent Schrodinger's Wave Equation, Physical interpretation of wave function and its properties, Applications of the Schrodinger's Equation: Particle in one dimensional and three dimensional boxes.	6
3	**Coherence and Optical Fibers:** Spatial and temporal coherence: Coherence length; Coherence time and 'Q' factor for light, Visibility as a measure of Coherence and spectral purity, Optical fiber as optical wave guide, Numerical aperture; Maximum angle of acceptance and applications of optical fiber.	4
4	**Laser:** Einstein's Theory of laser action; Einstein's coefficients; Properties of Laser beam, Amplification of light by population inversion, Components of laser, Construction and working of He-Ne and semiconductor lasers, Applications of Lasers in Science, engineering and medicine.	6
5	**Material Science & Semiconductor Physics:** Bonding in solids: covalent and metallic bonding, Energy bands in solids: Classification of solids as Insulators, Semiconductors and Conductors, Intrinsic and extrinsic semiconductors, Fermi dirac distribution function and Fermi energy, Conductivity in semiconductors, Hall Effect: Theory, Hall Coefficient and applications.	7
6	**Introduction to Electromagnetism:** Divergence and curl of electrostatic field, Laplace's and Poisson's equations for electrostatic potential, Bio-Savart law, Divergence and curl of static magnetic field, Faraday's law, Displacement current and magnetic field arising from time dependent electric field, Maxwell's equations, Flow of energy and Poynting vector.	8
	TOTAL	**40**

I & II Semester

Common to all branches of UG Engineering & Technology

1FY2-03/ 2FY2-03: Engineering Chemistry

Credit: 4 **Max. Marks: 200 (IA:40, ETE:160)**
3L+1T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	**Water:** Common impurities, hardness, determination of hardness by complexometric (EDTA method), Degree of hardness, Units of hardness Municipal water supply: Requisite of drinking water, Purification of water; sedimentation, filtration, disinfection, breakpoint chlorination. Boiler troubles: Scale and Sludge formation, Internal treatment methods, Priming and Foaming, Boiler corrosion and Caustic embrittlement Water softening; Lime-Soda process, Zeolite (Permutit) process, Demineralization process. Numerical problems based on Hardness, EDTA, Lime-Soda and Zeolite process.	10
2	**Organic Fuels:** Solid fuels: Coal, Classification of Coal, Proximate and Ultimate analyses of coal and its significance, Gross and Net Calorific value, Determination of Calorific value of coal by Bomb Calorimeter. Metallurgical coke, Carbonization processes; Otto-Hoffmann by-product oven method. Liquid fuels : Advantages of liquid fuels, Mining, Refining and Composition of petroleum, Cracking, Synthetic petrol, Reforming, Knocking, Octane number, Anti-knocking agents, Cetane number Gaseous fuels; Advantages, manufacturing, composition and Calorific value of coal gas and oil gas, Determination of calorific value of gaseous fuels by Junker's calorimeter Numerical problems based on determination of calorific value (bomb calorimeter/Junkers calorimeter/Dulongs formula, proximate analysis & ultimate and combustion of fuel.	10
3	**Corrosion and its control:** Definition and significance of corrosion, Mechanism of chemical (dry) and electrochemical (wet) corrosion, galvanic corrosion, concentration corrosion and pitting corrosion. Protection from corrosion; protective coatings-galvanization and tinning, cathodic protection, sacrificial anode and modifications in design.	3
4	**Engineering Materials:** Portland Cement; Definition, Manufacturing by Rotary kiln. Chemistry of setting and hardening of cement. Role of Gypsum. Glass: Definition, Manufacturing by tank furnace, significance of annealing, Types and properties of soft glass, hard glass, borosilicate glass, glass wool, safety glass Lubricants: Classification, Mechanism, Properties; Viscosity and viscosity index, flash and fire point, cloud and pour point.	10

	Emulsification and steam emulsion number.	
5	**Organic reaction mechanism and introduction of drugs:** Organic reaction mechanism: Substitution; SN1, SN2, Elecrophilic aromatic substitution in benzene, free radical halogenations of alkanes, Elimination; elimination in alkyl halides, dehydration of alcohols, Addition: electrophilic and free radical addition in alkenes, nucleophilic addition in aldehyde and ketones, Rearrangement; Carbocation and free radical rearrangements Drugs : Introduction, Synthesis, properties and uses of Aspirin, Paracetamol	**7**
	TOTAL	**40**

I & II Semester

Common to all branches of UG Engineering & Technology

1FY1-04/ 2FY1-04: Communication Skills

Credit: 2 **Max. Marks: 100 (IA:20, ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**Communication:** Meaning, Importance and Cycle of Communication. Media and Types of Communication. Verbal and Non-Verbal Communication. Barriers to communication. Formal and Informal Channels of Communication (Corporate Communication). Divisions of Human Communication and Methods to improve Interpersonal Communication. Qualities of good communication.	5
2	**Grammar:** Passive Voice. Reported Speech. Conditional Sentences. Modal Verbs. Linking Words (Conjunctions)	5
3	**Composition:** Job Application and Curriculum-Vitae Writing. Business Letter Writing. Paragraph Writing. Report Writing.	5
4	**Short Stories:** "Luncheon" by Somerset Maugham."How Much Land Does a Man Need?" by Count Leo Tolstoy. "The Night Train at Deoli" by Ruskin Bond.	5
5	**Poems:** "No Men are Foreign" by James Kirkup. "If" by Rudyard Kipling. "Where the Mind is without Fear" by Rabindranath Tagore.	5
	TOTAL	25

I & II Semester
Common to all branches of UG Engineering & Technology

1FY1-05/ 2FY1-05: Human Values

Credit: 2 **Max. Marks: 100 (IA:20, ETE:80)**
2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**Course Introduction - Need, Basic Guidelines, Content and Process for Value Education** Understanding the need, basic guidelines, Self Exploration - its content and process; 'Natural Acceptance' and Experiential Validation, Continuous Happiness and Prosperity- Human Aspirations, Right understanding, Relationship and Physical Facilities,Understanding Happiness and Prosperity correctly- A critical appraisal of the current scenario. Method to fulfill the above human aspirations: understanding and living in harmony at various levels	5
2	**Understanding Harmony in the Human Being - Harmony in Myself** Understanding human being as a co-existence of the sentient 'I' and the material 'Body' Understanding the needs of Self ('I') and 'Body' - Sukh and Suvidha Understanding the Body as an instrument of 'I',Understanding the characteristics and activities of 'I' and harmony in 'I' Understanding the harmony of I with the Body: Sanyam and Swasthya; correct appraisal of Physical needs, meaning of Prosperity in detail, Programs to ensure Sanyam and Swasthya.	5
3	**Understanding Harmony in the Family and Society- Harmony in Human-Human Relationship** Understanding harmony in the Family, Understanding values in human-human relationship; meaning of Nyaya and program for its fulfillment to ensure Ubhay-tripti; Trust (Vishwas) and Respect (Samman) , meaning of Vishwas; Difference between intention and competence, meaning of Samman, Difference between respect and differentiation; the other salient values in relationship, harmony in the society , Samadhan, Samridhi, Abhay, Sah-astitva as comprehensive Human Goals ,Visualizing a universal harmonious order in society- Undivided Society (AkhandSamaj), Universal Order (SarvabhaumVyawastha)- from family to world family.	5
4	**Understanding Harmony in the Nature and Existence - Whole existence as Coexistence** Understanding the harmony in the Nature. Interconnectedness and mutual fulfillment among the four orders of nature- recyclability and self-regulation in nature. Understanding Existence as Co-existence	5

	(Sah-astitva) of mutually interacting units in allpervasive Space. Holistic perception of harmony at all levels of existence	
5	**Implications of the above Holistic Understanding of Harmony on Professional Ethics. Natural acceptance of human values** Definitiveness of Ethical Human Conduct. Basis for Humanistic Education, Humanistic Constitution and Humanistic Universal Order. Competence in Professional Ethics: a) Ability to utilize the professional competence for augmenting universal human order, b) Ability to identify the scope and characteristics of people-friendly and eco-friendly production systems, technologies and management models. Strategy for transition from the present state to Universal Human Order: At the level of individual: as socially and ecologically responsible engineers, technologists and managers. Case studies related to values in professional life and individual life.	**5**
	TOTAL	**25**

1FY3-06/ 2FY3-06: Programming for Problem Solving

Credit: 2 **Max. Marks: 100 (IA:20, ETE:80)**
2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**Fundamentals of Computer:** Stored program architecture of computers, Storage device- Primary memory, and Secondary storage, Random, Direct, Sequential access methods, Concepts of High-level, Assembly and Low-level languages, Representing algorithms through flowchart and pseudo code.	**8**
2	**Number system:** Data representations, Concepts of radix and representation of numbers in radix r with special cases of r=2, 8, 10 and 16 with conversion from radix r1 to r2, r's and (r-1)'s complement, Binary addition, Binary subtraction, Representation of alphabets.	**8**
3	**C Programming:** Problem specification, flow chart, data types, assignment statements, input output statements, developing simple C programs, If statement, for loops, while loops, do-while loops, switch statement, break statement, continue statement, development of C programs using above statements, Arrays, functions, parameter passing, recursion, Programming in C using these statements, Structures, files, pointers and multi file handling.	**12**
	TOTAL	**28**

I & II Semester

Common to all branches of UG Engineering & Technology

1FY3-07/ 2FY3-07: Basic Mechanical Engineering

Credit: 2 **Max. Marks: 100 (IA:20, ETE:80)**
2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**Fundamentals:** Introduction to mechanical engineering, concepts of thermal engineering, mechanical machine design, industrial engineering and manufacturing technology. Steam Boilers classification and types of steam boilers and steam turbines. Introduction and Classification of power plants.	
2	**Pumps and IC Engines:** Applications and working of Reciprocating and Centrifugal pumps. Introduction, Classification of IC Engines, Main Components of IC Engines, Working of IC Engines and its components.	
3	**Refrigeration and Air Conditioning:** Introduction, classification and types of refrigeration systems and air-conditioning. Applications of refrigeration and Air-conditioning.	
4	**Transmission of Power:** Introduction and types of Belt and Rope Drives, Gears.	
5	**Primary Manufacturing Processes:** Metal Casting Process: Introduction to Casting Process, Patterns, Molding, Furnaces. Metal Forming Processes: Introduction to Forging, Rolling, Extrusion, Drawing. Metal Joining Processes: Introduction to various types of Welding, Gas Cutting, Brazing, and Soldering.	
6	**Engineering Materials and Heat Treatment of Steel:** Introduction to various engineering materials and their properties.	

I & II Semester

Common to all branches of UG Engineering & Technology

1FY3-08/ 2FY3-08: Basic Electrical Engineering

Credit: 2 **Max. Marks: 100 (IA:20, ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**DC Circuits:** Electrical circuit elements (R, L and C), voltage and current sources, Kirchhoff current and voltage laws, Series-Parallel circuits, Node voltage method, Mesh current method, Superposition, Thevenin's, Norton's and Maximum power transfer theorems.	5
2	**AC Circuits:** Representation of sinusoidal waveforms, peak and r.m.s values, phasor representation, real power, reactive power, apparent power, power factor. Analysis of single-phase AC circuits consisting of R, L, C, RL, RC and RLC combinations (series and parallel), resonance. Three phase balanced circuits, voltage and current relations in star and delta connections.	4
3	**Transformers:** Ideal and practical transformer, EMF equation, equivalent circuit, losses in transformers, regulation and efficiency.	4
4	**Electrical Machines:** Generation of rotating magnetic fields, Construction and working of a three-phase induction motor, Significance of torque-slip characteristic. Starting and speed control of induction motor, single-phase induction motor. Construction, working, torque-speed characteristic and speed control of separately excited DC motor. Construction and working of synchronous generators.	7
5	**Power Converters:** Semiconductor PN junction diode and transistor (BJT). Characteristics of SCR, power transistor and IGBT. Basic circuits of single phase rectifier with R load, Single phase Inverter, DC-DC converter.	4
6	**Electrical Installations:** Layout of LT switchgear: Switch fuse unit (SFU), MCB, ELCB, MCCB, Type of earthing. Power measurement, elementary calculations for energy consumption.	4
	TOTAL	**28**

I & II Semester
Common to all branches of UG Engineering & Technology

1FY3-09/ 2FY3-09: Basic Civil Engineering

Credit: 2 **Max. Marks: 100 (IA:20, ETE:80)**
2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**Introduction to objective, scope and outcome the subject**	1
2	**Introduction:** Scope and Specialization of Civil Engineering, Role of civil Engineer in Society, Impact of infrastructural development on economy of country.	2
3	**Surveying:** Object, Principles & Types of Surveying; Site Plans, Plans& Maps; Scales & Unit of different Measurements. Linear Measurements: Instruments used. Linear Measurement by Tape, Ranging out Survey Lines and overcoming Obstructions; Measurements on sloping ground; Tape corrections, conventional symbols. Angular Measurements: Instruments used; Introduction to Compass Surveying,Bearings and Longitude & Latitude of a Line, Introduction to total station. Levelling: Instrument used, Object of levelling, Methods of levelling in brief, Contour maps.	8
4	**Buildings:** Selection of site for Buildings, Layout of Building Plan, Types of buildings, Plinth area, carpet area, floor space index, Introduction to building byelaws, concept of sun light and ventilation. Components of Buildings & their functions, Basic concept of R.C.C., Introduction to types of foundation.	3
5	**Transportation:** Introduction to Transportation Engineering; Traffic and Road Safety: Types and Characteristics of Various Modes of Transportation; Various Road Traffic Signs, Causes of Accidents and Road Safety Measures.	2
6	**Environmental Engineering:** Environmental Pollution, Environmental Acts and Regulations, Functional Concepts of Ecology, Basics of Species, Biodiversity, Ecosystem, Hydrological Cycle;Chemical Cycles: Carbon, Nitrogen& Phosphorus; Energy Flow in Eco-systems. Water Pollution: Water Quality standards, Introduction to Treatment & Disposal of Waste Water. Reuse and Saving of Water, Rain Water Harvesting.	4 3 2

	Solid Waste Management: Classification of Solid Waste, Collection, Transportation and Disposal of Solid. Recycling of Solid Waste: Energy Recovery,Sanitary Land fill, On-Site Sanitation. Air& Noise Pollution: Primary and Secondary air pollutants, Harmful effects of Air Pollution, Control of Air Pollution. . Noise Pollution, Harmful Effects of noise pollution, control of noise pollution, Global warming& Climate Change, Ozone depletion, Green House effect	**3**
	TOTAL	**28**

1FY2-20/ 2FY2-20: Engineering Physics Lab

Credit: 1 **Max. Marks: 50 (IA:30, ETE:20)**
0L+0T+2P

1. To determine the wave length of monochromatic light with the help of Michelson's interferometer.
2. To determine the wave length of sodium light by Newton's Ring.
3. To determine the wave length of prominent lines of mercury by plane diffraction grating with the help of spectrometer.
4. Determination of band gap using a P-N junction diode.
5. To determine the height of given object with the help of sextant.
6. To determine the dispersive power of material of a prism with the help of spectrometer.
7. To study the charge and discharge of a condenser and hence determine the same constant (both current and voltage graphs are to be plotted.
8. To determine the coherence length and coherence time of laser using He – Ne laser.
9. To measure the numerical aperture of an optical fibre.
10. To study the Hall Effect and determine the Hall Voltage and Hall coefficients.

I & II Semester
Common to all branches of UG Engineering & Technology

1FY2-21/ 2FY2-21: Engineering Chemistry Lab

Credit: 1 **Max. Marks: 50 (IA:30, ETE:20)**
0L+0T+2P

1. Determination the hardness of water by EDTA method
2. Determination of residual chlorine in water
3. Determination of dissolved oxygen in water
4. Determination of the strength of Ferrous Ammonium sulphate solution with the help of $K_2Cr_2O_7$ solution by using diphenyl amine indicator
5. Determination of the strength of $CuSO_4$ solution iodometrically by using hypo solution
6. Determination of the strength of NaOH and Na_2CO_3 in a given alkali mixture
7. Proximate analysis of Coal
8. Determination of the flash & fire point and cloud & pour point of lubricating oil
9. Determination of the kinematic viscosity of lubricating oil by Redwood viscometer no. 1 at different temperature
10. Synthesis of Aspirin/ Paracetamol

I & II Semester
Common to all branches of UG Engineering & Technology

1FY2-22/ 2FY2-22: Language Lab

Credit: 1 **Max. Marks: 50 (IA:30, ETE:20)**
0L+0T+2P

1. Phonetic Symbols and Transcriptions.
2. Extempore.
3. Group Discussion.
4. Dialogue Writing.
5. Listening comprehension.

I & II Semester
Common to all branches of UG Engineering & Technology

1FY2-23/ 2FY2-23: Human Values Activities

Credit: 1 **Max. Marks: 50 (IA:30, ETE:20)**
0L+0T+2P

PS 1:
Introduce yourself in detail. What are the goals in your life? How do you set your goals in your life? How do you differentiate between right and wrong? What have been your salient achievements and shortcomings in your life? Observe and analyze them.

PS 2:
Now-a-days, there is a lot of talk about many technogenic maladies such as energy and material resource depletion, environmental pollution, global warming, ozone depletion, deforestation, soil degradation, etc. - all these seem to be manmade problems, threatening the survival of life Earth - What is the root cause of these maladies & what is the way out in opinion?
On the other hand, there is rapidly growing danger because of nuclear proliferation, arms race, terrorism, breakdown of relationships, generation gap, depression & suicidal attempts etc. - what do you think, is the root cause of these threats to human happiness and peace - what could be the way out in your opinion?

PS 3:
1. Observe that each of us has the faculty of 'Natural Acceptance', based on which one can verify what is right or not right for him. (As such we are not properly trained to listen to our 'Natural Acceptance' and may a time it is also clouded by our strong per-conditioning and sensory attractions).

Explore the following:
(i) What is Naturally Acceptable' to you in relationship the feeling of respect or disrespect for yourself and for others?
(ii) What is 'naturally Acceptable' to you - to nurture or to exploit others?

Is your living in accordance with your natural acceptance or different from it?
2. Out of the three basic requirements for fulfillment of your aspirations - right understanding, relationship and physical facilities - observe how the problems in your family are related to each. Also observe how much time & effort you devote for each in your daily routine.

PS 4:
list down all your important desires. Observe whether the desire is related to Self (I) or the Body. If it appears to be related to both, visualize which part of it is related to Self (I) and which part is related to Body.

PS 5:
1. a. Observe that any physical facility you use, follows the given sequence with time:

Necessary and tasteful - unnecessary but still tasteful - unnecessary and tasteless - intolerable

b. In contrast, observe that any feeling in you is either naturally acceptable or not acceptable at all. If not acceptable, you want it continuously and if not acceptable, you do not want it any moment!

2. List down all your important activities. Observe whether the activity is of 'I' or of

Body or with the participation of both or with the participation of both 'I' and Body.
3. Observe the activities within 'i'. Identify the object of your attention for different moments (over a period of sy 5 to 10 minutes) and draw a line diagram connecting these points. Try observe the link between any two nodes.

PS 6:

1. Chalk out some programs towards ensuring your harmony with the body - in terms of nurturing, protection and right utilization of the body.
2. Find out the plants and shrubs growing in and around your campus, which can be useful in curing common diseases.

PS 7:

Form small groups in the class and make them carry out a dialogue focusing on the following eight questions related to 'TRUST';
1a. Do I want to make myself happy?
2a. Do I want to make the other happy?
3a. Does the other want to make himself/herself happy?
4a. Does the other want to make me happy?
What is the answer?
Intention (Natural Acceptance)
1b. Am I able to always make myself happy?
2b. Am I able to always make the other happy?
3b. Is the other able to always make himself/herself happy?
What is the answer?
Let each student answer the questions for himself and everyone else. Discuss the difference between intention and competence. Observe whether you evaluate yourself and others on the basis of intention/competence.

PS 8:

1. Observe, on how many occasions, you are able to respect your related ones (by doing the right evaluation) and on how many occasions you are disrespecting by way of under-evaluation, over-evaluation or otherwise evaluation.
2. Also, observe whether your feeling of respect is based on treating the other as you would treat yourself or on differentiations based on body, physical facilities or belieds.

PS 9:

1. Write a narration in the form of a story, poem, skit or essay to clarify a salient Human Value to the children.
2. Recollect and narrate an incident in your life where you were able to exhibit willful adherence to balues in a difficult situation.

PS 10:

List down some common units (things) of Nature which you come across in your daily life and classify them in the four orders of Nature. Analysis and explain the aspect of mutual fulfillment of each unit with other orders.

PS 11:

Make a chart to show the whole existence as co-existence. With the help of this chart try to identify the role and the scope of some of the courses of your study. Also indicate the areas which are being either over-emphasized or ignored in the present context.

PS 12:

Identify any two important problems being faced by the society today and analyze the root cause of these problems. Can these be solved on the basic of natural acceptance of human values. If so, how should one proceed in this direction from

the present situation?

PS 13:

1. Suggest ways in which you can use your knowledge of Science/Technology/Management etc. for moving towards a universal human order.
2. Propose a broad outline for humanistic Constitution at the level of Nation.

PS 14:

The course is going to be over now. It is time to evaluate what difference in your thinking it has made. Summarize the core massage of this course grasped by you. How has this affected you in terms of;

a. Thought
b. Behavior
c. Work and
d. Relization

What practical steps are you able to visualize for the transition of the society from its present state.

Project:

Every student required to take-up a social project e.g. educating children in needy/weaker section, services in hospitals, NGO's and other such work i.e. social work at villages adopted by respective institute/ college.

I & II Semester
Common to all branches of UG Engineering & Technology

1FY3-24/ 2FY3-24: Computer Programming Lab

Credit: 1.5 **Max. Marks: 75 (IA:45, ETE:30)**
0L+0T+3P

1. To learn about the C Library, Preprocessor directive, Input-output statement.
2. Programs to learn data type, variables, If-else statement
3. Programs to understand nested if-else statement and switch statement
4. Programs to learn iterative statements like while and do-while loops
5. Programs to understand for loops for iterative statements
6. Programs to learn about array and string operations
7. Programs to understand sorting and searching using array
8. Programs to learn functions and recursive functions
9. Programs to understand Structure and Union operation
10. Programs to learn Pointer operations
11. Programs to understand File handling operations
12. Programs to input data through Command line argument

I & II Semester

Common to all branches of UG Engineering & Technology

1FY3-25/ 2FY3-25: Manufacturing Practices Workshop

Credit: 1.5 **Max. Marks: 75 (IA:45, ETE:30)**
0L+0T+3P

Carpentry Shop

1. T – Lap joint
2. Bridle joint

Foundry Shop

3. Mould of any pattern
4. Casting of any simple pattern

Welding Shop

5. Lap joint by gas welding
6. Butt joint by arc welding
7. Lap joint by arc welding
8. Demonstration of brazing, soldering & gas cutting

Machine Shop Practice

9. Job on lathe with one step turning and chamfering operations

Fitting and Sheet Metal Shop

10. Finishing of two sides of a square piece by filing
11. Making mechanical joint and soldering of joint on sheet metal
12. To cut a square notch using hacksaw and to drill a hole and tapping

1FY3-26/ 2FY3-26: Basic Electrical Engineering Lab

Credit: 1 **Max. Marks: 50 (IA:30, ETE:20)**
0L+0T+2P

1. Basic safety precautions. Introduction and use of measuring instruments – voltmeter, ammeter, multi-meter, oscilloscope. Real-life resistors, capacitors and inductors.
2. Transformers: Observation of the no-load current waveform on an oscilloscope. Loading of a transformer: measurement of primary and secondary voltages and currents, and power.
3. Three-phase transformers: Star and Delta connections. Voltage and Current relationships (line-line voltage, phase-to-neutral voltage, line and phase currents).Phase-shifts between the primary and secondary side.
4. Demonstration of cut-out sections of machines: dc machine (commutator-brush arrangement), induction machine (squirrel cage rotor), synchronous machine (field winging - slip ring arrangement) and single-phase induction machine.
5. Torque Speed Characteristic of separately excited dc motor.
6. Demonstration of (a) dc-dc converters (b) dc-ac converters – PWM waveform (c) the use of dc-ac converter for speed control of an induction motor and (d) Components of LT switchgear.

1FY3-27/ 2FY3-27: Basic Civil Engineering Lab

Credit: 1 **Max. Marks: 50 (IA:30, ETE:20)**
0L+0T+2P

1. Linear Measurement by Tape:
   a) Ranging and Fixing of Survey Station along straight line and across obstacles.
   b) Laying perpendicular offset along the survey line
2. Compass Survey: Measurement of bearing of linesusing Surveyor's and Prismatic compass
3. Levelling: Using Tilting/ Dumpy/ Automatic Level
   a) To determine the reduced levels in closed circuit.
   b) To carry out profile levelling and plot longitudinal and cross sections for road by Height of Instrument and Rise & Fall Method.
4. To study and take measurements using various electronic surveying instruments like EDM, Total Station etc.
5. To determine pH, hardness and turbidity of the given sample of water.
6. To study various water supply Fittings.
7. To determine the pH and total solids of the given sample of sewage.
8. To study various Sanitary Fittings.

1FY3-28/ 2FY3-28: Computer Aided Engineering Graphics

Credit: 1.5 **Max. Marks: 75 (IA:45, ETE:30)**
0L+0T+3P

Introduction: Principles of drawing, lines, type of lines, usage of Drawing instruments, lettering, Conic sections including parabola, hyperbola, Rectangular Hyperbola (General method only); Scales-Plain, Diagonal and Vernier Scales.

Projections of Point & Lines: Position of Point, Notation System, Systematic Approach for projections of points, front view & Top view of point, Position of straight lines, line parallel to Both the RPs, Line perpendicular to either of the RPs, Line inclined to one RP and parallel to the other, Line inclined to Both the RPs, Traces of a line (One drawing sheet, one assignment in sketch book).

Projection of Planes: Positions of planes, Terms used in projections of planes, plane parallel to RP, plane inclined to one RP and perpendicular to the other RP, plane perpendicular to Both the RPs, plane Inclined to Both the RPs, True shape of the plane, Distance of a point from plane, Angle between two planes.

Projections of Regular Solids: frustum and truncated solids, those inclined to both the Planes-Auxiliary Views.

Section of Solids: Theory of sectioning, section of prisms and cubes, section of pyramids and Tetrahedron section of Cylinders, section of cones, section of spheres (One drawing sheet, one assignment in sketch book)

Overview of Computer Graphics : Covering theory of CAD software [such as: The menu System, Toolbars (standard, Object Properties, Draw, Modify and Dimension), Drawing Area (Background, Crosshairs, Coordinate System), Dialog boxes and windows, Shortcut menus (Button Bars), Command Line (where applicable), The Status Bar, Different methods of zoom as used in CAD, Select and erase objects.: Isometric Views of lines, Planes, Simple and compound Solids.

1FY3-29/ 2FY3-29: Computer Aided Machine Drawing

Credit: 1.5 **Max. Marks: 75 (IA:45, ETE:30)**
0L+0T+3P

Introduction: Principles of drawing, conventional representation of machine components and materials, lines, types of lines, dimensioning types, rules of dimensioning.

Conversion of pictorial views into orthographic views: (1 drawing sheet) Introduction to orthographic projection, concept of first angle and third angle projection, drawing of simple machine elements in first angle projection, missing view problems covering Principles of Orthographic Projections.

Sectional views of mechanical components: (1 drawing sheet) Introduction, cutting plane line, type of sectional views-full section, half section, partial or broken section, revolved section, removed section, offset section, sectioning conventions-spokes, web rib, shaft, pipes, different types of holes, conventions of section lines for different metals and materials.

Fasteners and other mechanical components: (Free hand sketch) Temporary and permanent fasteners, thread nomenclature and forms, thread series, designation, representation of threads, bolted joints, locking arrangement of nuts, screws, washers, foundation bolts etc., keys, types of keys, cotter and knuckle joints. Riveted joints, rivets and riveting, type of rivets, types of riveted joints etc. Bearing: Ball, roller, needle, foot step bearing. Coupling: Protected type, flange, and pin type flexible coupling. Other components: Welded joints, belts and pulleys, pipes and pipe joints, valves etc.

Overview of Computer Graphics: (2 drawing sheets) Covering theory of CAD software such as: The menu System, Toolbars (Standard, Object Properties, Draw, Modify and Dimension), Drawing Area (Background, Crosshairs, Coordinate System), Dialog boxes and windows, Shortcut menus (Button Bars), Command Line (Where applicable), The Status Bar, Different methods of zoom as used in CAD, Select and erase objects.: Isometric Views of Lines, Planes, Simple and compound Solids.

II Semester

Common to all branches of UG Engineering & Technology

2FY2-01: Engineering Mathematics-II

Credit: 4 **Max. Marks: 200 (IA:40, ETE:160)**

3L+1T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	**Matrices:** Rank of a matrix, rank-nullity theorem; System of linear equations; Symmetric, skew-symmetric and orthogonal matrices; Eigenvalues and eigenvectors; Diagonalization of matrices; Cayley-Hamilton Theorem, and Orthogonal transformation.	10
2	**First order ordinary differential equations:** Linear and Bernoulli's equations, Exact equations, Equations not of first degree: equations solvable for p, equations solvable for y, equations solvable for x and Clairaut's type.	6
3	**Ordinary differential equations of higher orders:** Linear Differential Equations of Higher order with constant coefficients, Simultaneous Linear Differential Equations, Second order linear differential equations with variable coefficients: Homogenous and Exact forms, one part of CF is known, Change of dependent and independent variables, method of variation of parameters, Cauchy-Euler equation; Power series solutions including Legendre differential equation and Bessel differential equations.	12
4	**Partial Differential Equations – First order:** Order and Degree, Formation; Linear Partial differential equations of First order, Lagrange's Form, Non Linear Partial Differential equations of first order, Charpit's method, Standard forms.	6
5	**Partial Differential Equations– Higher order:** Classification of Second order partial differential equations, Separation of variables method to simple problems in Cartesian coordinates including two dimensional Laplace, one dimensional Heat and one dimensional Wave equations.	6
	TOTAL	**40**

Scheme and Syllabus

Rajasthan Technical University, Kota

B. Tech. in Computer Engineering

(Session 2018-19)

Scheme of

UNDERGRADUATE DEGREE COURSE

Computer Science and Engineering

Rajasthan Technical University, Kota

Effective from session: 2018 – 2019

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Teaching & Examination Scheme
B.Tech. : Computer Science & Engineering
2nd Year - III Semester

SN	Category	Course		Contact hrs/week			Marks				Cr
		Code	Title	L	T	P	Exm Hrs	IA	ETE	Total	
THEORY											
1	BSC	3CS2-01	Advanced Engineering Mathematics	3	0	0	3	30	120	**150**	**3**
2	HSMC	3CS1-02/ 3CS1-03	Technical Communication/ Managerial Economics and Financial Accounting	2	0	0	2	20	80	**100**	**2**
3	ESC	3CS3-04	Digital Electronics	3	0	0	3	30	120	**150**	**3**
4	PCC	3CS4-05	Data Structures and Algorithms	3	0	0	3	30	120	**150**	**3**
5		3CS4-06	Object Oriented Programming	3	0	0	3	30	120	**150**	**3**
6		3CS4-07	Software Engineering	3	0	0	3	30	120	**150**	**3**
			Sub Total	**17**	**0**	**0**		**170**	**680**	**850**	**17**
PRACTICAL & SESSIONAL											
7	PCC	3CS4-21	Data Structures and Algorithms Lab	0	0	3		45	30	**75**	**1.5**
8		3CS4-22	Object Oriented Programming Lab	0	0	3		45	30	**75**	**1.5**
9		3CS4-23	Software Engineering Lab	0	0	3		45	30	**75**	**1.5**
10		3CS4-24	Digital Electronics Lab	0	0	3		45	30	**75**	**1.5**
11	PSIT	3CS7-30	Industrial Training	0	0	1		0	0	**50**	**1**
12	SODECA	3CS8-00	Social Outreach, Discipline & Extra Curricular Activities							**25**	**0.5**
			Sub- Total	**0**	**0**	**13**		**180**	**120**	**375**	**7.5**
			TOTAL OF III SEMESTER	**17**	**0**	**13**		**350**	**800**	**1225**	**24.5**

L: *Lecture,* ***T:*** *Tutorial,* ***P:*** *Practical,* ***Cr:*** *Credits*

ETE: *End Term Exam,* ***IA:*** *Internal Assessment*

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Teaching & Examination Scheme
B.Tech. : Computer Science & Engineering
2nd Year - IV Semester

SN	Category	Course		Contact hrs/week			Marks				Cr
		Code	Title	L	T	P	Exm Hrs	IA	ETE	Total	
THEORY											
1	BSC	4CS2-01	Discrete Mathematics Structure	3	0	0	3	30	120	**150**	**3**
2	HSMC	4CS1-03/ 4CS1-02	Managerial Economics and Financial Accounting /Technical Communication	2	0	0	2	20	80	**100**	**2**
3	ESC	4CS3-04	Microprocessor & Interfaces	3	0	0	3	30	120	**150**	**3**
4	PCC	4CS4-05	Database Management System	3	0	0	3	30	120	**150**	**3**
5		4CS4-06	Theory of Computation	3	0	0	3	30	120	**150**	**3**
6		4CS4-07	Data Communication and Computer Networks	3	0	0	3	30	120	**150**	**3**
			Sub Total	**17**	**0**	**0**		**170**	**680**	**850**	**17**
PRACTICAL & SESSIONAL											
7	PCC	4CS4-21	Microprocessor & Interfaces Lab	0	0	2		30	20	**50**	**1**
8		4CS4-22	Database Management System Lab	0	0	3		45	30	**75**	**1.5**
9		4CS4-23	Network Programming Lab	0	0	3		45	30	**75**	**1.5**
10		4CS4-24	Linux Shell Programming Lab	0	0	2		30	20	**50**	**1**
11		4CS4-25	Java Lab	0	0	2		30	20	**50**	**1**
12	SODECA	4CS8-00	Social Outreach, Discipline & Extra Curricular Activities							**25**	**0.5**
			Sub- Total	**0**	**0**	**12**		**180**	**120**	**325**	**6.5**
			TOTAL OF IV SEMEESTER	**17**	**0**	**12**		**350**	**800**	**1175**	**23.5**

L: *Lecture,* ***T:*** *Tutorial,* ***P:*** *Practical,* ***Cr:*** *Credits*

ETE: *End Term Exam,* ***IA:*** *Internal Assessment*

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

V	Computer Science and Engineering	Teaching Hrs			Exm Hrs	Max. Marks			Comments
Course Code	Subject Name (Revised)	L	T	P		IA	TE	TOTAL	Common to
5CS1A	Computer Architecture	3			3	20	80	100	CS, IT
5CS2A	Digital Logic Design	3	-		3	20	80	100	
5CS3A	Telecommunication Fundamentals	3	-	-	3	20	80	100	CS, IT
5CS4A	Database Management Systems	3			3	20	80	100	CS, IT
5CS5A	Operating Systems	3	-		3	20	80	100	CS, IT
5CS6.1A	Advanced Data Structure	3	-		3	20	80	100	CS, IT
5CS6.2A	Digital Signal Processing								CS, IT
5CS6.3A	Information Theory & Coding								CS, IT
5CS7A	DataBase Lab			3		60	40	100	CS, IT
5CS8A	System Design in UML Lab.	-	-	3		45	30	75	
5CS9A	Operating Systems Simulation Lab			3		60	40	100	CS, IT
5CS10	Digital Hardware Design Lab			3		45	30	75	CS, IT
5CSDC	Discipline & Extra Curricular Activities							50	CS, IT
		18	0	12				1000	

VI	Computer Science and Engineering	Teaching Hrs			Exm Hrs	Max. Marks			Comments
Course Code	Subject Name (Revised)	L	T	P		IA	TE	TOTAL	Common to
6CS1A	Computer Networks	3	-		3	20	80	100	CS, IT
6CS2A	Design and Analysis of Algorithms	3	-		3	20	80	100	CS, IT
6CS3A	Theory Of Computation	3			3	20	80	100	CS, IT
6CS4A	Computer Graphics and Multimedia Techniques	3			3	20	80	100	
6CS5A	Embedded System Design	3	-		3	20	80	100	
6CS6.1A	Advance Topics in Operating Systems	3	-		3	20	80	100	CS, IT
6CS6.2A	Artificial Intelligence								
6CS6.3A	Human Computer Interface								CS, IT
6CS7A	Java Programming Lab			3		45	30	75	CS, IT
6CS8A	Computer Graphics & Multimedia Lab			2		30	20	50	
6CS9A	Design and Analysis of Algorithms Lab.			3		60	40	100	CS, IT
6CS10A	Embedded System Design Lab.			2		45	30	75	
6CS11A	Humanities and Social Sciences			2		30	20	50	CS, IT
6CSDC	Discipline & Extra Curricular Activities							50	CS, IT
		18	0	12				1000	

VII	Computer Science and Engineering	Teaching Hrs			Exm Hrs	Max. Marks			Comments
Course Code	Subject Name (Revised)	L	T	P		IA	TE	TOTAL	Common to
7CS1A	Cloud Computing	3	-		3	20	80	100	
7CS2A	Information System Security	3			3	20	80	100	CS, IT
7CS3A	Data Mining & Ware Housing	3			3	20	80	100	CS, IT
7CS4A	Computer Aided Design for VLSI	3	-		3	20	80	100	
7CS5A	Compiler Construction	3			3	20	80	100	
7CS6.1A	Advance DataBase Management Systems	3	-		3	20	80	100	
7CS6.2A	Robotics								
7CS6.3A	Data Compression Techniques								
7CS7A	Web Development Lab			2		30	20	50	
7CS8A	VLSI Physical Design Lab			3		45	30	75	
7CS9A	Compiler Design Lab			3		45	30	75	
7CSPR	Project-I			2		50		50	CS, IT
7CSTR	Practical Training*			2			100	100	CS, IT
7CSDC	Discipline & Extra Curricular Activities							50	CS, IT
		18	0	12				1000	

VIII	Computer Science and Engineering	Teaching Hrs			Exm Hrs	Max. Marks			Comments
Course Code	Subject Name (Revised)	L	T	P		IA	TE	TOTAL	Common to
8CS1A	Mobile Computing	3			3	20	80	100	CS, IT
8CS2A	Digital Image Processing	3	-		3	20	80	100	CS, IT
8CS3A	Distributed Systems	3			3	20	80	100	
8CS4.1A	Hardware Testing & Fault Tolerance	3	-		3	20	80	100	CS, IT
8CS4.2A	Real Time Systems								CS, IT
8CS4.3A	Information Retrieval								CS, IT
8CS5A	Unix Network Programming & Simulation Lab			3		60	40	100	
8CS6A	FPGA Lab.			3		60	40	100	
8CS7A	Digital Image Processing lab			2		30	20	50	CS, IT
8CSPR	Project-II			2		120	80	200	CS, IT
8CSSM	Seminar			2		60	40	100	CS, IT
8CSDC	Discipline & Extra Curricular Activities							50	CS, IT
		12	0	12				1000	

Syllabus of
UNDERGRADUATE DEGREE COURSE

Computer Science and Engineering



Rajasthan Technical University, Kota

Effective from session: 2018 – 2019

3CS2-01: Advanced Engineering Mathematics

Credit-3 **Max. Marks : 150 (IA:30,ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	**Random Variables:** Discrete and Continuous random variables, Joint distribution, Probability distribution function, conditional distribution.Mathematical Expectations: Moments, Moment Generating Functions, variance and correlation coefficients, Chebyshev's Inequality, Skewness and Kurtosis.	7
2	**Binomial distribution**, Normal Distribution, Poisson Distribution and their relations, Uniform Distribution, Exponential Distribution. Correlation: Karl Pearson's coefficient, Rank correlation. Curve fitting. Line of Regression.	5
3	**Historical development**, Engineering Applications of Optimization, Formulation of Design Problems as a Mathematical Programming Problems, Classification of Optimization Problems	8
4	**Classical Optimization using Differential Calculus:** Single Variable and Multivariable Optimization with & without Constraints, Langrangian theory, Kuhn Tucker conditions	6
5	**Linear Programming:** Simplex method, Two Phase Method and Duality in Linear Programming. Application of Linear Programming: Transportation and Assignment Problems.	14
	TOTAL	40

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Syllabus

II Year-III Semester: B.Tech. Computer Science and Engineering

3CS1-02/4CS1-02: Technical Communication

Credit-2 **Max. Marks : 100 (IA:20,ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**Introduction to Technical Communication-** Definition of technical communication, Aspects of technical communication, forms of technical communication, importance of technical communication, technical communication skills (Listening, speaking, writing, reading writing), linguistic ability, style in technical communication.	4
2	**Comprehension of Technical Materials/Texts and Information Design & development-** Reading of technical texts, Readingand comprehending instructions and technical manuals, Interpreting and summarizing technical texts, Note-making. Introduction of different kinds of technical documents, Information collection, factors affecting information and document design, Strategies for organization, Information design and writing for print and online media.	6
3	**Technical Writing, Grammar and Editing**- Technical writing process, forms of technical discourse, Writing, drafts and revising, Basics of grammar, common error in writing and speaking, Study of advanced grammar, Editing strategies to achieve appropriate technical style, Introduction to advanced technical communication. Planning, drafting and writing Official Notes, Letters, E-mail, Resume, Job Application, Minutes of Meetings.	8
4	**Advanced Technical Writing**- Technical Reports, types of technical reports, Characteristics and formats and structure of technical reports. Technical Project Proposals, types of technical proposals, Characteristics and formats and structure of technical proposals. Technical Articles, types of technical articles, Writing strategies, structure and formats of technical articles.	8
	TOTAL	**26**

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Syllabus

II Year-III Semester: B.Tech. Computer Science and Engineering

3CS1-03/ 4CS1-03: Managerial Economics and Financial Accounting

Credit-2 **Max. Marks : 100 (IA:20,ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**Basic economic concepts-** Meaning, nature and scope of economics, deductive vs inductive methods, static and dynamics, Economic problems: scarcity and choice, circular flow of economic activity, national income-concepts and measurement.	4
2	**Demand and Supply analysis-** Demand-types of demand, determinants of demand, demand function, elasticity of demand, demand forecasting –purpose, determinants and methods, Supply-determinants of supply, supply function, elasticity of supply.	5
3	**Production and Cost analysis-** Theory of production- production function, law of variable proportions, laws of returns to scale, production optimization, least cost combination of inputs, isoquants. Cost concepts-explicit and implicit cost, fixed and variable cost, opportunity cost, sunk costs, cost function, cost curves, cost and output decisions, cost estimation.	5
4	**Market structure and pricing theory-** Perfect competition, Monopoly, Monopolistic competition, Oligopoly.	4
5	**Financial statement analysis-** Balance sheet and related concepts, profit and loss statement and related concepts, financial ratio analysis, cash-flow analysis, funds-flow analysis, comparative financial statement, analysis and interpretation of financial statements, capital budgeting techniques.	8
	TOTAL	**26**

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

3CS3-04: Digital Electronics

Credit-3 **Max. Marks : 150 (IA:30,ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	**Fundamental concepts:** Number systems and codes, Basic logic Gates and Boolean algebra: Sign & magnitude representation, Fixed point representation, complement notation, various codes & arithmetic in different codes & their inter conversion. Features of logic algebra, postulates of Boolean algebra.Theorems of Boolean algebra.	8
2	**Minimization Techniques and Logic Gates**: Principle of Duality - Boolean expression -Minimization of Boolean expressions — Minterm – Maxterm - Sum of Products (SOP) – Product of Sums (POS) – Karnaugh map Minimization – Don't care conditions – Quine - McCluskey method of minimization.	8
3	**Digital Logic Gate Characteristics:** TTL logic gate characteristics. Theory & operation of TTL NAND gate circuitry. Open collector TTL. Three state output logic. TTL subfamilies.MOS& CMOS logic families. Realization of logic gates in RTL, DTL, ECL, C-MOS & MOSFET.	8
4	**Combinational Circuits:** Combinational logic circuit design, adder, subtractor, BCD adder encoder, decoder, BCD to 7-segment decoder, multiplexer demultiplexer.	8
5	**Sequential Circuits:** Latches, Flip-flops - SR, JK, D, T, and Master-Slave Characteristic table and equation,counters and their design, Synchronous counters – Synchronous Up/Down counters – Programmable counters – State table and state transition diagram ,sequential circuits design methodology. Registers –shift registers.	8
	TOTAL	**40**

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

3CS4-05: Data Structures and Algorithms

Credit-3 **Max. Marks: 150 (IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	Stacks: Basic Stack Operations, Representation of a Stack using Static Array and Dynamic Array, Multiple stack implementation using single array, Stack Applications: Reversing list, Factorial Calculation, Infix to postfix Transformation, Evaluating Arithmetic Expressions and Towers of Hanoi.	8
2	Queues: Basic Queue Operations, Representation of a Queue using array, Implementation of Queue Operations using Stack, Applications of Queues- Round Robin Algorithm. Circular Queues, DeQueue Priority Queues. Linked Lists:Introduction, single linked list, representation of a linked list in memory, Different Operations on a Single linked list, Reversing a single linked list, Advantages and disadvantages of single linked list, circular linked list, double linked list and Header linked list.	10
3	Searching Techniques: Sequential and binary search.Sorting Techniques: Basic concepts, Sorting by: bubble sort, Insertion sort, selection sort, quick sort, heap sort, merge sort, radix sort and counting sorting algorithms.	7
4	Trees: Definition of tree, Properties of tree, Binary Tree, Representation of Binary trees using arrays and linked lists, Operations on a Binary Tree, Binary Tree Traversals (recursive), Binary search tree, B-tree , B+ tree, AVL tree, Threaded binary tree.	7
5	Graphs: Basic concepts, Different representations of Graphs, Graph Traversals (BFS & DFS), Minimum Spanning Tree(Prims &Kruskal), Dijkstra's shortest path algorithms.Hashing: Hash function, Address calculation techniques, Common hashing functions, Collision resolution: Linear and Quadratic probing, Double hashing.	8
	TOTAL	**40**

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

3CS4-06: Object Oriented Programming

Credit-3 **Max. Marks : 150 (IA:30,ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	Introduction to different programming paradigm, characteristics of OOP, Class, Object, data member, member function, structures in C++, different access specifiers, defining member function inside and outside class, array of objects.	8
2	Concept of reference, dynamic memory allocation using new and delete operators, inline functions, function overloading, function with default arguments, constructors and destructors, friend function and classes, using this pointer.	8
3	Inheritance, types of inheritance, multiple inheritance, virtual base class, function overriding, abstract class and pure virtual function	9
4	Constant data member and member function, static data member and member function, polymorphism, operator overloading, dynamic binding and virtual function	9
5	Exception handling, Template, Stream class, File handling.	6
	TOTAL	40

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

3CS4-07: Software Engineering

Credit-3
3L+0T+0P

Max. Marks : 150 (IA:30,ETE:120)
End Term Exam: 3 Hours

SN	CONTENTS	Hours
1	Introduction, software life-cycle models, software requirements specification, formal requirements specification, verification and validation.	8
2	Software Project Management: Objectives, Resources and their estimation, LOC and FP estimation, effort estimation, COCOMO estimation model, risk analysis, software project scheduling.	8
3	Requirement Analysis: Requirement analysis tasks, Analysis principles. Software prototyping and specification data dictionary, Finite State Machine (FSM) models. Structured Analysis: Data and control flow diagrams, control and process specification behavioral modeling	8
4	Software Design: Design fundamentals, Effective modular design: Data architectural and procedural design, design documentation.	8
5	Object Oriented Analysis: Object oriented Analysis Modeling, Data modeling. Object Oriented Design: OOD concepts, Class and object relationships, object modularization, Introduction to Unified Modeling Language	8
	TOTAL	**40**

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

3CS4-21: Data Structures and Algorithms Lab

Credit-1.5 **Max. Marks :75 (IA:45,ETE:30)**

0L+0T+3P

SN	CONTENTS
1	Write a simple C program on a 32 bit compiler to understand the concept of array storage, size of a word. The program shall be written illustrating the concept of row major and column major storage. Find the address of element and verify it with the theoretical value. Program may be written for arrays up to 4-dimensions.
2	Simulate a stack, queue, circular queue and dequeue using a one dimensional array as storage element. The program should implement the basic addition, deletion and traversal operations.
3	Represent a 2-variable polynomial using array. Use this representation to implement addition of polynomials
4	Represent a sparse matrix using array. Implement addition and transposition operations using the representation.
5	Implement singly, doubly and circularly connected linked lists illustrating operations like addition at different locations, deletion from specified locations and traversal.
6	Repeat exercises 2, 3 & 4 with linked structure.
7	Implementation of binary tree with operations like addition, deletion, traversal.
8	Depth first and breadth first traversal of graphs represented using adjacency matrix and list.
9	Implementation of binary search in arrays and on linked Binary Search Tree.
10	Implementation of different sorting algorithm like insertion, quick, heap, bubble and many more sorting algorithms.

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3CS4-22 : Object Oriented Programming Lab

Credit-1.5 **Max. Marks :75 (IA:45,ETE:30)**

0L+0T+3P

SN	CONTENTS
1	Understand the basics of C++ library, variables, data input-output.
2	C++ program using with the concept of structures.
3	Implement class and object concepts and function overloading.
4	Write programs to understand dynamic memory allocation and array of objects.
5	Program to understand different types of constructors and destructor.
6	Implement friend function to access private data of a class and usage of this pointer.
7	Write programs to understand the usage of constant data member and member function, static data member and member function in a class.
8	Implement different types of inheritance, function overriding and virtual function
9	Implement Operator overloading concepts.
10	Write programs to understand function template and class template.
11	Write programs to understand exception handling techniques.
12	Write programs to understand file handling techniques.

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3CS4-23: Software Engineering Lab

Credit-1.5 **Max. Marks :75 (IA:45,ETE:30)**

0L+0T+3P

SN	CONTENTS
1	Development of requirements specification, function oriented design using SA/SD, object-oriented design using UML, test case design, implementation using Java and testing. Use of appropriate CASE tools and other tools such as configuration management tools, program analysis tools in the software life cycle.
2	Develop Software Requirements Specification (SRS) for a given problem in IEEE template.
3	Develop DFD model (level-0, level-1 DFD and Data dictionary) of the project.
4	Develop structured design for the DFD model developed.
5	Developed all Structure UML diagram of the given project.
6	Develop Behavior UML diagram of the given project.
7	Manage file, using ProjectLibre project management software tool.

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3CS4-24: Digital Electronics Lab

Credit-1.5 **Max. Marks :75 (IA:45,ETE:30)**

0L+0T+3P

SN	CONTENTS
1	To verify the truth tables of basic logic gates: AND, OR, NOR, NAND, NOR. Also to verify truth table of Ex-OR, Ex-NOR (For 2, 3, & 4 inputs using gateswith 2, 3, & 4 inputs).
2	To verify the truth table of OR, AND, NOR, Ex-OR, Ex-NOR realized usingNAND& NOR gates.
3	To realize an SOP and POS expression.
4	To realize Half adder/ Subtractor& Full Adder/ Subtractor using NAND & NOR gatesand to verify their truth tables.
5	To realize a 4-bit ripple adder/ Subtractor using basic Half adder/ Subtractor& basic Full Adder/ Subtractor.
6	To verify the truth table of 4-to-1 multiplexer and 1-to-4 demultiplexer. Realize the multiplexer using basic gates only. Also to construct and 8-to-1 multiplexer and 1-to-8 demultiplexer using blocks of 4-to-1 multiplexer and 1-to-4 demultiplexer.
7	Design & Realize a combinational circuit that will accept a 2421 BCD code and drive a TIL -312 seven-segment display.
8	Using basic logic gates, realize the R-S, J-K and D-flip flops with and without clock signal and verify their truth table.
9	Construct a divide by 2,4& 8 asynchronous counter. Construct a 4-bit binary counter and ring counter for a particular output pattern using D flip flop.
10	Perform input/output operations on parallel in/Parallel out and Serial in/Serial out registers using clock. Also exercise loading only one of multiple values into the register using multiplexer. Note: As far as possible, the experiments shall be performed on bread board. However, experiment Nos. 1-4 are to be performed on bread board only.

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Syllabus of
UNDERGRADUATE DEGREE COURSE

Computer Science and Engineering



Rajasthan Technical University, Kota

Effective from session: 2018 – 2019

4CS2-01: Discrete Mathematics Structure

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Set Theory:** Definition of sets, countable and uncountable sets, Set operations, Partition of set, Cardinality (Inclusion-Exclusion & Addition Principles) Venn Diagrams, proofs of some general identities on sets. **Relation:** Definition, types of relation, composition of relations, Pictorial representation of relation, Equivalence relation, Partial ordering relation, Job-Scheduling problem. **Function:** Definition, type of functions, one to one, into and onto function, inverse function, composition of functions, recursively defined functions, pigeonhole principle. Theorem proving Techniques: Mathematical induction, Proof by contradiction. Composition of Functions. The Pigeonhole and Generalized Pigeonhole Principles.	7
3	**Propositional Logic:** Proposition, First order logic, Basic logical operation, truth tables, tautologies, Contradictions, Algebra of Proposition, logical implications, logical equivalence, predicates, Normal Forms, Universal and existential quantifiers. 2 way predicate logic. Introduction to finite state machine Finite state machines as models of physical system equivalence machines, Finite state machines as language recognizers.	8
4	**Posets, Hasse Diagram and Lattices:** Introduction, ordered set, Hasse diagram of partially, ordered set, isomorphic ordered set, well ordered set, properties of Lattices, bounded and complemented lattices. **Combinatorics:** Introduction, Permutation and combination, Binomial Theorem, Multimodal Coefficients Recurrence Relation and Generating Function: Introduction to Recurrence Relation and Recursive algorithms, linear recurrence relations with constant coefficients, Homogeneous solutions, Particular solutions, Total solutions, Generating functions, Solution by method of generating functions.	8
5	**Algebraic Structures:** Definition, Properties, types: Semi Groups, Monoid, Groups, Abelian group, properties of groups, Subgroup, cyclic groups, Cosets, factor group, Permutation groups, Normal subgroup, Homomorphism and isomorphism of Groups, example and standard results, Rings and Fields: definition and standard results.	8
6	**Graph Theory:** Introduction and basic terminology of graphs, Planer graphs, Multigraphs and weighted graphs, Isomorphic graphs, Paths, Cycles and connectivity, Shortest path in weighted graph, Introduction to Eulerian paths and circuits, Hamiltonian paths and circuits, Graph coloring, chromatic number, Isomorphism and Homomorphism of graphs, matching, vertex/edge covering.	8
	Total	40

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RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Syllabus

II Year-IV Semester: B.Tech. Computer Science and Engineering

4CS1-03/3CS1-03: Managerial Economics and Financial Accounting

Credit-2 **Max. Marks : 100 (IA:20,ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Basic economic concepts-** Meaning, nature and scope of economics, deductive vs inductive methods, static and dynamics, Economic problems: scarcity and choice, circular flow of economic activity, national income-concepts and measurement.	3
3	**Demand and Supply analysis-** Demand-types of demand, determinants of demand, demand function, elasticity of demand, demand forecasting –purpose, determinants and methods, Supply-determinants of supply, supply function, elasticity of supply.	5
4	**Production and Cost analysis-** Theory of production- production function, law of variable proportions, laws of returns to scale, production optimization, least cost combination of inputs, isoquants. Cost concepts-explicit and implicit cost, fixed and variable cost, opportunity cost, sunk costs, cost function, cost curves, cost and output decisions, cost estimation.	5
5	**Market structure and pricing theory-** Perfect competition, Monopoly, Monopolistic competition, Oligopoly.	4
6	**Financial statement analysis-** Balance sheet and related concepts, profit and loss statement and related concepts, financial ratio analysis, cash-flow analysis, funds-flow analysis, comparative financial statement, analysis and interpretation of financial statements, capital budgeting techniques.	8
	TOTAL	26

Office of Dean Academic Affairs
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4CS1-02/3CS1-02: Technical Communication

Credit-2 **Max. Marks : 100 (IA:20,ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Introduction to Technical Communication-** Definition of technical communication, Aspects of technical communication, forms of technical communication, importance of technical communication, technical communication skills (Listening, speaking, writing, reading writing), linguistic ability, style in technical communication.	3
3	**Comprehension of Technical Materials/Texts and Information Design & development-** Reading of technical texts, Reading and comprehending instructions and technical manuals, Interpreting and summarizing technical texts, Note-making. Introduction of different kinds of technical documents, Information collection, factors affecting information and document design, Strategies for organization, Information design and writing for print and online media.	6
4	**Technical Writing, Grammar and Editing**- Technical writing process, forms of technical discourse, Writing, drafts and revising, Basics of grammar, common error in writing and speaking, Study of advanced grammar, Editing strategies to achieve appropriate technical style, Introduction to advanced technical communication. Planning, drafting and writing Official Notes, Letters, E-mail, Resume, Job Application, Minutes of Meetings.	8
5	**Advanced Technical Writing**- Technical Reports, types of technical reports, Characteristics and formats and structure of technical reports. Technical Project Proposals, types of technical proposals, Characteristics and formats and structure of technical proposals. Technical Articles, types of technical articles, Writing strategies, structure and formats of technical articles.	8
	TOTAL	26

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4CS3-04: Microprocessor & Interfaces

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	Introduction to Microprocessors, microcontroller; 8085 Microprocessor Architecture, pin description, Bus concept and organization; concept of multiplexing and de-multiplexing of buses; concept of static and dynamic RAM, type of ROM, memory map.	7
3	Software architecture registers and signals, Classification of instruction, Instruction set, addressing modes, Assembly Language Programming and Debugging, Programming Technique, instruction Format and timing.	8
4	Advance Assembly Language Programming, Counter and time delay; types of Interrupt and their uses, RST instructions and their uses, 8259 programmable interrupt controller; Macros, subroutine; Stack-implementation and uses with examples; Memory interfacing.	8
5	8085 Microprocessor interfacing:, 8255 Programmable Peripheral Interface, 8254 programmable interval timer, interfacing of Input/output device, 8279 Key board/Display interface.	8
6	Microprocessor Application: Interfacing scanned multiplexed display and liquid crystal display, Interfacing and Matrix Keyboard, MPU Design; USART 8251, RS232C and RS422A, Parallel interface-Centronics and IEEE 488.	8
	Total	40

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4CS4-05: Database Management System

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Introduction to database systems:** Overview and History of DBMS. File System v/s DBMS.Advantage of DBMS Describing and Storing Data in a DBMS.Queries in DBMS.Structure of a DBMS. **Entity Relationship model:** Overview of Data Design Entities, Attributes and Entity Sets, Relationship and Relationship Sets. Features of the ER Model- Key Constraints, Participation Constraints, Weak Entities, Class Hierarchies, Aggregation, Conceptual Data Base, and Design with ER Model- Entity v/s Attribute, Entity vs Relationship Binary vs Ternary Relationship and Aggregation v/s ternary Relationship Conceptual Design for a Large Enterprise.	7
3	**Relationship Algebra and Calculus:** Relationship Algebra Selection and Projection, Set Operations, Renaming, Joints, Division, Relation Calculus, Expressive Power of Algebra and Calculus. **SQL queries programming and Triggers:** The Forms of a Basic SQL Query, Union, and Intersection and Except, Nested Queries, Correlated Nested Queries, Set-Comparison Operations, Aggregate Operators, Null Values and Embedded SQL, Dynamic SQL, ODBC and JDBC, Triggers and Active Databases.	8
4	**Schema refinement and Normal forms:** Introductions to Schema Refinement, Functional Dependencies, Boyce-Codd Normal Forms, Third Normal Form, Normalization-Decomposition into BCNF Decomposition into 3-NF.	8
5	**Transaction Processing:** Introduction-Transaction State, Transaction properties, Concurrent Executions. Need of Serializability, Conflict vs. View Serializability, Testing for Serializability, Recoverable Schedules, Cascadeless Schedules.	8
6	**Concurrency Control: Implementation of Concurrency:** Lock-based protocols, Timestamp-based protocols, Validation-based protocols, Deadlock handling, **Database Failure and Recovery:** Database Failures, Recovery Schemes: Shadow Paging and Log-based Recovery, Recovery with Concurrent transactions.	8
	Total	40

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4CS4-06: Theory Of Computation

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	Finite Automata & Regular Expression: Basic machine, Finite state machine, Transition graph, Transition matrix, Deterministic and non-deterministic finite automation, Equivalence of DFA and NDFA, Decision properties, minimization of finite automata, Mealy & Moore machines. Alphabet, words, Operations, Regular sets, relationship and conversion between Finite automata and regular expression and vice versa, designing regular expressions, closure properties of regular sets, Pumping lemma and regular sets, Myhill- Nerode theorem , Application of pumping lemma, Power of the languages.	7
3	Context Free Grammars (CFG), Derivations and Languages, Relationship between derivation and derivation trees, leftmost and rightmost derivation, sentential forms, parsing and ambiguity, simplification of CFG, normal forms, Greibach and Chomsky Normal form , Problems related to CNF and GNF including membership problem.	8
4	Nondeterministic PDA, Definitions, PDA and CFL, CFG for PDA, Deterministic PDA, and Deterministic PDA and Deterministic CFL , The pumping lemma for CFL's, Closure Properties and Decision properties for CFL, Deciding properties of CFL.	8
5	Turing Machines: Introduction, Definition of Turing Machine, TM as language Acceptors and Transducers, Computable Languages and functions, Universal TM & Other modification, multiple tracks Turing Machine. Hierarchy of Formal languages: Recursive & recursively enumerable languages, Properties of RL and REL, Introduction of Context sensitive grammars and languages, The Chomsky Hierarchy.	8
6	Tractable and Untractable Problems: P, NP, NP complete and NP hard problems, Un-decidability, examples of these problems like vertex cover problem, Hamiltonian path problem, traveling sales man problem.	8
	Total	40

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4CS4-07: Data Communication and Computer Networks

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	Introductory Concepts: Network hardware, Network software, topologies, Protocols and standards, OSI model, TCP model, TCP/IP model, Physical Layer: Digital and Analog Signals, Periodic Analog Signals, Signal Transmission, Limitations of Data Rate, Digital Data Transmission, Performance Measures, Line Coding, Digital Modulation, Media and Digital Transmission System	7
3	Data Link Layer: Error Detection and Correction, Types of Errors, Two dimensional parity check, Detection verses correction, Block Coding, Linear Block Coding, Cyclic Codes, Checksum, Standardized Polynomial Code, Error Correction Methods, Forward Error Correction, Protocols: Stop and wait, Go-back-N ARQ, Selective Repeat ARQ, Sliding window, Piggy backing, Pure ALOHA, Slotted ALOHA, CSMA/CD, CSMA/CA	9
4	Network Layer: Design issues, Routing algorithms: IPV4, IPV6, Address mapping: ARQ, RARQ, Congestion control, Unicast, Multicast, Broadcast routing protocols, Quality of Service, Internetworking	8
5	Transport Layer: Transport service, Elements of transport protocols, User Datagram Protocol, Transmission Control Protocol, Quality of service, Leaky Bucket and Token Bucket algorithm	8
6	Application Layer**:** WWW, DNS, Multimedia, Electronic mail, FTP, HTTP, SMTP, Introduction to network security	7
	Total	40

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4CS4-21: Microprocessor & Interfaces Lab

Credit: 1 **Max. Marks: 50(IA:30, ETE:20)**

0L+0T+2P

List of Experiments:

1. Add the contents of memory locations XX00 &XX01 & place the result in memory location XX02.
2. Add the 16 bit numbers stored in memory location & store the result in another memory location.
3. Transfer a block of data from memory location XX00 to another memory location XX00 in forward & reverse order.
4. Write a program to swap two blocks of data stored in memory.
5. Write a program to find the square of a number.
6. Write a main program and a conversion subroutine to convert Binary to its equivalent BCD.
7. Write a program to find largest & smallest number from a given array.
8. Write a program to Sort an array in ascending & descending order.
9. Write a program to multiply two 8 bit numbers whose result is 16 bit.
10. Write a program of division of two 8 bit numbers.
11. Generate square wave from SOD pin of 8085 & observe on CRO.
12. Write a program to perform traffic light control operation.
13. Write a program to control the speed of a motor.

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4CS4-22: Database Management System Lab

Credit: 1.5 **Max. Marks: 75(IA:45, ETE:30)**

0L+0T+3P

List of Experiments:

1. Design a Database and create required tables. For e.g. Bank, College Database
2. Apply the constraints like Primary Key, Foreign key, NOT NULL to the tables.
3. Write a SQL statement for implementing ALTER,UPDATE and DELETE.
4. Write the queries to implement the joins.
5. Write the query for implementing the following functions: MAX (), MIN (), AVG () and COUNT ().
6. Write the query to implement the concept of Integrity constrains.
7. Write the query to create the views.
8. Perform the queries for triggers.
9. Perform the following operation for demonstrating the insertion , updation and deletion
10. Using the referential integrity constraints.
11. Write the query for creating the users and their role.

Data Base Designing Project:

For better understanding students (group of 3-4 students) should design data base for any data base project, understand the requirement and design methodology of project by its own.

Some example of data base design project like:

College management system, Inventory management system and Hospital management system.

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4CS4-23: Network Programming Lab

Credit: 1.5 **Max. Marks: 75(IA:45, ETE:30)**

0L+0T+3P

List of Experiments:

1. Study of Different Type of LAN& Network Equipments.
2. Study and Verification of standard Network topologies i.e. Star, Bus, Ring etc.
3. LAN installations and Configurations.
4. Write a program to implement various types of error correcting techniques.
5. Write a program to implement various types of framing methods.
6. Write two programs in C: hello_client and hello_server
    a. The server listens for, and accepts, a single TCP connection; it reads all the data it can from that connection, and prints it to the screen; then it closes the connection
    b. The client connects to the server, sends the string “Hello, world!”, then closes the connection
7. Write an Echo_Client and Echo_server using TCP to estimate the round trip time from client to the server. The server should be such that it can accept multiple connections at any given time.
8. Repeat Exercises 6 & 7 for UDP.
9. Repeat Exercise 7 with multiplexed I/O operations.
10. Simulate Bellman-Ford Routing algorithm in NS2.

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4CS4-24: Linux Shell Programming Lab

Credit: 1 **Max. Marks: 50(IA:30, ETE:20)**

0L+0T+2P

List of Experiments:

1. Use of Basic Unix Shell Commands: ls, mkdir, rmdir, cd, cat, banner, touch, file, wc, sort, cut, grep, dd, dfspace, du, ulimit.
2. Commands related to inode, I/O redirection and piping, process control commands, mails.
3. Shell Programming: Shell script based on control structure- **If-then-fi, if-then-else-if, nested if-else, to find:**
   - 3.1 Greatest among three numbers.
   - 3.2 To find a year is leap year or not.
   - 3.3 To input angles of a triangle and find out whether it is valid triangle or not.
   - 3.4 To check whether a character is alphabet, digit or special character.
   - 3.5 To calculate profit or loss.
4. Shell Programming - Looping- while, until, for loops
   - 4.1 Write a shell script to print all even and odd number from 1 to 10.
   - 4.2 Write a shell script to print table of a given number
   - 4.3 Write a shell script to calculate factorial of a given number.
   - 4.4 Write a shell script to print sum of all even numbers from 1 to 10.
   - 4.5 Write a shell script to print sum of digit of any number.
5. Shell Programming - case structure, use of break
   - 5.1 Write a shell script to make a basic calculator which performs addition, subtraction, Multiplication, division
   - 5.2 Write a shell script to print days of a week.
   - 5.3 Write a shell script to print starting 4 months having 31 days.
6. Shell Programming - Functions
   - 6.1 Write a shell script to find a number is Armstrong or not.
   - 6.2 Write a shell script to find a number is palindrome or not.
   - 6.3 Write a shell script to print Fibonacci series.
   - 6.4 Write a shell script to find prime number.
   - 6.5 Write a shell script to convert binary to decimal and decimal to binary
7. Write a shell script to print different shapes- Diamond, triangle, square, rectangle, hollow square etc.
8. Shell Programming – Arrays
   - 8.1 Write a C program to read and print elements of array.
   - 8.2 Write a C program to find sum of all array elements.
   - 8.3 Write a C program to find reverse of an array.
   - 8.4 Write a C program to search an element in an array.
   - 8.5 Write a C program to sort array elements in ascending or descending order.

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4CS4-25: Java Lab

Credit: 1 **Max. Marks: 50(IA:30, ETE:20)**

0L+0T+2P

List of Experiment:

1. Develop an in depth understanding of programming in Java: data types, variables, operators, operator precedence, Decision and control statements, arrays, switch statement, Iteration Statements, Jump Statements, Using break, Using continue, return.
2. Write Object Oriented programs in Java: Objects, Classes constructors, returning and passing objects as parameter, Inheritance, Access Control, Using super, final with inheritance Overloading and overriding methods, Abstract classes, Extended classes.
3. Develop understanding to developing packages & Interfaces in Java: Package, concept of CLASSPATH, access modifiers, importing package, Defining and implementing interfaces.
4. Develop understanding to developing Strings and exception handling: String constructors, special string operations, character extraction, searching and comparing strings, string Buffer class. Exception handling fundamentals, Exception types, uncaught exceptions, try, catch and multiple catch statements. Usage of throw, throws and finally.
5. Develop applications involving file handling: I/O streams, File I/O.
6. Develop applications involving concurrency: Processes and Threads, Thread Objects, Defining and Starting a Thread, Pausing Execution with Sleep, Interrupts, Joins, and Synchronization.

Indicative List of exercises:

7. Programs to demonstrate basic concepts e.g. operators, classes, constructors, control & iteration statements, recursion etc. such as complex arithmetic, matrix arithmetic, tower of Hanoi problem etc.
8. Development of programs/projects to demonstrate concepts like inheritance, exception handling, packages, interfaces etc. such as application for electricity department, library management, ticket reservation system, payroll system etc.
9. Development of a project to demonstrate various file handling concepts.
10. Develop applications involving Applet: Applet Fundamentals, using paint method and drawing polygons. It is expected that each laboratory assignments to given to the students with an aim to In order to achieve the above objectives.

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5CS1A COMPUTER ARCHITECTURE (Common to CS & IT)

Class: V Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. Schedule per Week Lectures: 3	**Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Introduction to Computer Architecture and Organization. Von Neuman Architecture, Flynn Classification. Register Transfer and Micro operations: Register transfer language, Arithmetic Micro-operations, Logic Micro-operations, Shift Micro-operations, Bus and memory transfers. Computer Organization and Design: Instruction cycle, computer registers, common bus system, computer instructions, addressing modes, design of a basic computer
II	Central Processing Unit: General register organization, stack organization, Instruction formats, Data transfer and manipulation, program control. RISC, CISC characteristics. Pipeline and Vector processing: Pipeline structure, speedup, efficiency, throughput and bottlenecks. Arithmetic pipeline and Instruction pipeline.
III	Computer Arithmetic: Adder, Ripple carry Adder, carry look Ahead Adder, Multiplication: Add and Shift, Array multiplier and Booth Multiplier, Division: restoring and Non-restoring Techniques. Floating Point Arithmetic: Floating point representation, Add, Subtract, Multiplication, Division.
IV	Memory Organization: RAM, ROM, Memory Hierarchy, Organization, Associative memory, Cache memory, and Virtual memory: Paging and Segmentation.
V	Input-Output Organization: Input-Output Interface, Modes of Transfer, Priority Interrupt, DMA, IOP processor.

Text/References:

1. Computer Organization and Architecture - William Stallings (Pearson Education Asia)

2. Computer Organization and Architecture -John P. Hayes (McGraw -Hill)

3. Computer Organization -V. Carl. Hamacher (McGraw-Hill)

5CS2A Digital Logic Design

Class: V Sem. B.Tech.	Evaluation
Branch: Computer Engg. Schedule per Week Lectures: 3	**Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Hardware Description Languages and their use in digital logic design. VHDL: Modelling Concepts, Lexical Elements & Syntax Descriptions, Scalar Data types & Operations, Sequential Statements, Composite Data Types & Operations, Basic Modelling Constructs. Case Study: VHDL Simulation of Ripple Carry, & Look Ahead carry Adders.
II	VHDL: Subprograms, Packages & Use Clauses, Aliases, Resolved Signals, Components & Configurations, Generate Statements, Concurrent Statements. Use of VHDL in simulation and synthesis.
III	Clocked Sequential circuits. Design steps for synchronous sequential circuits. Design of a sequence detector. Moore and Mealy Machines. Design using JK flip-flops and D flip-flops. State reduction, State assignment, Algorithmic State Charts, converting ASM charts to hardware, one-hot state assignment. Considerations of clock skew, set-up time, hold-time and other flip-flop parameters, timing constraints. Programmable Logic Devices. Read-only memory. Boolean function implementation through ROM. PLD, PGA, PLA, PAL, FPGA.
IV	Event-driven Circuits. Design procedure for asynchronous circuits, stable and unstable states, races, race-free assignments. State reduction of incompletely specified machines. Compatibility and state reduction procedure. Hazards in combinational networks. Dynamic hazards, Function Hazards, and Essential Hazards. Eliminating hazards.
V	Field Programmable Gate Arrays: Introduction, Logic Elements & programmability, Interconnect structures & programmability, Extended Logic Elements, SRAM, Flash Memory & Antifuse Configuration, Case Studies of Altera Stratix & Xilinx Virtex-II pro. Technology Mapping for FPGAs: Logic Synthesis, Lookup Table Technology Mapping.

Text Book:

1. Brian Holdsworth and Clive Woods. Digital Logic Design. Newnes (Elsevier). [Available in Indian Edition].
2. Ashenden, The Designer's Guide to VHDL, Elsevier.
3. Stephen D. Brown, et.al., Field Programmable Gate Arrays, Kluwer Academic Publishers.
4. Scott Hauck, André DeHon , Reconfigurable computing: the theory and practice of FPGA based computation, Morgan Kauffman
5. Zvi Kohavi: Switching and Finite Automata Theory. TMH.
6. Parag K. Lala, Practical Digital Logic Design and Testing. PHI
7. Stephen H. Unger, The essence of logic circuits. Wiatrowski & House.

5CS3A TELECOMMUNICATION FUNDAMENTALS (Common to CS & IT)

Class: V Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. Schedule per Week Lectures: 3	**Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Data Transmission: Terminology, Frequency, spectrum, bandwidth, analog and digital transmission, Transmission impairments, channel capacity, Transmission Media. Wireless Transmission: Antenna and antenna gain. Network Reference Models (OSI/ISO and TCP/IP) Physical Layer: Line Encoding Schemes. Concept of bit period, effect of clock skew, Synchronous and Asynchronous communication. Data Link Layer: Functions of data link layer and design issues Flow Control: Flow control in loss less and lossy channels using stop-and-wait, sliding window protocols. Performance of protocols used for flow control.
II	Error Control Coding: Error Detection, Two Dimensional Parity Checks, and Internet Checksum. Polynomial Codes, Standardized polynomial codes, error detecting capability of a polynomial codes. Linear codes, performance of linear codes, error detection & correction using linear codes. Data Link Control: HDLC & PPP including frame structures. MAC sublayer: Channel Allocation Problem, Pure and slotted Aloha, CSMA, CSMA/CD, collision free multiple access. Throughput analysis of pure and slotted Aloha. Ethernet Performance.
III	Wireless LAN: Hidden node and Exposed node Problems, RTS/CTS based protocol, 802.11 Architecture, protocol stack, Physical layer, MAC Sublayer. Bluetooth Architecture and Protocol Stack Data Link Layer Switching: Bridges (Transparent, Learning and Spanning Tree), Virtual LANs
IV	Multiplexing: Frequency division, time division (Synchronous and statistical) multiplexing. ADSL, DS1 and DS3 carriers. Multiple Accesses: TDMA frame structure, TDMA Burst Structure, TDMA Frame efficiency, TDMA Superframe structure, Frame acquisition and synchronization, Slip rate in digital terrestrial networks. Switching: Qualitative description of Space division, time division and space-time-space division switching.
V	Spread Spectrum Techniques: Direct sequence(DSSS) & frequency hopping(FHSS); Performance consideration in DSSS & FHSS; Code division Multiple access (CDMA): frequency & channel specifications, forward & reverse CDMA channel, pseudo noise(PN) sequences, m-sequence, gold sequence, orthogonal code, gold sequences, Walsh codes, synchronization, power control, handoff, capacity of CDMA system, IMT-2000, WCDM

Text/References:

1. Stallings, Data and computer communication, 8th ed. Pearson
2. Tri.T.Ha, Digital Satellite Communications, 2/e, Tata McGraw Hill
3. Alberto Leon-Garcia, Indra Widjaja, COMMUNICATION NETWORKS, 2nd ed., TMH
4. Wireless Communications, 2/e, Rappaport, PHI

5. Analysis of Computer and Communication Networks, ISBN: 0387744363, Fayez Gebali, 2008, Springer-verlag, 1st Ed.

5CS4A DATABASE MANAGEMENT SYSTEMS (Common to CS & IT)

Class: V Sem. B.Tech	**Evaluation**
Branch: Computer Engg. Schedule per Week **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	INTRODUCTION TO DATABASE SYSTEMS: Overview and History of DBMS. File System v/s DBMS .Advantage of DBMS Describing and Storing Data in a DBMS. Queries in DBMS. Structure of a DBMS.
II	ENTITY RELATIONSHIP MODEL: Overview of Data Design Entities, Attributes and Entity Sets, Relationship and Relationship Sets. Features of the ER Model- Key Constraints, Participation Constraints, Weak Entities, Class Hierarchies, Aggregation, Conceptual Data Base, Design with ER Model-Entity v/s Attribute, Entity vs Relationship Binary vs Ternary Relationship and Aggregation v/s ternary Relationship Conceptual Design for a Large Enterprise.
III	RELATIONSHIP ALGEBRA AND CALCULUS**:** Relationship Algebra Selection and Projection, Set Operations, Renaming, Joints, Division, Relation Calculus, Expressive Power of Algebra and Calculus.
IV	SQL QUERIES PROGRAMMING AND TRIGGERS: The Forms of a Basic SQL Query, Union, Intersection and Except, Nested Queries ,Correlated Nested Queries, Set-Comparison Operations, Aggregate Operators, Null Values and Embedded SQL, Dynamic SQL, ODBC and JDBC, Triggers and Active Databases.
V	SCHEMA REFINEMENT AND NORMAL FORMS: Introductions to Schema Refinement, Functional Dependencies, Boyce-Codd Normal Forms, Third Normal Form, Normalization-Decomposition into BCNF Decomposition into 3-NF.

References:

1. H.f. Korth and Silberschatz: Database Systems Concepts, McGraw Hill
2. Almasri and S.B. Navathe: Fundamentals of Database Systems,
3. C.J. Date: Data Base Design, Addison Wesley
4. Hansen and Hansen : DBM and Design, PHI

5CS5A OPERATING SYSTEMS (Common to CS & IT)

Class: V Sem. B.Tech.	Evaluation
Branch: Computer Engg. Schedule per Week Lectures: 3	**Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Introduction and need of operating system, layered architecture/logical structure of operating system, Type of OS, operating system as resource manager and virtual machine, OS services, BIOS, System Calls/Monitor Calls, Firmware- BIOS, Boot Strap Loader. Process management- Process model, creation, termination, states & transitions, hierarchy, context switching, process implementation, process control block, Basic System calls- Linux & Windows. Threads- processes versus threads, threading, concepts, models, kernel & user level threads, thread usage, benefits, multithreading models.
II	Interprocess communication- Introduction to message passing, Race condition, critical section problem, mutual exclusion with busy waiting- disabling interrupts, lock variables, strict alteration, Peterson's solution, TSL instructions, busy waiting, sleep and wakeup calls, semaphore, monitors, classical IPC problems. Process scheduling- Basic concepts, classification, CPU and I/O bound, CPU scheduler- short, medium, long-term, dispatcher, scheduling:- preemptive and non-preemptive, Static and Dynamic Priority, Co-operative & Non-cooperative, Criteria/Goals/Performance Metrics, scheduling algorithms- FCFS, SJFS, shortest remaining time, Round robin, Priority scheduling, multilevel queue scheduling, multilevel feedback queue scheduling, Fair share scheduling.
III	Deadlock- System model, resource types, deadlock problem, deadlock characterization, methods for deadlock handling, deadlock prevention, deadlock avoidance, deadlock detection, recovery from deadlock. Memory management- concepts, functions, logical and physical address space, address binding, degree of multiprogramming, swapping, static & dynamic loading- creating a load module, loading, static & dynamic linking, shared libraries, memory allocation schemes- first fit, next fit, best fit, worst fit, quick fit. Free space management- bitmap, link list/free list, buddy's system, memory protection and sharing, relocation and address translation.
IV	Virtual Memory- concept, virtual address space, paging scheme, pure segmentation and segmentation with paging scheme hardware support and implementation details, memory fragmentation, demand paging, pre-paging, working set model, page fault frequency, thrashing, page replacement algorithms- optimal, NRU, FIFO, second chance, LRU, LRU-approximation clock, WS clock; Belady's anomaly, distance string; design issues for paging system- local versus global allocation policies, load control, page size, separate instruction and data spaces, shared pages, cleaning policy, TLB (translation look aside buffer) reach, inverted page table, I/O interlock, program structure, page fault handling, Basic idea of MM in Linux & windows.
V	File System- concepts, naming, attributes, operations, types, structure, file organization & access(Sequential, Direct ,Index Sequential) methods, memory mapped files, directory structures- one level, two level, hierarchical/tree, acyclic graph, general graph, file system mounting, file sharing, path name, directory operations, overview of file system in Linux & windows. Input/Output subsystems- concepts, functions/goals, input/output devices- block and character, spooling, disk structure & operation, disk attachment, disk storage capacity, disk scheduling algorithm- FCFS, SSTF, scan scheduling, C-scan schedule.

Text/Reference Books:

1. A. Silberschatz and Peter B Galvin: Operating System Principals, Wiley India Pvt. Ltd.
2. Achyut S Godbole: Operating Systems, Tata McGraw Hill
3. Tanenbaum: Modern Operating System, Prentice Hall.
4. DM Dhamdhere: Operating Systems – A Concepts Based Approach, Tata McGraw Hill
5. Charles Crowly: Operating System A Design – Oriented Approach, Tata McGraw Hill.

5CS6.1A ADVANCED DATA STRUCTURE (Common to CS & IT)

Class: V Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. Schedule per Week Lectures: 3	**Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	ADVANCED TREES: Definitions, Operations on Weight Balanced Trees (Huffman Trees), 2-3 Trees and Red- Black Trees. Dynamic Order Statistics, Interval Tree; Dictionaries.
II	MERGEABLE HEAPS: Mergeable Heap Operations, Binomial Trees, Implementing Binomial Heaps and its Operations, 2-3-4. Trees and 2-3-4 Heaps. Amortization analysis and Potential Function of Fibonacci Heap, Implementing Fibonacci Heap.
III	GRAPH THEORY DEFINITIONS: Definitions of Isomorphic Components. Circuits, Fundamental Circuits, Cut-sets. Cut- Vertices Planer and Dual graphs, Spanning Trees, Kuratovski's two Graphs. GRAPH THEORY ALGORITHMS: Algorithms for Connectedness, Finding all Spanning Trees in a Weighted Graph, Breadth First and Depth First Search, Topological Sort, Strongly Connected Components and Articulation Point. Single Min-Cut Max-Flow theorem of Network Flows. Ford-Fulkerson Max Flow Algorithms.
IV	SORTING NETWORK: Comparison network, zero-one principle, bitonic sorting and merging network sorter. Priority Queues and Concatenable Queues using 2-3 Trees. Operations on Disjoint sets and its union-find problem, Implementing Sets.
V	NUMBER THEORITIC ALGORITHM: Number theoretic notions, Division theorem, GCD, recursion, Modular arithmetic, Solving Modular Linear equation, Chinese Remainder Theorem, power of an element, Computation of Discrete Logarithms, primality Testing and Integer Factorization.

Text/References:

1. Cormen, Leiserson, Rivest: Introduction to Algorithms, Prentice Hall of India.

2. Horowitz and Sahani: Fundamental of Computer algorithms.

3. Aho A.V , J.D Ulman: Design and analysis of Algorithms, Addison Wesley

4. Brassard : Fundamental of Algorithmics, PHI.

5CS6.2A DIGITAL SIGNAL PROCESSING

Class: V Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. Schedule per Week Lectures: 3	**Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	INTRODUCTION : Discrete time signals and systems, properties of discrete time systems, Linear time invariant systems - discrete time. Properties of LTI systems and their block diagrams. Convolution, Discrete time systems described by difference equations.
II	Fourier Transform: Discrete time Fourier transform for periodic and aperiodic signals. Properties of DTFT. Z-transform: The region of convergence for the Z-transform. The Inverse Z-transform. Properties of Z transform.
III	SAMPLING: Mathematical theory of sampling. Sampling theorem. Ideal & Practical sampling. Interpolation technique for the reconstruction of a signal from its samples. Aliasing. Sampling in freq. domain. Sampling of discrete time signals.
IV	THE DISCRETE FOURIER TRANSFORMS (DFT): Properties of the DFT, Linear Convolution using DFT. Efficient computation of the DFT: Decimation–in-Time and Decimation-in frequency FFT Algorithms.
V	FILTER DESIGN TECHNIQUES: Structures for discrete-time systems- Block diagram and signal flow graph representation of LCCD (LCCD – Linear Constant Coefficient Difference) equations, Basic structures for IIR and FIR systems, Transposed forms. Introduction to filter Design: Butterworth & Chebyshev.IIR filter design by impulse invariance & Bilinear transformation. Design of FIR filters by Windowing: Rectangular, Hamming & Kaiser.

Text/References:

1. Oppenheim, Discrete-Time Signal Processing, 2/e, Pearson Education
2. Proakis, Digital Signal Processing, 4/e, Pearson Education
3. S.K.Mitra, Digital Signal Processing, 2/e, Tata McGraw Hill

5CS6.3A INFORMATION THEORY & CODING

Class: V Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. Schedule per Week Lectures: 3	**Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Introduction to information theory. Uncertainty, Information and Entropy, Information measures for continuous random variables, source coding theorem. Discrete Memory less channels, Mutual information, Conditional entropy.
II	Source coding schemes for data compaction: Prefix code, Huffman code, Shanon-Fane code & Hempel-Ziv coding channel capacity. Channel coding theorem. Shannon limit.
III	Linear Block Code: Introduction to error connecting codes, coding & decoding of linear block code, minimum distance consideration, conversion of non systematic form of matrices into systematic form.
IV	Cyclic Code: Code Algebra, Basic properties of Galois fields (GF) polynomial operations over Galois fields, generating cyclic code by generating polynomial, parity check polynomial. Encoder & decoder for cyclic codes.
V	Convolutional Code: Convolutional encoders of different rates. Code Tree, Trllis and state diagram. Maximum likelihood decoding of convolutional code: The viterbi Algorithm fee distance of a convolutional code.

Text/References

1. Digital Communication, Simon Haykin, Wiley.

5CS7A DATABASE LAB (Common to CS & IT)

Class: V Sem. B.Tech.	Evaluation
Branch: Computer Engg. Schedule per Week Practical Hrs.: 3	Examination Time = Four (4) Hours Maximum Marks = 100 [Sessional/Mid-term (60) & End-term (40)]

Objectives: At the end of the semester, the students should have clearly understood and implemented the following:

1. Stating a database design & application problem.
2. Preparing ER diagram
3. Finding the data fields to be used in the database.
4. Selecting fields for keys.
5. Normalizing the database including analysis of functional dependencies.
6. Installing and configuring the database server and the front end tools.
7. Designing database and writing applications for manipulation of data for a stand alone and shared data base including concepts like concurrency control, transaction roll back, logging, report generation etc.
8. Get acquainted with SQL.

In order to achieve the above objectives, it is expected that each students will chose one problem. The implementation shall being with the statement of the objectives to be achieved, preparing ER diagram, designing of database, normalization and finally manipulation of the database including generation of reports, views etc. The problem may first be implemented for a standalone system to be used by a single user.

All the above steps may then be followed for development of a database application to be used by multiple users in a client server environment with access control. The application shall NOT use web techniques.

One exercise may be assigned on creation of table, manipulation of data and report generation using SQL.

Suggested Tool:

For standalone environment, Visual FoxPro or any similar database having both the database and manipulation language may be used.

For multi-user application, MYSql is suggested. However, any other database may also be used. For front end, VB.Net, Java, VB Script or any other convenient but currently used by industry may be chosen.

Indicative List of exercise:

1. Student information system for your college.

2. Student grievance registration and redressal system.

3. A video library management system for a shop.
4. Inventory management system for a hardware/ sanitary item shop.
5. Inventory management system for your college.
6. Guarantee management system for the equipments in your college.

5CS8A SYSTEM DESIGNS in UML LAB

Class: V Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. Schedule per Week Practical Hrs : 3	**Examination Time = Four (4) Hours Maximum Marks = 75 [Sessional/Mid-term (45) & End-term (30)]**

Objectives:

1. The students shall be able to use following modules of UML for system description, implementation and finally for product development.

   - Capture a business process model.
   - The User Interaction or Use Case Model - describes the boundary and interaction between the system and users. Corresponds in some respects to a requirements model.
   - The Interaction or Communication Model - describes how objects in the system will interact with each other to get work done.
   - The State or Dynamic Model - State charts describe the states or conditions that classes assume over time. Activity graphs describe the workflows the system will implement.
   - The Logical or Class Model - describes the classes and objects that will make up the system.
   - The Physical Component Model - describes the software (and sometimes hardware components) that make up the system.
   - The Physical Deployment Model - describes the physical architecture and the deployment of components on that hardware architecture.

The students are expected to use the UML models, prepare necessary documents using UML and implement a system. Some hardware products like digital clock, digital camera, washing machine controller, air conditioner controller, an elctronic fan regulator, an elementary mobile phone etc. may also be chosen.

The students shall be assigned one problem on software based systems and another involving software as well as hardware.

5CS9A OPERATING SYSTEMS SIMULATION LAB (Common to CS & IT)

Class: V Sem. B.Tech.	Evaluation
Branch: Computer Engg. Schedule per Week Practical Hrs : 3	Examination Time = Four (4) Hours Maximum Marks = 100 [Sessional/Mid-term (60) & End-term (40)]

Objectives:

Understand the basic functions of operating systems.

In depth knowledge of the algorithms used for implementing the tasks performed by the operating systems.

Understand & simulate strategies used in Linux & Windows operating systems.

Develop aptitude for carrying out research in the area of operating system.

Suggested Tools:

Operating system simulator- MOSS preferably on Linux platform (Available for free download from http://www.ontko.com/moss/).

Recommended Exercises:

A. Exercises shall be given on simulation of algorithms used for the tasks performed by the operating systems. Following modules of the simulator may be used:

Scheduling

Deadlock

Memory Management Systems

File system simulator

Algorithms described in the text may be assigned. The simulation results such as average latency, hit & Miss Ratios or other performance parameters may be computed.

B. One exercise shall be on simulation of algorithms reported in the recent conferences/ journals and reproducing the results reported therein.

5CS10A DIGITAL HARDWARE DESIGN LAB (Common to CS & IT)

Class: V Sem. B.Tech.	Evaluation
Branch: Computer Engg. Schedule per Week Practical Hrs : 3	Examination Time = Four (4) Hours Maximum Marks = 75 [Sessional/Mid-term (45) & End-term

Objectives: At the end of course, the students shall be able to

- Should be able to design datapath for digital systems
- Create a digital system using discrete digital ICs
- Design a hard wired / micro-programmed control circuit
- Simulate a digital datapath in Hardware Description Language
- Understand IC descriptions and select proper IC in a given circuit based on its timing characteristics

Suggested Methodology and tools: Hardware description language like Verilog /VHDL can be used for simulation.

The exercise shall involve design of datapath, its simulation and finally realization on breadboard. Library of digital ICs have to be built. Similarly, manuals of Digital IC families have to be placed in the laboratories for reference by students.

Suggested Exercises

- Create a microprocessor from ALU 74181. For this, the students may design a small instruction set and attach necessary registers and suitable control unit to realize a microprocessor.
- Simulate and realize a Cordic calculator.
- Simulate & realize a Four bit Adder
    - Design and simulation of a 4-bit Adder
    - VHDL/Verilog HDL (Hardware description language)
    - Interfacing 7-segment decoder
- Combinational Multiplier
    - 4x4-bit multiplier
    - Binary-to-BCD conversion
    - Timing Constraints
- CRC checksum generator & verifier
- Realizing a carry look ahead adder

6CS1A COMPUTER NETWORKS (Common to CS & IT)

Class: VI Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. Schedule per Week Lectures: 3	**Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]**

NOTE: The first 2 lectures shall be devoted to review of the basis architectures and responsibilities of different layers.

Units	Contents of the subject
I	Network layer-design issue, routing algorithms: Distance vector, link state, hierarchical, Broadcast routing. Congestion control: congestion prevention policies, congestion control in Datagram subnets, load shedding, jitter control, Leaky bucket and token bucket algorithms.
II	Internetworking: Differences in networks, Tunneling, Internetwork routing, Fragmentation Network layer in the Internet: IPv4 classful and classless addressing, subnetting Network layer protocols(only working and purpose; packet headers etc. not included), Differences in IPV6 over IPV4. Routing to Mobile Hosts and Mobile IP
III	Elements of transport protocols: addressing, connection establishment and release, flow control and buffering, multiplexing and demultiplexing, crash recovery, introduction to UDP protocol. Principles of Reliable Data Transfer: Reliable data transfer over a perfectly reliable channel, Channel with bit errors and Lossy Channel with bit errors.
IV	Transport Layer in the Internet: Introduction to TCP, TCP service Model, TCP Header and segment structure, TCP connection establishment and release, transmission policy, timer management, Transactional TCP. Mobile TCP TCP Congestion Control: Fairness, TCP delay modeling.
V	Application Layer: World Wide Web (WWW), Domain Name System (DNS), E-mail, File Transfer Protocol (FTP), Introduction to Network security. P2P File Sharing: Centralized Directory, Query flooding, exploiting heterogeneity.

Text/References:

1. Tanenbaum; Computer Network, 4th Ed., Pearson.
2. Kurose; Computer Networking, 3rd Ed., Pearson.
3. Peterson, Davie; Computer Networks, 4rd Ed., ELSEVIER

6CS2A DESIGN AND ANALYSIS OF ALGORITHMS(Common to CS & IT)

Class: VI Sem. B.Tech.	Evaluation
Branch: **Computer Engg.** **Schedule per** **Week Lectures:** **3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	BACKGROUND: Review of Algorithm Complexity, Order Notations: definitions and calculating complexity. DIVIDE AND CONQUER METHOD: Binary Search, Merge Sort, Quick sort and Strassen's matrix multiplication algorithms. GREEDY METHOD: Knapsack Problem, Job Sequencing, Optimal Merge Patterns and Minimal Spanning Trees.
II	DYNAMIC PROGRAMMING: Matrix Chain Multiplication. Longest Common Subsequence and 0/1 Knapsack Problem. BRANCH AND BOUND: Traveling Salesman Problem and Lower Bound Theory. Backtracking Algorithms and queens problem.
III	PATTERN MATCHING ALGORITHMS: Naïve and Rabin Karp string matching algorithms, KMP Matcher and Boyer Moore Algorithms. ASSIGNMENT PROBLEMS: Formulation of Assignment and Quadratic Assignment Problem.
IV	RANDOMIZED ALGORITHMS. Las Vegas algorithms, Monte Carlo algorithms, randomized algorithm for Min-Cut, randomized algorithm for 2- SAT. Problem definition of Multicommodity flow, Flow shop scheduling and Network capacity assignment problems.
V	PROBLEM CLASSES NP, NP-HARD AND NP-COMPLETE: Definitions of P, NP-Hard and NP-Complete Problems. Decision Problems. Cook's Theorem. Proving NP-Complete Problems - Satisfiability problem and Vertex Cover Problem. Approximation Algorithms for Vertex Cover and Set Cover Problem.

Text/References:

1. Cormen, Leiserson, Rivest: Introduction to Algorithms, Prentice Hall of India.

2. Horowitz and Sahani: Fundamental of Computer algorithms.

3. Aho A.V , J.D Ulman: Design and analysis of Algorithms, Addison Wesley

6CS3A THEORY OF COMPUTATION (Common to CS & IT)

Class: VI Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. Schedule per Week Lectures: 3,	**Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Finite Automata & Regular Expression: Basic Concepts of finite state system, Deterministic and non-deterministic finite automation and designing regular expressions, relationship between regular expression & Finite automata minimization of finite automation mealy & Moore Machines.
II	Regular Sets of Regular Grammars: Basic Definition of Formal Language and Grammars. Regular Sets and Regular Grammars, closure proportion of regular sets, Pumping lemma for regular sets, decision Algorithms for regular sets, Myhell_Nerod Theory & Organization of Finite Automata.
III	Context Free Languages& Pushdown Automata: Context Free Grammars – Derivations and Languages – Relationship between derivation and derivation trees – ambiguity – simplification of CEG – Greiback Normal form – Chomsky normal forms – Problems related to CNF and GNF Pushdown Automata: Definitions – Moves – Instantaneous descriptions – Deterministic pushdown automata – Pushdown automata and CFL - pumping lemma for CFL - Applications of pumping Lemma.
IV	Turing Machines: Turing machines – Computable Languages and functions – Turing Machine constructions – Storage in finite control – multiple tracks – checking of symbols – subroutines – two way infinite tape. Undecidability: Properties of recursive and Recursively enumerable languages – Universal Turing Machines as an undecidable problem – Universal Languages – Rice's Theorems.
V	Linear bounded Automata Context Sensitive Language: Chomsky Hierarchy of Languages and automata, Basic Definition & descriptions of Theory & Organization of Linear bounded Automata Properties of context-sensitive languages

Text/References

1. Aho, Hopcroft and Ullman, Introduction to Automata Theory, Formal Languages and Computation, Narosa
2. Cohen, Introduction to Computer Theory, Addison Wesley.
3. Papadimitriou, Introduction to Theory of Computing, Prentice Hall.

6CS4A COMPUTER GRAPHICS & MULTIMEDIA TECHNIQUES.

Class: VII Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Introduction to Raster scan displays, Storage tube displays, refreshing, flicking, interlacing, color monitors, display processors, resolution, Introduction to Interactive. Computer Graphics: Picture analysis, Overview of programmer's model of interactive graphics, Fundamental problems in geometry. Scan Conversion: point, line, circle, ellipse polygon, Aliasing, and introduction to Anti Aliasing (No anti aliasing algorithm).
II	2D & 3D Co-ordinate system**:** Homogeneous Co-ordinates, Translation, Rotation, Scaling, Reflection, Inverse transformation, Composite transformation. Polygon Representation, Flood Filling, Boundary filling. Point Clipping, Cohen-Sutherland Line Clipping Algorithm, Polygon Clipping algorithms.
III	Hidden Lines & Surfaces: Image and Object space, Depth Buffer Methods, Hidden Facets removal, Scan line algorithm, Area based algorithms. Curves and Splines: Parametric and Non parametric Representations, Bezier curve, B-Spline Curves.
IV	Rendering: Basic illumination model, diffuse reflection, specular reflection, phong shading, Gourand shading, ray tracing, color models like RGB, YIQ, CMY, HSV
V	Multimedia components, Multimedia Input/Output Technologies: Storage and retrieval technologies, Architectural and telecommunication considerations. Animation: Introduction, Rules, problems and Animation techniques.

Text/References:

1. J. Foley, A. Van Dam, S. Feiner, J. Hughes: Computer Graphics- Principles and Practice, Pearson
2. Hearn and Baker: Computer Graphics, PHI
3. Multimedia Systems Design, Prabhat Andleigh and Thakkar, PHI.

4. Multimedia Information Networking, N.K.Sharda, PHI..

6CS5A EMBEDDED SYSTEM DESIGN

Class: VI Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Introduction to embedded systems hardware needs; typical and advanced, timing diagrams, memories (RAM, ROM, EPROM). Tristate devices, Buses, DMA, UART and PLD's. Built-ins on the microprocessor.
II	Interrupts basics, ISR;Context saving, shared data problem. Atomic and critical section, Interrupt latency. Survey of software architectures, Round Robin, Function queue scheduling architecture, Use of real time operating system.
III	RTOS, Tasks, Scheduler, Shared data reentrancy, priority inversion, mutex binary semaphore and counting semaphore. Inter task communication, message queue, mailboxes and pipes, timer functions, events. Interrupt routines in an RTOS environment.
IV	Embedded system software design using an RTOS. Hard real-time and soft real time system principles, Task division, need of interrupt routines, shared data.
V	Embedded Software development tools. Host and target systems, cross compilers, linkers, locators for embedded systems. Getting embedded software in to the target system. Debugging techniques. Testing on host machine, Instruction set emulators, logic analysers. In-circuit emulators and monitors. Regional

Text Books:

1. John Davies, MSP430 Microcontroller Basics, Elsevier, 2008.
2. Andrew N. Sloss et.al. ARM System Developers Guide, ELSEVIER
3. Muhammad Ali Mazidi et.al., The 8051 Microcontroller & Embedded Systems, Pearson
4. Embedded System Design, A Unified Hardware/Software Introduction, Frank Vahid / Tony Givargis, 2006 reprint, John Wiley Student Edition.

6CS6.1A ADVANCE TOPICS IN OPERATING SYSTEMS (Common to CS & IT)

Class: VI Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. Schedule per Week Lectures: 3	**Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Operating system structures – policies & mechanism, Structures- monolithic, layered, virtual machines, micro kernel, exokernels, client- server model. Examples from Linux & Windows. Threads Advance Concepts– Libraries- Pthreads, win32 threads, Java threads, Introduction to threading issues, system calls, cancellation, signal handling, thread pool, thread specific data, window threads, Linux threads, Solaris Threads. Message Passing System – Need of Message Passing Systems, design issues, naming, synchronization, Implementation–buffering and delivery; mailboxes; RPC & RMI. Examples Systems – Linux, Windows.
II	File System- file system layouts, file system implementation, contagious allocation, link list allocation, indexed allocation, file allocation table, virtual file system, directory implementation- linear list and hash table. File System reliability and integrity. I/O system: device drivers/ controllers, busses and interfaces- USB, IDE, SCSI, IEEE1394, RAID system, disk caching and buffering, disk management-disk formatting, RAID Structure, boot block, bad block, swap-space management. System Security: Security Problems, Program Threats, System Network Threats, Cryptography as a Security Tool, User Authentication, Implementing Security Defenses, Firewalling to Protect Systems and Network, Computer Security Classifications. Overview of security in Windows. [4]
III	The Linux OS: Unix Vs Linux, Design Principles, Kernel Structure, components Kernel Modules, Shell- usage, types; An overview of- Process Management, Thread Management and Scheduling, Memory Management, Process Scheduling in Linux, File System structure & implementation, I/O Management, Network File System, Inter-process Communications, Booting and login process, security.[3]
IV	The Window OS: Design Principles, System Components- Hardware Abstraction layer, Kernel, Executives; Environmental Subsystems- MS-DOS Environment, 16-bit Windows Environment, Win32 API, POSIX subsystem; Exception and Interrupts; An overview of-memory management, process management and thread; Process Scheduling in Windows; File Systems: Internal Layout, recovery, Volume Management and Fault Tolerance, FAT and NTFS, Security features, window registry, OS organizations.[3]

V	Multiprocessor Operating Systems: Architecture of Multiprocessor Systems, Overview of Multiprocessor OS, Kernal Structure and Multiprocessing support in Linux & Windows, Process Synchronization- Queued Lock, Spin Lock, Sleep Lock; Process Scheduling. Multimedia Operating System- Introduction to Multimedia & Data Compression- concepts, common graphics file formats, common audio file formats; Video server, Process management- real time scheduling; Multimedia file systems, Multimedia file storage mechanisms, Video sever organization.[2] Mobile Operating System- Windows CE, Palm OS, Symbian OS, JAVA card, Multos.

Text/Reference Books:

1. DM Dhamdhere: Operating Systems – A Concepts Based Approach, Tata McGraw Hill
2. Achyut S Godbole: Operating Systems, Tata McGraw Hill
3. Tanenbaum: Modern Operating System, Prentice Hall
4. A. Silberschatz and Peter B Galvin: Operating System Principals, Wiley India Pvt. Ltd.
5. Charles Crowly: Operating System A Design – Oriented Approach, Tata McGraw Hill.
6. Bach, Design of Unix Operating Systems.

6CS6.2A ARTIFICIAL INTELLIGENCE

Class: VII Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. Schedule per Week Lectures: 3	**Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Meaning and definition of artificial intelligence, Various types of production systems, Characteristics of production systems, Study and comparison of breadth first search and depth first search. Techniques, other Search Techniques like hill Climbing, Best first Search. A* algorithm, AO* algorithms etc, and various types of control strategies.
II	Knowledge Representation, Problems in representing knowledge, knowledge representation using propositional and predicate logic, comparison of propositional and predicate logic, Resolution, refutation, deduction, theorem proving, inferencing, monotonic and nonmonotonic reasoning.
III	Probabilistic reasoning, Baye's theorem, semantic networks scripts schemas, frames, conceptual dependency and fuzzy logic, forward and backward reasoning.
IV	Game playing techniques like minimax procedure, alpha-beta cut-offs etc, planning, Study of the block world problem in robotics, Introduction to understanding and natural languages processing.
V	Introduction to learning, Various techniques used in learning, introduction to neural networks, applications of neural networks, common sense, reasoning, some example of expert systems.

Text Books & References:

1. Artificial Intelligence: Elaine Rich, Kevin Knight, Mc-Graw Hill.
2. Introduction to AI & Expert System: Dan W. Patterson, PHI.
3. Artificial Intelligence by Luger (Pearson Education)
4. Russel & Norvig, Artificial Intelligence: A Modern Approach, Prentice-Hall

6CS6.3A HUMANCOMPUTER INTERFACE (Common to CS & IT)

Class: VI Sem. B.Tech.	**Evaluation**
Branch: Comp. **Engg. Schedule per** **Week Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	The Human: input-output channels, Human memory, thinking, emotions, individual differences, psychology and the design of interactive systems. The Computer: Text entry devices with focus on the design of key boards, positioning, pointing and drawing, display devices. The Interaction: Models of interaction, ergonomics, interaction styles, elements of WIMP interfaces, interactivity, experience, engagement and fun. Paradigms for Interaction.
II	Design Process: The process of design, user focus, scenarios, navigation design screen design and layout, iteration & prototyping. Usability Engineering Design rules: Principles to support usability, standards, guidelines, rules and heuristics, HCI patterns.
III	Evaluation Techniques: Definition and goals of evaluation, evaluation through expert analysis and user participation, choosing an evaluation method. User support, requirement, approaches, adaptive help systems, designing user support systems
IV	Cognitive methods: Goals and task hierarchies, linguistic models, challenges of display based systems, physical and device models, cognitive architectures.
V	Communications and collaborations models: Face to Face communication, conversations, Text based communication, group working. Task Analysis: Differences between task analysis and other techniques, task decomposition, knowledge based analysis, ER based analysis, sources of information and data collection, use of task analysis.

Text/References:

1. Human Computer Interaction; Alan Dix et.al, 3rd ed., Pearson

6CS7A JAVA PROGRAMMING LAB (Common to CS & IT)

Class: VI Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. Schedule per Week Practical Hrs.: 3	**Examination Time = Four (4) Hours Maximum Marks = 75 [Sessional/Mid-term (60) & End-term**

Objectives: At the end of the semester, the students should have clearly understood and implemented the following:

1. Develop an in depth understanding of programming in Java: data types, variables, operators, operator precedence, Decision and control statements, arrays, switch statement, Iteration Statements, Jump Statements, Using break, Using continue, return.
2. Write Object Oriented programs in Java: Objects, Classes constructors, returning and passing objects as parameter, Inheritance, Access Control, Using super, final with inheritance Overloading and overriding methods, Abstract classes, Extended classes.
3. Develop understanding to developing packages & Interfaces in Java: Package, concept of CLASSPATH, access modifiers, importing package, Defining and implementing interfaces.
4. Develop understanding to developing Strings and exception handling: String constructors, special string operations, character extraction, searching and comparing strings, string Buffer class. Exception handling fundamentals, Exception types, uncaught exceptions, try, catch and multiple catch statements. Usage of throw, throws and finally.
5. Develop applications involving file handling: I/O streams, File I/O.
6. Develop applications involving concurrency: Processes and Threads, Thread Objects, Defining and Starting a Thread, Pausing Execution with Sleep, Interrupts, Joins, and Synchronization.
7. Develop applications involving Applet: Applet Fundamentals, using paint method and drawing polygons.

It is expected that each laboratory assignments to given to the students with an aim to In order to achieve the above objectives

Indicative List of exercises:

7. Programs to demonstrate basic concepts e.g. operators, classes, constructors, control & iteration statements, recursion etc. such as complex arithmetic, matrix arithmetic, tower of Hanoi problem etc.
8. Development of programs/projects to demonstrate concepts like inheritance, exception handling, packages, interfaces etc. such as application for electricity department, library management, ticket reservation system, payroll system etc.
9. Development of a project to demonstrate various file handling concepts.
10. Development of a project to demonstrate various applet concepts.

6CS8A Computer graphics & multimedia lab

Class: VI Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Practical Hrs:2**	**Examination Time = Three (3) Hours** **Maximum Marks = 50** **[Mid-term (60) & End-term (40)]**

S. No.	List of Experiment
1	Implementation of Line, Circle and ellipse attributes
2	Two Dimensional transformations - Translation, Rotation, Scaling, Reflection, Shear
3	Composite 2D Transformations
4	Cohen Sutherland 2D line clipping and Windowing
5	Sutherland – Hodgeman Polygon clipping Algorithm
6	Three dimensional transformations - Translation, Rotation, Scaling
7	Composite 3D transformations
8	Drawing three dimensional objects and Scenes
9	Generating Fractal images
10	To plot a point (pixel) on the screen
11	To draw a straight line using DDA Algorithm
12	Implementation of mid-point circle generating Algorithm
13	Implementation of ellipse generating Algorithm
14	To translate an object with translation parameters in X and Y directions
15	To scale an object with scaling factors along X and Y directions
16	To rotate an object with a certain angle about origin
17	Perform the rotation of an object with certain angle about an arbitrary point

6CS9A DESIGN AND ANALYSIS OF ALGORITHMS Lab(Common to CS & IT)

Class: VI Sem. B.Tech.	**Evaluation**
Branch: Comp. Engg. Schedule per Week Practical Hrs : 3	**Examination Time = Four (4) Hours Maximum Marks = 100 [Sessional/Mid-term (60) & End-term (40)]**

Objectives: Upon successful completion of this course, students should be able to:

- Prove the correctness and analyze the running time of the basic algorithms for those classic problems in various domains;
- Apply the algorithms and design techniques to solve problems;
- Analyze the complexities of various problems in different domains.

Suggested Tools: For implementation and estimation of running time on various sizes of input(s) or output(s) as the case may be, Linux platform is suggested.

Suggested Exercises:

A. It is expected that teachers will assign algorithms to the students for estimation of time & space complexity. Algorithms reported in various research journals may be chosen by the teachers.

B. Problem on designing algorithms to meet complexity constraints may be assigned. For example, a problem on design, analysis and implementation for transposing a sparse matrix requiring not more than one pass from the original matrix may be assigned.

C. A guide to such problems is given below:

1. Exploring a Binary Heap: Consider a binary heap containing n numbers (the root stores the greatest number). You are given a positive integer k < n and a number x. You have to determine whether the k^{th} largest element of the heap is greater than x or not. Your algorithm must take O(k) time. You may use O(k) extra storage.

2. Merging two search trees: You are given two height balanced binary search trees T and T', storing m and n elements respectively. Every element of tree T is smaller than every element of tree T'. Every node u also stores height of the subtree rooted at it. Using this extra information how can you merge the two trees in time O(log m + log n) (preserving both the height balance and the order)?

3. Complete binary tree as an efficient data-structure:
   You are given an array of size n (n being a power of two). All the entries of the array are initialized to zero. You have to perform a sequence of the following online operations :
   1. (i) Add(i,x) which adds x to the entry A[i].
   2. (ii) Report sum(i,j) = sum of the entries in the array from indices i to j for any 0 < i < j <= n.

It can be seen easily that we can perform the first operation in O(1) time whereas the second operation may cost O(n) in worst case. Your objective is to perform these operations efficiently. Give a data-structure which will guarantee O(log n) time per operation.

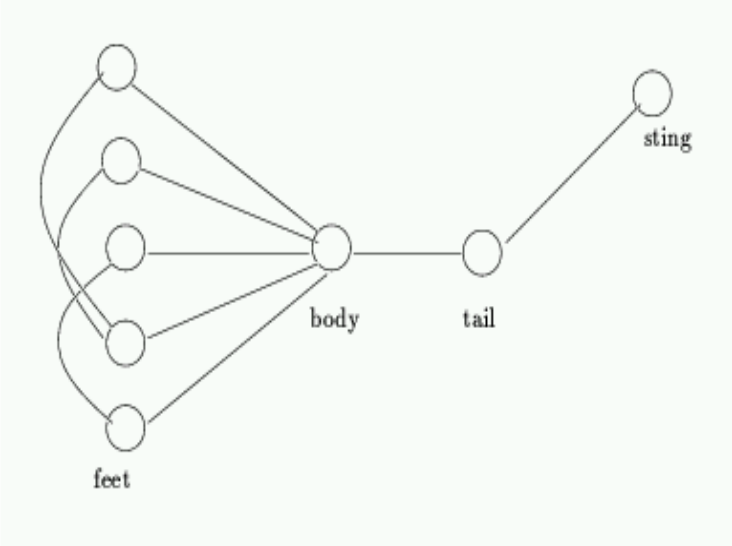
4. Problems on Amortized Analysis
    a. Delete-min in constant time!!! Consider a binary heap of size n, the root storing the smallest element. We know that the cost of insertion of an element in the heap is O(log n) and the cost of deleting the smallest element is also O(log n). Suggest a valid potential function so that the amortized cost of insertion is O(log n) whereas amortized cost of deleting the smallest element is O(1).
    b. Implementing a queue by two stack
    c. Show how to implement a queue with two ordinary stacks so that the amortized cost of each Enqueue and each Dequeue operation is O(1).

5. Computing a spanning tree having smallest value of largest edge weight: Describe an efficient algorithm that, given an undirected graph G, determines a spanning tree of G whose largest edge weight is minimum over all spanning trees of G.

6. Shortest Path Problems:
    i. From a subset of vertices to another subset of vertices
        a. Given a directed graph G(V,E), where edges have nonnegative weights. S and D are two disjoint subsets of the set of vertices. Give an O(|V| log |V| + |E|) time algorithm to find the shortest path among the set of paths possible from any node in S to any node in D.
    ii. Paths in Directed Acyclic Graph
        a. Counting the number of paths
        Given two nodes u,v in a directed acyclic graph G(V,E). Give an O(|E|) time algorithm to count all the paths from u to v.
        b. Path passing through a subset of nodes
        Given two nodes u,v and a set of vertices w_1, w_2,...,w_k in a directed acyclic graph G(V,E). Give an O(|E|) time algorithm to output a path(if exists) from u to v which passes through each of the nodes w_1,...,w_k. If there is no such path then your algorithm must report that "no such path exists".

7. Searching for a friend:
    You are standing at a crossing from where there emerge four roads extending to infinity. Your friend is somewhere on one of the four roads. Youdonotknowonwhichroadheisandhowfarheisfromyou. You have to walk to your friend and the total distance traveled by you must be at most a constant times the actual distance of your friend from you. In terminology of algorithms, you should traverse O(d) distance, where d is the distance of your friend from you.

8. A simple problem on sorted array: Design an O(n)-time algorithm that, given a real number x and a sorted array S of n numbers, determines whether or not there exist two elements in S whose sum is exactly x .
9. Finding the decimal dominant in linear time: You are given n real numbers in an array. A number in the array is called a decimal dominant if it occurs more than n/10 times in the array. Give an O(n) time algorithm to determine if the given array has a decimal dominant.
10. Finding the first one: You are given an array of infinite length containing zeros followed by ones. How fast can you locate the first one in the array?
11. Searching for the Celebrity: Celebrity is a person whom everybody knows but he knows nobody. You have gone to a party. There are total n persons in the party. Your job is to find the celebrity in the party. You can ask questions of the form Does Mr. X know Mr. Y ?. You will get a binary answer for each such question asked. Find the celebrity by asking only O(n) questions.
12. Checking the Scorpion: An n-vertex graph is a scorpion if it has a vertex of degree 1(the sting) connected to a vertex of degree two (the tail) connected to a vertex of degree n-2 (the body) connected to the other n-3 (the feet). Some of the feet may be connected to other feet. Design an algorithm that decides whether a given adjacency matrix represents a scorpion by examining only O(n) entries.



13. Endless list: You are having a pointer to the head of singly linked list. The list either terminates at null pointer or it loops back to some previous location(not necessarily to the head of the list). You have to determine whether the list loops back or ends at a null location in time proportional to the length of the list. You can use at most a constant amount of extra storage.
14. Nearest Common Ancestor:
Given a rooted tree of size n. You receive a series of online queries: "Give nearest common ancestor of u, v ". Your objective is to preprocess the tree in O(n) time to get a data structure of size O(n) so that you can answer any such query in O(log n) time.

6CS10A Embedded System Design Lab.

Class: VI Sem. B.Tech.	**Evaluation**
Branch: Comp. Engg. Schedule per Week Practical Hrs :2	**Examination Time = Four (4) Hours Maximum Marks = 75 [Sessional/Mid-term (60) & End-term (40)]**

Course Objectives

Upon successful completion of the course, students will be able to design simple embedded systems and develop related software. Students also learn to work in a team environment and communicate the results as written reports and oral presentations.

Suggested Microcontroller Platform: Texas Instruments MSP430, ARM 9, 68HC12, 8051.

It is assumed that there are 14 weeks in the semester and about 5 to 6 experiments will be carried out. More experiments are provided to bring in variation.

Experiment #0

Get familiar with the microcontroller kit and the development software. Try the sample programs that are supplied to get familiar with the Microcontroller.

Experiment #1

a) Blink an LED which is connected to your microcontroller using the built-in timer in the microcontroller. Assume that the LED should be on for x milliseconds and off for y milliseconds; assume that these values are stored in memory locations X and Y. We should be able to change the value of x and y and rerun the program.

b) Consider an alternate way to program this application. Here, the microcontroller turns the LED on and waits in a busy loop to implement a delay of x milliseconds. Then it turns the LED off and waits in a busy loop to implement a delay of y milliseconds. How do you compare these two solutions?

Experiment #2

Assume that in Experiment #1, the values of x and y have been chosen to be 200 and 500 respectively. When the LED blinking program runs, pressing a key on the keyboard should generate an interrupt to the microcontroller. If the key that has been pressed is a numeric key, the value of x and y must be interchanged by the interrupt service routine. If the key that has been pressed is not a numeric key, then the LED must be turned off for 2 seconds before resuming the blinking.

Experiment #3

If your microcontroller kit has an LCD interface, write a program to display a character string on the LCD. Assume that the string is stored at a location

STRING and consists of alphanumeric characters. The string is null-terminated. Modify your program to scroll the displayed string from left to right.

Experiment #4

Modern microcontrollers usually have an in-built Digital-to-Analog and Analog-to-Digital converter. Use the built-in DAC to generate voltage waveforms such as (a) pulse train (b) triangular waveform (c) sinusoidal waveform. Observe these waveforms on an oscilloscope.

Experiment #5

Your microcontroller may have a built-in temperature sensor. If not, interface an external temperature sensor to the microcontroller. Write a program to take several measurements of temperature at regular intervals and display the average temperature on the LCD display. Test if the readings change when the ambient temperature changes.

Experiment #6

Your microcontroller may have a built-in ADC. Build a voltmeter that can measure stable voltages in a certain range. The measured value must be displayed on the LCD display. Measure the same voltage using a multimeter and record the error in measurement. Tabulate the error for several values of the voltage.

Experiment #7

Build a simple security device based on the microcontroller kit. Interface an external motion sensor to the microcontroller. An alarm must be generated if motion is sensed in a specified region. There must be a provision to record the time at which the intrusion was detected. Similarly, there must be a provision to turn the alarm off by pressing a key.

Experiment #8

A voltage waveform v(t) is available as an input to the microcontroller. We must continuously check the waveform and record the maximum value of the waveform and display the maximum value on the LCD display. Test the program by using a DC supply to generate v(t) and varying the DC value.

6CS11A Humanities and Social Sciences (Common to CS & IT)

Class: VI Sem. B.Tech.	Evaluation
Branch: Computer Engg. Schedule per Week Practical Hrs :2	Examination Time = Three (3) Hours Maximum Marks = 50 [Sessional/Mid-term (30) & End-term (20)]

Units	Contents of the subject
I	India-brief history of Indian constitution ,framing-features fundamental rights,duties,directive principles of states,History of Indian National movement,Socio economic growth after independence.
II	Society-Social groups-concepts and types,socialization-concept theory,social control:concept,social problem in contempory India,status and role.
III	The fundamental of Economics-meaning,definition animportance of economics,Logic of choice,central economic problems,positive and normative approaches,economic systems-socialism and capitalism.
IV	Microeconomics-Law of demand and supply,utility approach,indifferencecurves,elasticity of demand & supply and applications,consumer surplus,Law of returns to factors and returns to scale.
V	Macroeconomics- concept relating to National product-National income and its measurement,simple Keynesian theory,simple multiplier, money and banking.Meaning,concept of international trade,determination of exchange rate,Balance of payments.

References:

1. Economics-Lipsey & Chrystal, Oxford Univ.Press,2010
Nordhaus, William, Samuelson,Paul-2009-10

7CS1A Cloud Computing

Class: VII Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Introduction Cloud Computing: Nutshell of cloud computing, Enabling Technology, Historical development, Vision, feature Characteristics and components of Cloud Computing. Challenges, Risks and Approaches of Migration into Cloud. Ethical Issue in Cloud Computing, Evaluating the Cloud's Business Impact and economics, Future of the cloud. Networking Support for Cloud Computing. Ubiquitous Cloud and the Internet of Things
II	Cloud Computing Architecture: Cloud Reference Model, Layer and Types of Clouds, Services models, Data center Design and interconnection Network, Architectural design of Compute and Storage Clouds. Cloud Programming and Software: Fractures of cloud programming, Parallel and distributed programming paradigms-MapReduce, Hadoop , High level Language for Cloud. Programming of Google App engine,
III	Virtualization Technology: Definition, Understanding and Benefits of Virtualization. Implementation Level of Virtualization, Virtualization Structure/Tools and Mechanisms , Hypervisor VMware, KVM, Xen. Virtualization: of CPU, Memory, I/O Devices, Virtual Cluster and Resources Management, Virtualization of Server , Desktop, Network, and Virtualization of data-center
IV	Securing the Cloud : Cloud Information security fundamentals, Cloud security services, Design principles, Policy Implementation, Cloud Computing Security Challenges, Cloud Computing Security Architecture . Legal issues in cloud Computing. Data Security in Cloud: Business Continuity and Disaster Recovery , Risk Mitigation , Understanding and Identification of Threats in Cloud, SLA-Service Level Agreements, Trust Management
V	*Cloud Platforms in Industry:* Amazon web services , Google AppEngine, Microsoft Azure Design, Aneka: Cloud Application Platform -Integration of Private and Public Clouds *Cloud applications:* Protein structure prediction, Data Analysis, Satellite Image Processing, CRM and ERP ,Social networking . Cloud Application- Scientific Application, Business Application. *Advance Topic in Cloud Computing:* Federated Cloud/InterCloud, Third Party Cloud Services

Recommended Text:

1. " Distributed and Cloud Computing " By Kai Hawang , Geofrey C.Fox, Jack J. Dongarra Pub: Elservier
2. Cloud Computing ,Principal and Paradigms, Edited By Rajkumar Buyya, Jemes Broberg, A. Goscinski, Pub.- Wiley
3. Kumar Saurabh, "Cloud Computing" , Wiley Pub
4. Krutz , Vines, "Cloud Security " , Wiley Pub
5. Velte, "Cloud Computing- A Practical Approach" ,TMH Pub

7CS2A Information System Security (Common to CS & IT)

Class: VII Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Introduction to security attacks, services and mechanism, classical encryption techniques- substitution ciphers and transposition ciphers, cryptanalysis, stream and block ciphers. Modern Block Ciphers: Block ciphers principals, Shannon's theory of confusion and diffusion, fiestal structure, data encryption standard(DES), differential and linear cryptanalysis of DES, block cipher modes of operations, triple DES.
II	AES, RC6, random number generation. S-box theory: Boolean Function, S-box design criteria, Bent functions, Propagation and nonlinearity, construction of balanced functions, S-box design.
III	Public Key Cryptosystems: Principles of Public Key Cryptosystems, RSA Algorithm, security analysis of RSA, Exponentiation in Modular Arithmetic. Key Management in Public Key Cryptosystems: Distribution of Public Keys, Distribution of Secret keys using Public Key Cryptosystems. X.509 Discrete Logarithms, Diffie-Hellman Key Exchange.
IV	Message Authentication and Hash Function: Authentication requirements, authentication functions, message authentication code, hash functions, birthday attacks, security of hash functions and MAC, MD5 message digest algorithm, Secure hash algorithm(SHA). Digital Signatures: Digital Signatures, authentication protocols, digital signature standards (DSS), proof of digital signature algorithm. Remote user Authentication using symmetric and Asymmetric Authentication
V	Pretty Good Privacy. IP Security: Overview, IP Security Architecture, Authentication Header, Encapsulation Security Payload in Transport and Tunnel mode with multiple security associations (Key Management not Included). Strong Password Protocols: Lamport's Hash, Encrypted Key Exchange.

Text/References:

1. Stalling Williams: Cryptography and Network Security: Principles and Practices, 4th Edition, Pearson Education, 2006.
2. Kaufman Charlie et.al; Network Security: Private Communication in a Public World, 2nd Ed., PHI/Pearson.
3. Pieprzyk Josef and et.al; Fundamentals of Computer Security, Springer-Verlag, 2008.
4. Trappe & Washington, Introduction to Cryptography, 2nd Ed. Pearson.

7CS3A Data Mining & Ware Housing (Common to CS & IT)

Class: VII Sem. B.Tech.	Evaluation
Branch: Computer Engg. Schedule per Week Lectures: 3	Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]

Units	Contents of the subject
I	Overview, Motivation(for Data Mining),Data Mining-Definition & Functionalities, Data Processing, Form of Data Preprocessing, Data Cleaning: Missing Values, Noisy Data, (Binning, Clustering, Regression, Computer and Human inspection), Inconsistent Data, Data Integration and Transformation. Data Reduction:-Data Cube Aggregation, Dimensionality reduction, Data Compression, Numerosity Reduction, Clustering, Discretization and Concept hierarchy generation.
II	Concept Description: Definition, Data Generalization, Analytical Characterization, Analysis of attribute relevance, Mining Class comparisons, Statistical measures in large Databases. Measuring Central Tendency, Measuring Dispersion of Data, Graph Displays of Basic Statistical class Description, Mining Association Rules in Large Databases, Association rule mining, mining Single-Dimensional Boolean Association rules from Transactional Databases– Apriori Algorithm, Mining Multilevel Association rules from Transaction Databases and Mining Multi- Dimensional Association rules from Relational Databases.
III	What is Classification & Prediction, Issues regarding Classification and prediction, Decision tree, Bayesian Classification, Classification by Back propagation, Multilayer feed-forward Neural Network, Back propagation Algorithm, Classification methods K-nearest neighbour classifiers, Genetic Algorithm. Cluster Analysis: Data types in cluster analysis, Categories of clustering methods, Partitioning methods. Hierarchical Clustering- CURE and Chameleon. Density Based Methods-DBSCAN, OPTICS. Grid Based Methods- STING, CLIQUE. Model Based Method –Statistical Approach, Neural Network approach, Outlier Analysis
IV	Data Warehousing: Overview, Definition, Delivery Process, Difference between Database System and Data Warehouse, Multi Dimensional Data Model, Data Cubes, Stars, Snow Flakes, Fact Constellations, Concept hierarchy, Process Architecture, 3 Tier Architecture, Data Mining.
V	Aggregation, Historical information, Query Facility, OLAP function and Tools. OLAP Servers, ROLAP, MOLAP, HOLAP, Data Mining interface, Security, Backup and Recovery, Tuning Data Warehouse, Testing Data Warehouse.

Text Books & References:

1. Data Warehousing in the Real World – Anahory and Murray, Pearson Education.
2. Data Mining – Concepts and Techniques – Jiawai Han and Micheline Kamber.
3. Building the Data Warehouse – WH Inmon, Wiley.

7CS4A COMPUTER AIDED DESIGN FOR VLSI

Class: VII Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Complexity in microelectronic circuit design and Moore's Law, design styles -Full-custom design, standard-cell design, Programmable Logic Devices, Field Programmable Gate Arrays, Design Stages, Computer-Aided Synthesis and Optimizations, design flow and related problems.
II	Boolean functions and its representations – co-factor, unite, derivatives, consensus and smoothing; tabular representations and Binary Decision Diagram (BDD), OBDD, ROBDD and Bryant's reduction algorithm and ITE algorithm. Hardware abstract models – structures and logic networks, State diagram, data-flow and sequencing graphs, hierarchical sequencing graphs. Compilation and behavioral optimizations.
III	Architectural Synthesis – Circuit description and problem definition, temporal and spatial domain scheduling, synchronization problem. Scheduling algorithms - ASAP and ALAP scheduling algorithms, scheduling under constraints, relative scheduling, list scheduling heuristic. Scheduling in pipelined circuits.
IV	Resource Sharing & Binding in sequencing graphs for resource dominated circuits, sharing of registers and busses; binding variables to registers. Two-level logic optimization principles – definitions and exact logic minimizations. Positional cube notations, functions with multi-valued logic. List-oriented manipulations.
V	Physical Design. Floor planning – goals and objectives. Channel definition, I/O and power planning. Clock Planning. Placement – goals and objectives. Placement algorithms. Iterative improvement algorithms. Simulated Annealing. Timing-driven Placement. Global routing – goals and objectives. Global routing methods. Timing-driven global routing. Detailed Routing – goals and objectives. Left-edge algorithm. Constraints and routing graphs. Channel routing algorithms. Via minimization. Clock routing, power routing, circuit extraction and Design Rule Checking.

Text Books:

1. S.H. Gerez. Algorithms VLSI Design Automation. Wiley India. (Indian edition available.)
2. Michael John Sebastian Smith. Application-Specific Integrated Circuits. Addison-Wesley. (Low-priced edition is available.)
3. G.D. Micheli, Synthesis and optimization of digital circuits, TMH.

References:

1. http://www.fie-conference.org/fie98/papers/1002.pdf
2. S. Sait and H. Youssef. VLSI Physical Design Automation: Theory and Practice.

7CS5A COMPILER CONSTRUCTION

Class: VII Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Compiler, Translator, Interpreter definition, Phase of compiler introduction to one pass & Multipass compilers, Bootstrapping, Review of Finite automata lexical analyzer, Input, buffering, Recognition of tokens, Idea about LEX: A lexical analyzer generator, Error handling.
II	Review of CFG Ambiguity of grammars, Introduction to parsing. Bottom up parsing Top down parsing techniques, Shift reduce parsing, Operator precedence parsing, Recursive descent parsing predictive parsers. LL grammars & passers error handling of LL parser. LR parsers, Construction of SLR, Conical LR & LALR parsing tables, parsing with ambiguous grammar. Introduction of automatic parser generator: YACC error handling in LR parsers.
III	Syntax directed definitions; Construction of syntax trees, L-attributed definitions, Top down translation. Specification of a type checker, Intermediate code forms using postfix notation and three address code, Representing TAC using triples and quadruples, Translation of assignment statement. Boolean e xpression and control structures.
IV	Storage organization, Storage allocation, Strategies, Activation records, Accessing local and non local names in a block structured language, Parameters passing, Symbol table organization, Data structures used in symbol tables.
V	Definition of basic block control flow graphs, DAG representation of basic block, Advantages of DAG, Sources of optimization, Loop optimization, Idea about global data flow analysis, Loop invariant computation, Peephole optimization, Issues in design of code generator, A simple code generator, Code generation from DAG.

Text/References:

1. Aho, Ullman and Sethi: Compilers, Addison Wesley.

2. Holub, Compiler Design in C, PHI.

7CS6.1A ADVANCE DATABASE MANGEMENT SYSTEMS

Class: VII Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Query Processing and Optimization: Overview of Relational Query Optimization, System Catalog in a Relational DBMS, Alternative Plans, Translating SQL, Queries into Algebra, Estimating the Cost of a Plan, Relational Algebra Equivalences, Enumeration of Alternative Plans. [2]
II	Object Database Systems: Motivating Examples, Structured Data Types, Operations On Structured Data, Encapsulation and ADT's, Inheritance, Objects, OIDs and Reference Types, Database Design for an ORDBMS, ORDBMS Implementation Challenges, ORDBMS, Comparing RDBMS, OODBMS, and ORDBMS.
III	Parallel and Distributed Databases: Architectures for Parallel, Databases, Parallel Query Evaluation, Parallelizing Individual Operations, Parallel Query Optimization, Distributed DBMS Architectures, Storing Data in a Distributed DBMS, Distributed Catalog Management, Distributed Query Processing, Updating Distributed Data, Introduction to Distributed Transactions, Distributed Concurrency Control, Distributed Recovery. [2]
IV	Database Security and Authorization: Introduction to Database Security, Access Control, Discretionary Access Control- Grant and Revoke on Views and Integrity Constraints, Mandatory Access Control- Multilevel Relations and Polyinstantiation, Covert Channels, DoD Security Levels, Additional Issues Related to Security- Role of the Database Administrator, Security in Statistical Databases, Encryption. [2]
V	POSTGES: POSTGRES user interfaces, sql variations and extensions, Transaction Management, Storage and Indexing, Query processing and optimizations, System Architectures. XML: Motivation, Structure of XML data, XML Document Schema, Querying and Transformation, Application Program Interface to XML, Storage of XML Data, XML applications. [2]

Text/References

1. Elmasri R and Navathe SB, Fundamentals of Database Systems, 3rd Edition, Addison Wesley,2000.
2. Connolly T, Begg C and Strachan A, Database Systems, 2nd Edition, Addison Wesley, 1999
3. Ceri Pelagatti , Distributed Database: Principles and System - (McGraw Hill)
4. Simon AR, Strategic Database Technology: Management for the Year 2000, Morgan Kaufmann, 1995
5. A. Silversatz, H. Korth and S. Sudarsan: Database Cocepts 5th edition, Mc-Graw Hills 2005.

7CS6.2A Robotics

Class: Vll Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Unit	Contents
I	Introduction -- brief history, types, classification and usage, Science and Technology of robots, Some useful websites, textbooks and research journals.
II	Elements of robots -- joints, links, actuators, and sensors Position and orientation of a rigid body, Homogeneous transformations, Representation of joints, link representation using D-H parameters, Examples of D-H parameters and link transforms, different kinds of actuators – stepper, DC servo and brushless motors, model of a DC servo motor, Types of transmissions, Purpose of sensors, internal and external sensors, common sensors – encoders, tachometers, strain gauge based force-torque sensors
III	Introduction, Direct and inverse kinematics problems, Examples of kinematics of common serial manipulators, workspace of a serial robot, Inverse kinematics of constrained and redundant robots, Tractrix based approach for fixed and free robots and multi-body systems, simulations and experiments, Solution procedures using theory of elimination, Inverse kinematics solution for the general 6R serial manipulator.
IV	Degrees-of-freedom of parallel mechanisms and manipulators, Active and passive joints, Constraint and loop-closure equations, Direct kinematics problem, Mobility of parallel manipulators, Closed-from and numerical solution, Inverse kinematics of parallel manipulators and mechanisms, Direct kinematics of Gough-Stewart platform.
V	Linear and angular velocity of links, Velocity propagation, Manipulator Jacobians for serial and parallel manipulators, Velocity ellipse and ellipsoids, Singularity analysis for serial and parallel manipulators, Loss and gain of degree of freedom, Statics of serial and parallel manipulators, Statics and force transformation matrix of a Gough-Stewart platform, Singularity analysis and statics.

Text Books :

1. Mittal and Nagrath, Robotics and Control, Tata McGraw-Hill Education, 2003.

2. Fred G. Martin, Robotic Explorations: A Hands On Introduction to Engineering, Pearson Education, 2001.

7CS6.3A Data Compression Techniques

Class: VII Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Compression Techniques: Lossless, lossy, measure of performance, modeling & coding. Lossless compression: Derivation of average information, data models, uniquely decodable codes with tests, prefix codes, Kraft-Mc Millan inequality. Huffman coding: Algorithms, minimum variance Huffman codes, optimality, length extended codes, adaptive coding, Rice codes, using Huffman codes for lossless image compression.
II	Arithmetic coding with application to lossless compression. Dictionary Techniques: LZ77, LZ78, LZW Predictive coding: Burrows-Wheeler Transform and move-to-front coding, JPEG-LS Facsimile Encoding: Run length, T.4 and T.6
III	Lossy coding- Mathematical preliminaries: Distortion criteria, conditional entropy, average mutual information, differential entropy, rate distortion theory, probability and linear system models. Scalar quantization: The quantization problem, uniform quantizer, Forward adaptive quantization, non-uniform quantization-Formal adopting quantization, companded Quantization Vector quantization: Introduction, advantages, The Linde-Ruzo-Grey algorithm, lattice vector quantization.
IV	Differential encoding – Introduction, Basic algorithm, Adaptive DPCM, Delta modulation, speech and image coding using delta modulation. Sampling in frequency and time domain, z-transform, DCT, DST, DWHT, quantization and coding of transform coefficient.
V	Sub band coding: Introduction, Filters, Basic algorithm, Design of Filter banks, G.722, MPEG. Wavelet based compression: Introduction, wavelets multi-resolution analysis and the scaling function implementation using filters.

Text Books & References:

1. Sayood K: Introduction to Data Compression: ELSEVIER 2005.

7CS7A Web Development Lab

Class: VII Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Practical Hrs: 2**	**Examination Time = Three (3) Hours** **Maximum Marks = 50** **[Mid-term (60) & End-term (40)]**

S. No.	List of Experiment
1	. Creation of HTML Files
2	Working with Client Side Scripting : VBScript, JavaScript
3	Configuration of web servers: Apache Web Server, Internet Information Server (IIS)
4	Working with ActiveX Controls in web documents
5	Experiments in Java Server Pages: Implementing MVC Architecture using Servlets, Data Access Programming (using ADO), Session and Application objects, File System Management
6	Working with other Server Side Scripting: Active Server Pages, Java Servlets, PHP
7	Experiments in Ajax Programming
8	Developing Web Services
9	Developing any E-commerce application (Mini Project)
10	Application Development in cloud computing Environment
11	Experiment Using Open Source Tool e.g. ANEKA

7CS8A VLSI PHYSICAL DESIGN LAB

Class: VII Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. **Schedule per Week** **Practical Hrs : 3**	**Examination Time = Four (4) Hours** **Maximum Marks = 75** **[Sessional/Mid-term (45) & End-term (30)]**

VLSI Physical Design Automation is essentially the research, development and productization of algorithms and data structures related to the physical design process. The objective is to investigate optimal arrangements of devices on a plane (or in three dimensions) and efficient interconnection schemes between these devices to obtain the desired functionality and performance. Since space on a wafer is very expensive real estate, algorithms must use the space very efficiently to lower costs and improve yield. In addition, the arrangement of devices plays a key role in determining the performance of a chip. Algorithms for physical design must also ensure that the layout generated abides by all the rules required by the fabrication process. Fabrication rules establish the tolerance limits of the fabrication process. Finally, algorithms must be efficient and should be able to handle very large designs. Efficient algorithms not only lead to fast turn-around time, but also permit designers to make iterative improvements to the layouts. The VLSI physical design process manipulates very simple geometric objects, such as polygons and lines. As a result, physical design algorithms tend to be very intuitive in nature, and have significant overlap with graph algorithms and combinatorial optimization algorithms. In view of this observation, many consider physical design automation the study of graph theoretic and combinatorial algorithms for manipulation of geometric objects in two and three dimensions. However, a pure geometric point of view ignores the electrical (both digital and analog) aspect of the physical design problem. In a VLSI circuit, polygons and lines have inter-related electrical properties, which exhibit a very complex behavior and depend on a host of variables. Therefore, it is necessary to keep the electrical aspects of the geometric objects in perspective while developing algorithms for VLSI physical design automation. With the introduction of Very Deep Sub-Micron (VDSM), which provides very small features and allows dramatic increases in the clock frequency, the effect of electrical parameters on physical design will play a more dominant role in the design and development of new algorithms.

(Source: Algorithms For VLSI Physical Design Automation, by Naveed A. Sherwani).

The exercise should be such that the above objectives are met.

Automation tools such as Synopsis/ Cadence are available in the area. However, to begin, the students shall be assigned exercises on route optimization, placement & floor planning. Small circuits may be taken & algorithms implemented. At a later stage, the students may use tools and design more complex circuits.

7CS9A COMPILER DESIGN LAB

Class: VII Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. **Schedule per Week** **Practical Hrs : 3**	**Examination Time = Four (4) Hours** **Maximum Marks = 75** **[Sessional/Mid-term (60) & End-term (40)]**

Objectives: At the end of the semester, the students should have clearly understood and implemented the following:

1. Develop an in depth understanding of system programming concept. Lexical analysis, syntax analysis, semantics analysis, code optimization, code generation. Language specification and processing

2. Develop an Understanding of Scanning by using concept of Finite state automaton. Parse tree and syntax tree, Top down parsing (recursive decent parsing, LL (1) parser) Bottom up parsing (operator precedence parsing) .Managing symbol table, opcode table, literal table, pool table

3. Develop an Understanding of Intermediate code form: Three address code, Polish notation (Postfix strings)

4. Develop an Understanding of Allocation data structure. Heaps

5. Develop an Understanding about Language processor development tools: LEX, YACC. Language processing activities (Program generation and execution)

It is expected that each laboratory assignments to given to the students with an aim to In order to achieve the above objectives

Indicative List of exercises:

1. Write grammar for a fictitious language and create a lexical analyzer for the same.
2. Develop a lexical analyzer to recognize a few patterns in PASCAL and C (ex: identifiers, constants, comments, operators etc.)
3. Write a program to parse using Brute force technique of Top down parsing
4. Develop on LL (1) parser (Construct parse table also).
5. Develop an operator precedence parser (Construct parse table also)
6. Develop a recursive descent parser
7. Write a program for generating for various intermediate code forms

   i) Three address code ii) Polish notation
8. Write a program to simulate Heap storage allocation strategy
9. Generate Lexical analyzer using LEX
10. Generate YACC specification for a few syntactic categories
11. Given any intermediate code form implement code optimization techniques

Reference

V.V Das, Compiler Design using FLEX and YACC, PHI

8CS1A MOBILE COMPUTING (Common to CS & IT)

Class: VIII Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Mobile computing: Definitions, adaptability issues (transparency, Environmental Constraints, application aware adaptation), mechanisms for adaptation and incorporating adaptations. Mobility management: mobility management, location management principle and techniques, PCS location management Scheme.
II	Data dissemination and management: challenges, Data dissemination, bandwidth allocation for publishing, broadcast disk scheduling, mobile cache maintenance schemes, Mobile Web Caching. Introduction to mobile middleware.
III	Middleware for application development: adaptation, Mobile agents. Service Discovery Middleware: Service Discovery & standardization Methods (universally Unique Identifiers, Textual Description & using interfaces), unicast Discovery, Multicast Discovery & advertisement, service catalogs, Garbage Collection, Eventing.
IV	Mobile IP, Mobile TCP, Database systems in mobile environments, World Wide Web and mobility
V	Ad Hoc networks, localization, MAC issues, Routing protocols, global state routing (GSR), Destination sequenced distance vector routing (DSDV), Dynamic source routing (DSR), Ad Hoc on demand distance vector routing (AODV), Temporary ordered routing algorithm (TORA), QoS in Ad Hoc Networks, applications.

Text/References:

1. Frank Adelstein, Sandeep Gupta, Golden Richard III, Loren Schwiebert, Fundamentals of Mobile and Pervasive Computing, TMH.
2. Principles of mobile computing Hansmann & Merk., Springer
3. Mobile communications Jochen Schiller , Pearson
4. 802.11 wireless networks Matthew S.Gast, O'REILLY.
5. Wireless LANs: Davis & McGuffin, McGraw Hill
6. Mobile Communications Handbook by Jerry D. Gybson
7. Mobile Communications Handbook by R

8CS2A Digital Image Processing (Common to CS & IT)

Class: VIII Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	**Introduction to Image Processing:** Digital Image representation, Sampling & Quantization, Steps in image Processing, Image acquisition, color image representation
II	**Image Transformation & Filtering:** Intensity transform functions, histogram processing, Spatial filtering, Fourier transforms and its properties, frequency domain filters, colour models, Pseudo colouring, colour transforms, Basics of Wavelet Transforms
III	**Image Restoration:** Image degradation and restoration process, Noise Models, Noise Filters, degradation function, Inverse Filtering, Homomorphism Filtering
IV	**Image Compression:** Coding redundancy, Interpixel redundancy, Psychovisual redundancy, Huffman Coding, Arithmetic coding, Lossy compression techniques, JPEG Compression
V	**Image Segmentation & Representation:** Point, Line and Edge Detection, Thresholding, Edge and Boundary linking, Hough transforms, Region Based Segmentation, Boundary representation, Boundary Descriptors, Regional

References:

1. Gonzalez and Woods: Digital Image Processing ISDN 0-201-600- 781, Addison Wesley 1992.

Boyle and Thomas: Computer Vision - A First Gurse 2nd Edition, ISBN 0-632-028-67X, Blackwell

Science 1995.

2. Gonzalez and Woods: Digital Image Processing ISDN 0-201-600- 781, Addison Wesley 1992.

3. Pakhera Malay K: Digital Image Processing and Pattern Recogination, PHI.

4. Trucco&Verri: Introductory Techniques for 3-D Computer Vision, Prentice Hall, Latest Edition

5. Low: Introductory Computer Vision and Image Processing, McGraw-Hill 1991, ISBN 0-07-707403-3.

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8CS3A DISTRIBUTED SYSTEMS

Class: VIII Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Distributed Systems: Features of distributed systems, nodes of a distributed system, Distributed computation paradigms, Model of distributed systems, Types of Operating systems: Centralized Operating System, Network Operating Systems, Distributed Operating Systems and Cooperative Autonomous Systems, design issues in distributed operating systems. Systems Concepts and Architectures: Goals, Transparency, Services, Architecture Models, Distributed Computing Environment (DCE).[1.2] Theoretical issues in distributed systems: Notions of time and state, states and events in a distributed system, time, clocks and event precedence, recording the state of distributed systems.[2]
II	Concurrent Processes and Programming: Processes and Threads, Graph Models for Process Representation, Client/Server Model, Time Services, Language Mechanisms for Synchronization, Object Model Resource Servers, Characteristics of Concurrent Programming Languages (Language not included).[1] Inter-process Communication and Coordination: Message Passing, Request/Reply and Transaction Communication, Name and Directory services, RPC and RMI case studies.[1]
III	Distributed Process Scheduling: A System Performance Model, Static Process Scheduling with Communication, Dynamic Load Sharing and Balancing, Distributed Process Implementation.[1] Distributed File Systems: Transparencies and Characteristics of DFS, DFS Design and implementation, Transaction Service and Concurrency Control, Data and File Replication.[1,2] Case studies: Sun network file systems, General Parallel file System and Window's file systems. Andrew and Coda File Systems [2,3]
IV	Distributed Shared Memory: Non-Uniform Memory Access Architectures, Memory Consistency Models, Multiprocessor Cache Systems, Distributed Shared Memory, Implementation of DSM systems.[1] Models of Distributed Computation: Preliminaries, Causality, Distributed Snapshots, Modeling a Distributed Computation, Failures in a Distributed System, Distributed Mutual Exclusion, Election, Distributed Deadlock handling, Distributed termination detection. [1]
V	Distributed Agreement: Concept of Faults, failure and recovery, Byzantine Faults, Adversaries, Byzantine Agreement, Impossibility of Consensus and Randomized Distributed Agreement.[1] Replicated Data Management: concepts and issues, Database Techniques, Atomic Multicast, and Update Propagation.[1] CORBA case study: Introduction, Architecture, CORBA RMI, CORBA Services.[3]

Text Books:

1. Distributed operating systems and algorithm analysis by Randy Chow and T. Johnson, Pearson
2. Operating Systems A concept based approach by DM Dhamdhere, TMH
3. Distributed Systems- concepts and Design, Coulouris G., Dollimore J, and Kindberg T., Pearson

8CS4.1A Hardware Testing and Fault Tolerance

Class: VIII Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Overview of hardware testing. Reliability and Testing, Difference between Verification and Testing, Concepts of fault models, test pattern generation and fault coverage. Types of tests – exhaustive testing, pseudo-exhaustive testing, pseudo-random testing, and deterministic testing. Test Application. Design for Test. Testing Economics. Defects, Failures and Faults. How are physical defects modeled as faults. Stuck-at faults, Single stuck-at-faults multiple stuck-at faults, bridging faults, delay faults, transient faults.
II	Relation between VLSI Design and Testing. a) Design Representation for the purpose of testing – Representation in the form of mathematical equations, tabular format, graphs, Binary Decision Diagrams, Netlists, or HDL descriptions. b) Recap of VLSI Design Flow and where testing fits in the flow. Importance of Simulation and Fault Simulation. Compiled and event-driven simulation. Parallel and deductive fault simulation. Using fault simulation to estimate fault coverage and building a fault dictionary
III	Combinational Test Pattern Generation. D-algorithm. Critical Path Tracking. PODEM algorithm for test generation. Testing sequential circuits. Functional and deterministic ATPG for sequential circuits and the associated challenges. Motivation for Design for Testability. Test Points, Partitioning for Testability. Scan Testing. Scan Architectures. Cost of Scan Testing. Boundary Scan Testing. Board-level testing. Boundary-scan Architecture and various modes of operation.
IV	a) Built-in Self Test. Pseudo-random test generation. Response Compaction. Random pattern-resistant faults. BIST architectures – Circular BIST, BILBO, STUMPS. b) Testing of Memories – Fault models, Functional tests for memories, Memory BIST. c) Testing of microprocessors.
V	Hardware fault tolerance. Failure Rate, Reliability, Mean Time to Failure. Different kinds of redundancy schemes for fault-tolerance (Space, Time, and Information Redundancy). N-modular Redundancy. Watch Dog Processors, Byzantine Failures. Information Redundancy – parity codes, checksums, m-of-n codes. RAID architectures for disk storage systems. Fault tolerance in interconnection networks. Fault-tolerant routing techniques.

Text Book:

1. Samiha Mourad and Yervant Zorian. Principles of Electronic Systems. Wiley Student Editon. [Available in Indian Edition].
2. Koren and C. Mani Krishna. Fault-Tolerant Systems. Elsevier. (Indian Edition Available.)

Text/References:

1. Abramovici, M., Breuer, M. A. and Friedman, A. D. Digital systems testing and testable design. IEEE press (Indian edition available through Jayco Publishing house), 2001.2. Essentials of Electronic Testing for Digital, Memory and Mixed-Signal VLSI Circuits by Bushnell and Agrawal, Springer, 2000.

8CS4.2A REAL TIME SYSTEMS

Class: VIII Sem. B.Tech.	Evaluation
Branch: Computer Engg. Schedule per Week Lectures: 3	Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]

Units	Contents of the subject
I	Introduction: Definition, Typical Real Time Applications, concept of tasks, types of tasks and real time systems, block diagram of RTS, and tasks parameters -Release Times, execution time, period, Deadlines, and Timing Constraints etc. RTS requirements.
II	Reference Models for Real Time Systems: processors and Resources, Temporal Parameters of Real-Time Workload, Periodic and Aperiodic Task Model, Precedence Constrains and Data Dependency, Other Types of Dependencies, Functional Parameters, Resource Parameters. Real Time Scheduling: classification of Real Time Scheduling, scheduling criteria, performance metrics, schedulability analysis, Introduction to Clock Driven scheduling, Weighted Round Robin Approach and Priority Driven Approach. Dynamic Versus Static systems, Offline Versus Online Scheduling.
III	Periodic tasks scheduling: Clock Driven Scheduling – definition, notations and assumption, scheduler concepts, general scheduling structure, cyclic executives. Priority Driven Scheduling; notations and assumption, fixed priority verses dynamic priority, fixed priority scheduling algorithms (RM and DM) and their schedulability analysis, concept of schedulability tests – Inexact and exact schedulability tests for RM and DM, Optimality of the RM and DM algorithms, practical factors.
IV	Aperiodic task scheduling; assumption and approaches, server based and non-server based fixed priority scheduling algorithms – polling server, deferrable server , simple sporadic server, priority exchange, extended priority exchange, slack stealing. Introduction to scheduling of flexible computations –flexible applications, imprecise computation model and firm deadline model.
V	Resources Access Control: Assumptions on Resources and their usage, Effect of Resource Contention and Resource Access Control (RAC), Non-preemptive Critical Sections, priority inversion problem, need of new resource synchronization primitives/protocols for RTS, Basic Priority-Inheritance and Priority-Ceiling Protocols, Stack Based Priority-Ceiling Protocol, Use of Priority- Ceiling Protocol in Dynamic Priority Systems, Preemption Ceiling Protocol, Access Control in Multiple-Unit Resources, Controlling Concurrent Accesses to Data Objects.

Text & References:

1. J.W.S.Liu: Real-Time Systems, Pearson Education Asia
2. P.D.Laurence, K.Mauch: Real-time Microcomputer System Design, An Introduction, McGraw Hill
3. C.M. Krisna & K. G. Shim- Real time systems- TMH

8CS4.3A AInformation Retrieval (Common to CS & IT)

Class: VIII Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Knowledge Representation: Knowledge representation, Basics of Prepositional logic, Predicate logic, reasoning using first order logic, unification, forward chaining, backward chaining, resolution Production rules, frames, semantic networks scripts.
II	Ontology Development: Description logic-taxonomies, Topic maps Ontology, Definition expressing ontology, logically ontology representations, – XML, RDF, RDFS, OWL, OIL, ontology development for specific domain, ontology engineering, Semantic web services.
III	Information Retrieval Modeling: Information retrieval, taxonomy, formal characterization, classic information retrieval, set theoretic model, algebraic model, probabilistic model, structured text, retrieval models, models for browsing, retrieval performance evaluation, keyword based querying, pattern matching, structural queries, query operations.
IV	Text and Multimedia Languages and Properties: Introduction, metadata, markup languages, multimedia. Text operations: document preprocessing, document clustering text Compressionbasic concepts - statistical methods. Indexing and searching: inverted files, suffix trees, signature file, Boolean queries, sequential searching, pattern matching.
V	Recent Trends in IR: Parallel and distributed IR, multimedia IR, data modeling, query languages, A generic Multimedia indexing Approach, one dimensional time series, two dimensional color images, Automatic feature extraction. Web Searching, Characterizing the Web, Search Engines, Browsing, Meta searchers, Searching using hyperlinks

TEXT BOOKS :

1. Stuart Russell and Peter Norvig, “Artificial Intelligence – A Modern Approach”, Pearson Education, Second edition, 2003. (UNIT I)

2. Michael C. Daconta, Leo J. Obart and Kevin J. Smith,”Semantic Web – A Guide to the Future of XML, Web Services and Knowledge Management”, Wiley Publishers, 2003 (UNIT II)

3. Ricardo Baeza-Yates, BerthierRibeiro-Neto, “Modern Information Retrieval”, Addison Wesley, 1999. (UNITs III, IV & V)

REFERENCES

1. Elain Rich and Kevin Knight, "Artificial Intelligence", Tata McGraw-Hill, Third edition, 2003

2. Christopher D. Manning, PrabhakarRaghavan and HinrichSchutze, “Introduction to Information Retrieval”, Cambridge University Press, 2008.

8CS5A UNIX NETWORK PROGRAMMING & SIMULATION LAB

Class: VIII Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. **Schedule per Week** **Practical Hrs.: 3**	**Examination Time = Four (4) Hours** **Maximum Marks = 100** **[Sessional/Mid-term (60) & End-term (40)]**

Objectives:

At the end of course, the students should be able to

- Understand various distributions of Unix viz. BSD, POSIX etc.
- Write client/server applications involving unix sockets involving TCP or UDP involving iterative or concurrent server.
- Understand IPV4 & IPV6 interoperability issues
- Use fork( ) system call.
- Understand the network simulator NS2 and Simulate routing algorithm on NS2 (Available on http://www.isi.edu/nsnam/ns/).

Suggested Platform: For Socket Programming- Linux, For NS2 Any of Microsoft Windows or Linux (In case of Microsoft, Virtual environment cygwin will also be required).

Suggested Exercises

S.No.	**List of Experiments**
1.	Write two programs in C: hello_client and hello_server • The server listens for, and accepts, a single TCP connection; it reads all the data it can from that connection, and prints it to the screen; then it closes the connection • The client connects to the server, sends the string "Hello, world!", then closes the connection
2.	Write an Echo_Client and Echo_server using TCP to estimate the round trip time from client to the server. The server should be such that it can accept multiple connections at any given time.
3.	Repeat Exercises 1 & 2 for UDP.
4.	Repeat Exercise 2 with multiplexed I/O operations
5.	Simulate Bellman-Ford Routing algorithm in NS2

References:

- **Stevens, Unix Network Programming, Vol-I**

8CS6A FPGA LAB.

Class: VIII Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. **Schedule per Week** **Practical Hrs : 3**	**Examination Time = Four (4) Hours** **Maximum Marks = 100** **[Sessional/Mid-term (60) & End-term (40)]**

S. No.	**List of Experiments**
1.	Fundamental Theory Introduction to DSP architectures and programming Sampling Theory, Analog-to-Digital Converter (ADC), Digital-to-Analog Converter (DAC), and Quantization; Decimation, Interpolation, Convolution, Simple Moving Average; Periodic Signals and harmonics; Fourier Transform (DFT/FFT), Spectral Analysis, and time/spectrum representations; FIR and IIR Filters;
2.	Design (Simulation) using MATLAB/ Simulink Simulate the lab exercises using MATLAB/Simulink
3.	Implementation using pure DSP, pure FPGA and Hybrid DSP/FPGA platforms Digital Communications: On-Off- Keying (OOK), BPSK modulation, and a simple transceiver design Adaptive Filtering: Echo/Noise Cancellation, Least Mean Square (LMS) algorithm (2 weeks) Wireless Communications: Channel coding/decoding, Equalization, Simple Detection Algorithm, OFDM Speech Processing: Prediction Algorithms, Speech Classification and Synthesis

8CS7A Digital Image Processing lab(Common to Comp. Engg. & Info. Tech)

Class: VIII Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Practical Hrs: 2**	**Examination Time = Three (3) Hours** **Maximum Marks = 50** **[Mid-term (60) & End-term (40)]**

S. No.	List of Experiment
1	Color image segmentation algorithm development
2	Wavelet/vector quantization compression
3	Deformable templates applied to skin tumor border finding
4	Helicopter image enhancement
5	High-speed film image enhancement
6	Computer vision for skin tumor image evaluation
7	New Border Images

Scheme and Syllabus

Rajasthan Technical University, Kota

B. Tech. in Electronics & Communication Engineering

(Session 2018-19)

Scheme of
UNDERGRADUATE DEGREE COURSE
in
Electronics & Communication Engineering



Rajasthan Technical University, Kota

Effective from session: 2018 – 2019

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Teaching & Examination Scheme
B.Tech. : Electronics & Communication Engineering
2nd Year - III Semester

SN	Category	Course		Contact hrs/week			Marks				Cr
		Code	Title	L	T	P	Exm Hrs	IA	ETE	Total	
THEORY											
1	BSC	3EC2-01	Advanced Engineering Mathematics-I	3	0	0	3	30	120	**150**	**3**
2	HSMC	3EC1-02/ 3EC1-03	Technical Communication/Managerial Economics and Financial Accounting	2	0	0	2	20	80	**100**	**2**
3	PCC	3EC4-04	Digital System Design	3	0	0	3	30	120	**150**	**3**
4		3EC4-05	Signal & Systems	3	0	0	3	30	120	**150**	**3**
5		3EC4-06	Network Theory	3	1	0	3	40	160	**200**	**4**
6		3EC4-07	Electronics Devices	3	1	0	3	40	160	**200**	**4**
			Sub Total	17	2	0		190	760	**950**	**19**
PRACTICAL & SESSIONAL											
8	PCC	3EC4-21	Electronics Devices Lab	0	0	2		30	20	**50**	**1**
9		3EC4-22	Digital System Design Lab	0	0	2		30	20	**50**	**1**
10		3EC4-23	Signal Processing Lab	0	0	2		30	20	**50**	**1**
11	ESC	3EC3-24	Computer Programming Lab-I	0	0	2		30	20	**50**	**1**
13	PSIT	3EC7-30	Industrial Training	0	0	1				**50**	**1**
14	SODECA	3EC8-00	Social Outreach, Discipline & Extra Curricular Activities							**25**	**0.5**
			Sub- Total	0	0	9		120	80	**275**	**5.5**
			TOTAL OF III SEMESTER	17	2	9		310	840	**1225**	**24.5**

L: *Lecture,* ***T:*** *Tutorial,* ***P:*** *Practical,* ***Cr:*** *Credits*

ETE: *End Term Exam,* ***IA:*** *Internal Assessment*

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Teaching & Examination Scheme
B.Tech. : Electronics & Communication Engineering
2nd Year - IV Semester

SN	Category	Course		Contact hrs/week			Marks				Cr
		Code	Title	L	T	P	Exm Hrs	IA	ETE	Total	
THEORY											
1	BSC	4EC2-01	Advanced Engineering Mathematics-II	3	0	0	3	30	120	**150**	**3**
2	HSMC	4EC1-03/ 4EC1-02	Managerial Economics and Financial Accounting/ Technical Communication	2	0	0	2	20	80	**100**	**2**
3	PCC	4EC4-04	Analog Circuits	3	0	0	3	30	120	**150**	**3**
4		4EC4-05	Microcontrollers	3	0	0	3	30	120	**150**	**3**
5	ESC	4EC3-06	Electronics Measurement & Instrumentation	3	0	0	3	30	120	**150**	**3**
6	PCC	4EC4-07	Analog and Digital Communication	3	0	0	3	30	120	**150**	**3**
			Sub Total	17	0	0		170	680	**850**	**17**
PRACTICAL & SESSIONAL											
8	PCC	4EC4-21	Analog and Digital Communication Lab	0	0	3		45	30	**75**	**1.5**
9		4EC4-22	Analog Circuits Lab	0	0	3		45	30	**75**	**1.5**
10		4EC4-23	Microcontrollers Lab	0	0	3		45	30	**75**	**1.5**
11		4EC4-24	Electronics Measurement & Instrumentation Lab	0	0	3		45	30	**75**	**1.5**
12	SODECA	4EC18-00	Social Outreach, Discipline & Extra Curricular Activities							**25**	**0.5**
			Sub- Total	0	0	12		180	120	**325**	**6.5**
			TOTAL OF IV SEMEESTER	17	0	12		350	800	**1175**	**23.5**

L: *Lecture,* ***T:*** *Tutorial,* ***P:*** *Practical,* ***Cr:*** *Credits*

ETE: *End Term Exam,* ***IA:*** *Internal Assessment*

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BRANCH CODE	EC						
Course Code	ELECTRONICS AND COMMUNICATION ENGG		Hrs. / Week				
SEMESTER - V	Title	L	T	P	IA	Exam	Total
5EC1A	Signals & Systems	3	1		20	80	100
5EC2A	Linear Integrated Circuits	3	0		20	80	100
5EC3A	Telecommunication Engg.	3	0		20	80	100
5EC4A	Analog Communication	3	1		20	80	100
5EC5A	Microwave Engg. -I	3	0		20	80	100
5EC6A	Elective (any one of the following)						
5EC6.1A	Biomedical Instrumentation	3	0		20	80	100
5EC6.2A	Advanced Data Structures						
5EC6.3A	Computer Oriented Numerical & Statistical Methods						
	Total						600
5EC7A	Electronic Engineering Design Lab			2	45	30	75
5EC8A	Microwave Engg. Lab			2	45	30	75
5EC9A	Communication Lab-I			2	45	30	75
5EC10A	Signal Processing Lab			2	45	30	75
5EC11A	Professoinal Ethics and Disaster Management			2	30	20	50
5ECDCA	Discipline & Extra Curricular Activity						50
	Total						400
	Grand Total	18	2	10			1000

BRANCH CODE	EC						
Course Code	ELECTRONICS AND COMMUNICATION ENGG		Hrs. / Week				
SEMESTER - VI	Title	L	T	P	IA	Exam	Total
6EC1A	Microwave Engg.-II	3	1		20	80	100
6EC2A	Microprocessors	3	0		20	80	100
6EC3A	Industrial Electronics	3	0		20	80	100
6EC4A	Digital Communication	3	1		20	80	100
6EC5A	Control Systems	3	0		20	80	100
6EC6A	Elective (any one of the following)						
6EC6.1A	Neural Networks	3	0		20	80	100
6EC6.2A	Parallel Computation & Architecture						
6EC6.3A	Optical Fiber Communication						
	Total						600
6EC7A	Communication Lab-II			3	45	30	75
6EC8A	Microprocessor Lab			2	45	30	75
6EC9A	RF Simulation Lab			3	45	30	75
6EC10A	Industrial Electronics Lab			2	45	30	75
6EC11A	Personality Development & General Aptitute			2	30	20	50
6ECDCA	Discipline & Extra Curricular Activity						50
	Total						400
	Grand Total	18	2	10			1000

BRANCH CODE	EC						
Course Code	ELECTRONICS AND COMMUNICATION ENGG		Hrs. / Week				
SEMESTER - VII		L	T	P	IA	Exam	Total
7EC1A	Antenna & Wave Propagation	3	0		20	80	100
7EC2A	Digital Signal Processing	3	1		20	80	100
7EC3A	Digital Image Processing	3	1		20	80	100
7EC4A	Wireless Communication	3	0		20	80	100
7EC5A	VLSI Design	3	0		20	80	100
7EC6A	Elective (any one of the following)						
7EC6.1A	Advanced Microprocessors	3	0		20	80	100
7EC6.2A	Artificial Intelligence and Expert Systems						
7EC6.3A	VHDL						
	Total						600
7EC7A	Signal & Image Processing Lab			3	60	40	100
7EC8A	Wireless Communication Lab			3	60	40	100
7EC9A	Practical Training & Industrial Visit			2	60	40	100
7EC10A	Project-I			2	50		50
7ECDCA	Discipline & Extra Curricular Activity						50
	Total						400
	Grand Total	18	2	10			1000

BRANCH CODE	EC						
Course Code	ELECTRONICS AND COMMUNICATION ENGG		Hrs. / Week				
SEMESTER - VIII		L	T	P	IA	Exam	Total
8EC1A	IC Technology	3	0		20	80	100
8EC2A	Radar & TV Engineering	3	1		20	80	100
8EC3A	MEMS and Nanotechnology	3	1		20	80	100
8EC4.1A	Computer Networks	3	0		20	80	100
8EC4.2A	Operating Systems						
8EC4.3A	Microcontroller and Embedded Systems						
	Total						400
8EC5A	RF Fabrication Lab			3	60	40	100
8EC6A	Industrial Economics & Management.			2	30	20	50
8EC7A	VLSI & Optical Fiber Lab			3	60	40	100
8EC8A	Project -II			4	120	80	200
8EC9A	Seminar			2	60	40	100
8ECDCA	Discipline & Extra Curricular Activity						50
	Total						600
	Grand Total	12	2	14			1000

Syllabus of

UNDERGRADUATE DEGREE COURSE

Electronics & Communication Engineering

Rajasthan Technical University, Kota

Effective from session: 2018 – 2019

3EC2-01: Advance Engineering Mathematics-I

3 Credits **Max. Marks: 150 (IA:30, ETE:120)**
3L:0T:0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Numerical Methods – 1:** Finite differences, Relation between operators, Interpolation using Newton's forward and backward difference formulae. Gauss's forward and backward interpolation formulae. Stirling's Formulae. Interpolation with unequal intervals: Newton's divided difference and Lagrange's formulae. Numerical Differentiation, Numerical integration: Trapezoidal rule and Simpson's 1/3rd and 3/8 rules.	**10**
2	**Numerical Methods – 2:** Numerical solution of ordinary differential equations: Taylor's series, Euler and modified Euler's methods. Runge- Kutta method of fourth order for solving first and second order equations. Milne's and Adam's predicator-corrector methods. Solution of polynomial and transcendental equations-Bisection method, Newton-Raphson method and Regula-Falsi method.	**8**
3	**Laplace Transform:** Definition and existence of Laplace transform, Properties of Laplace Transform and formulae, Unit Step function, Dirac Delta function, Heaviside function, Laplace transform of periodic functions. Finding inverse Laplace transform by different methods, convolution theorem. Evaluation of integrals by Laplace transform, solving ODEs by Laplace transforms method.	**10**
4	**Fourier Transform:** Fourier Complex, Sine and Cosine transform, properties and formulae, inverse Fourier transforms, Convolution theorem, application of Fourier transforms to partial ordinary differential equation (One dimensional heat and wave equations only).	**7**
5	**Z-Transform:** Definition, properties and formulae, Convolution theorem, inverse Z-transform, application of Z-transform to difference equation.	**5**
	Total	**40**

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3EC1-02/4EC1-02: Technical Communication

2 Credit **Max. Marks: 100 (IA:20, ETE:80)**
2L:0T:0P **End Term Exam: 2 Hours**

SN	Contents	Hours
1	**Introduction to Technical Communication-** Definition of technical communication, Aspects of technical communication, forms of technical communication, importance of technical communication, technical communication skills (Listening, speaking, writing, reading writing), linguistic ability, style in technical communication.	4
2	**Comprehension of Technical Materials/Texts and Information Design & development-** Reading of technical texts, Readingand comprehending instructions and technical manuals, Interpreting and summarizing technical texts, Note-making. Introduction of different kinds of technical documents, Information collection, factors affecting information and document design, Strategies for organization, Information design and writing for print and online media.	6
3	**Technical Writing, Grammar and Editing**- Technical writing process, forms of technical discourse, Writing, drafts and revising, Basics of grammar, common error in writing and speaking, Study of advanced grammar, Editing strategies to achieve appropriate technical style, Introduction to advanced technical communication. Planning, drafting and writing Official Notes, Letters, E-mail, Resume, Job Application, Minutes of Meetings.	8
4	**Advanced Technical Writing**- Technical Reports, types of technical reports, Characteristics and formats and structure of technical reports. Technical Project Proposals, types of technical proposals, Characteristics and formats and structure of technical proposals. Technical Articles, types of technical articles, Writing strategies, structure and formats of technical articles.	8
	Total	**26**

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3EC1-03/4EC1-03: Managerial Economics And Financial Accounting

2 Credit **Max. Marks: 100 (IA:20, ETE:80)**

2L:0T:0P **End Term Exam: 2 Hours**

SN	Contents	Hours
1	Basic economic concepts- Meaning, nature and scope of economics, deductive vs inductive methods, static and dynamics, Economic problems: scarcity and choice, circular flow of economic activity, national income-concepts and measurement.	4
2	Demand and Supply analysis- Demand-types of demand, determinants of demand, demand function, elasticity of demand, demand forecasting –purpose, determinants and methods, Supply-determinants of supply, supply function, elasticity of supply.	5
3	Production and Cost analysis- Theory of production- production function, law of variable proportions, laws of returns to scale, production optimization, least cost combination of inputs, isoquants. Cost concepts-explicit and implicit cost, fixed and variable cost, opportunity cost, sunk costs, cost function, cost curves, cost and output decisions, cost estimation.	5
4	Market structure and pricing theory- Perfect competition, Monopoly, Monopolistic competition, Oligopoly.	4
5	Financial statement analysis- Balance sheet and related concepts, profit and loss statement and related concepts, financial ratio analysis, cash-flow analysis, funds-flow analysis, comparative financial statement, analysis and interpretation of financial statements, capital budgeting techniques.	8
	Total	**26**

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3EC4-04: Digital System Design

3 Credits **Max. Marks: 150 (IA:30, ETE:120)**
3L:0T:0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Logic Simplification and Combinational Logic Design: Review of Boolean Algebra and De Morgan's Theorem, SOP & POS forms, Canonical forms, Karnaugh maps up to 6 variables, Binary codes, Code Conversion.	7
2	MSI devices like Comparators, Multiplexers, Encoder, Decoder, Driver & Multiplexed Display, Half and Full Adders, Subtractors, Serial and Parallel Adders, BCD Adder, Barrel shifter and ALU	8
3	Sequential Logic Design: Building blocks like S-R, JK and Master-Slave JK FF, Edge triggered FF, Ripple and Synchronous counters, Shift registers, Finite state machines, Design of Synchronous FSM, Algorithmic State Machines charts. Designing synchronous circuits like Pulse train generator, Pseudo Random Binary Sequence generator, Clock generation.	9
4	Logic Families and Semiconductor Memories: TTL NAND gate, Specifications, Noise margin, Propagation delay, fan-in, fan-out, Tristate TTL, ECL, CMOS families and their interfacing, memory elements, Concept of Programmable logic devices like FPGA. Logic implementation using programmable devices.	8
5	VLSI Design flow: Design entry: Schematic, FSM & HDL, different modeling styles in VHDL, Data types and objects, Dataflow, Behavioral and Structural Modeling, Synthesis and Simulation VHDL constructs and codes for combinational and sequential circuits.	8
	Total	**40**

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Course Outcome:

Course Code	Course Name	Course Outcome	Details
3EC4-04	Digital System Design	CO 1	Develop the understanding of number system and its application in digital electronics.
		CO 2	Development and analysis of K-map to solve the Boolean function to the simplest form for the implementation of compact digital circuits.
		CO 3	Design various combinational and sequential circuits using various metrics: switching speed, throughput/latency, gate count and area, energy dissipation and power.
		CO 4	Understanding Interfacing between digital circuits and analog component using Analog to Digital Converter (ADC), Digital to Analog Converter (DAC) etc.
		CO 5	Design and implement semiconductor memories, programmable logic devices (PLDs) and field programmable gate arrays (FPGA) in digital electronics.

CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
3EC4-04 Digital System Design	CO 1	3	2	2	1		1						
	CO 2	3	2	3	2								
	CO 3	2	2	3	1	1							
	CO 4	3	2	1	1	1							
	CO 5	2	1	3	1	1							

3: Strongly **2: Moderate** **1: Weak**

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Lecture Plan:

Lecture No.	Content to be taught
Lecture 1	Zero Lecture
Lecture 2	Review of Boolean Algebra
Lecture 3	DeMorgan's Theorem, SOP & POS forms,
Lecture 4	Problem of SOP and POS forms of boolean functions.
Lecture 5	Simplification of karnaugh map up to 6 variables
Lecture 6	Simplification of karnaugh map up to 6 variables
Lecture 7	Simplification of karnaugh map up to 6 variables
Lecture 8	Binary codes and code conversion
Lecture 9	Binary codes and code conversion
Lecture 10	Encoder, Decoder
Lecture 11	Half and Full Adders, Subtractors, Serial and Parallel Adders
Lecture 12	BCD Adder, Barrel shifter
Lecture 13	S-R FF, edge triggered and level triggered
Lecture 14	D and J-K FF
Lecture 15	Master-Slave JK FF and T FF
Lecture 16	Ripple and Synchronous counters
Lecture 17	Other type of counters
Lecture 18	Shift registers, Finite state machines, Asynchronous FSM
Lecture 19	Design of synchronous FSM
Lecture 20	Design of synchronous FSM
Lecture 21	Design of synchronous FSM
Lecture 22	Designing synchronous circuits (pulse train generator, pseudo random binary sequence generator, clock generation)

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Lecture 23	TTL NAND gate, specifications, noise margin, propagation delay, fan-in, fan-out
Lecture 24	TTL NAND gate
Lecture 25	Tristate TTL, ECL
Lecture 26	CMOS families and their interfacing
Lecture 27	CMOS families and their interfacing
Lecture 28	Read-Only Memory, Random Access Memory
Lecture 29	Programmable Logic Arrays (PLA)
Lecture 30	Programmable Array Logic (PAL),
Lecture 31	Field Programmable Gate Array (FPGA)
Lecture 32	Combinational PLD-Based State Machines,
Lecture 33	State Machines on a Chip
Lecture 34	Schematic, FSM & HDL
Lecture 35	Different modeling styles in VHDL
Lecture 36	Data types and objects, Data flow
Lecture 37	Behavioral and Structural Modeling
Lecture 38	Behavioral and Structural Modeling
Lecture 39	Simulation VHDL constructs and codes for combinational and sequential circuits
Lecture 40	Simulation VHDL constructs and codes for combinational and sequential circuits

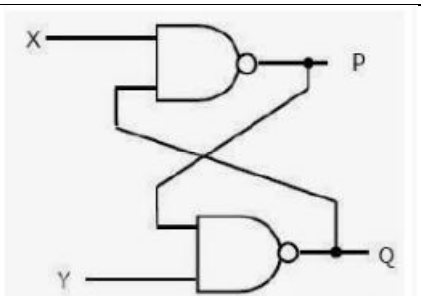
Content delivery method:

1. Chalk and Duster
2. PPT
3. Hand-outs

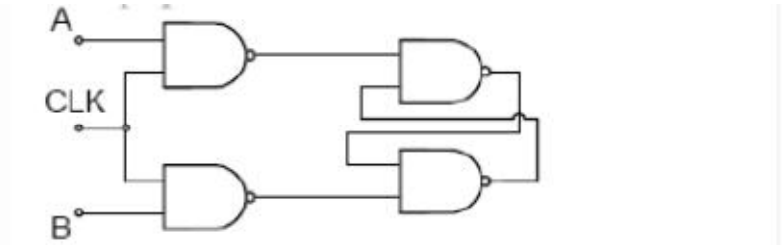
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Sample Assignments:

Assignment 1	**Q1.** Using K-maps, find the minimal Boolean expression of the following SOP and POS representations. a. f (w,x,y,z)= Σ (7,13,14,15) b. f(w,x,y,z) = Σ (1,3,4,6,9,11,14,15) c. f(w,x,y,z) = Π(1,4,5,6,11,12,13,14,15) d. f(w,x,y,z) = Σ (1,3,4,5,7,8,9,11,15) e. f(w,x,y,z) = Π (0,4,5,7,8,9,13,15)
	Q2. Find the function h(a,b,c,d) such that f = f^d. f (a,b,c,d) = a ·b· c + (a · c + b)· d + h(a,b,c,d)
	Q3. Using K-maps of the functions f1 and f2, find the following: (provide the canonical form expression and simplify) a. T1 = f1 · f2 b. T2 = f1 + f2 c. T3 = f1 ⊕ f2 where f1(w,x,y,z) = Σ (0,2,4,9,12,15), f2(w,x,y,z) = Σ (1,2,4,5,12,13)
Assignment 2	**Q1**. Draw the state diagram of a serial adder.
	Q2. In the following circuit, given binary values were applied to the Inputs X and Y inputs of the NAND latch shown in the figure. X = 0, Y = 1; X = 0, Y = 0; X = 1, Y = 1. Find out the corresponding stable output P, Q.



Q3. When the race around condition will occur in the circuit given Below:



3EC4-05: Signals & Systems

3 Credits **Max. Marks: 150 (IA:30, ETE:120)**

3L:0T:0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Energy and power signals, continuous and discrete time signals, continuous and discrete amplitude signals. System properties: linearity: additivity and homogeneity, shift-invariance, causality, stability, realizability.	6
2	Linear shift-invariant (LSI) systems, impulse response and step response, convolution, input output behavior with aperiodic convergent inputs. Characterization of causality and stability of linear shift-invariant systems. System representation through differential equations and difference equations	7
3	Periodic and semi-periodic inputs to an LSI system, the notion of a frequency response and its relation to the impulse response, Fourier series representation, the Fourier Transform, convolution/multiplication and their effect in the frequency domain, magnitude and phase response, Fourier domain duality. The Discrete-Time Fourier Transform (DTFT) and the Discrete Fourier Transform (DFT). Parseval's Theorem. The idea of signal space and orthogonal bases	8
4	The Laplace Transform, notion ofeigen functions of LSI systems, a basis of eigen functions, region of convergence, poles and zeros of system, Laplace domain analysis, solution to differential equations and system behavior.	6
5	The z-Transform for discrete time signals and systems- eigen functions, region of convergence, z-domain analysis.	5
6	State-space analysis and multi-input, multi-output representation. The state-transition matrix and its role. The Sampling Theorem and its implications- Spectra of sampled signals. Reconstruction: ideal interpolator, zero-order hold, first-order hold, and so on. Aliasing and its effects. Relation between continuous and discrete time systems.	8
	Total	40

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Course Outcome:

Course Code	Course Name	Course Outcome	Details
3EC4-05	Signals & Systems	CO 1	Analyze different types of signals and system properties
		CO 2	Represent continuous and discrete systems in time and frequency domain using different transforms
		CO 3	Investigate whether the system is stable.
		CO 4	Sampling and reconstruction of a signal.
		CO 5	Acquire an understanding of MIMO systems

CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
3EC4-05 Signals & Systems	CO 1	3	3	1	2	2			1				2
	CO 2	3	1		2	3			1				2
	CO 3	3	2	2	3								2
	CO 4	3	2	3	3	1							
	CO 5	3	2	2	3	1			2				1

3: Strongly 2: Moderate 1: Weak

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Lecture Plan:

Lecture No.	Content to be taught
Lecture 1	Zero Lecture
Lecture 2	Energy signals power signals
Lecture 3	Continuous and discrete time signals
Lecture 4	Continuous amplitude signals
Lecture 5	and discrete amplitude signals
Lecture 6	System properties: linearity: additivity and homogeneity
Lecture 7	shift-invariance, causality
Lecture 8	stability, realizability.
Lecture 9	Linear shift-invariant (LSI) systems
Lecture 10	impulse response
Lecture 11	Step response
Lecture 12	Convolution.
Lecture 13	Input output behavior with aperiodic convergent inputs
Lecture 14	Characterization of causality and stability of linear shift-invariant systems.
Lecture 15	System representation through differential equations and difference equations.
Lecture 16	Characterization of causality and stability of linear shift-invariant systems.
Lecture 17	System representation through differential equations and difference equations.
Lecture 18	Periodic and semi-periodic inputs to an LSI system
Lecture 19	The notion of a frequency response.
Lecture 20	Its relation to the impulse response
Lecture 21	Fourier series representation
Lecture 22	Fourier Transform
Lecture 23	Convolution/multiplication and their effect in the frequency domain
Lecture 24	Magnitude and phase response
Lecture 25	Fourier domain duality.
Lecture 26	The Discrete-Time Fourier Transform (DTFT) and Discrete Fourier Transform (DFT).
Lecture 27	Parseval's Theorem. The idea of signal space and orthogonal bases
Lecture 28	The Laplace Transform
Lecture 29	Notion of eigen functions of LSI systems

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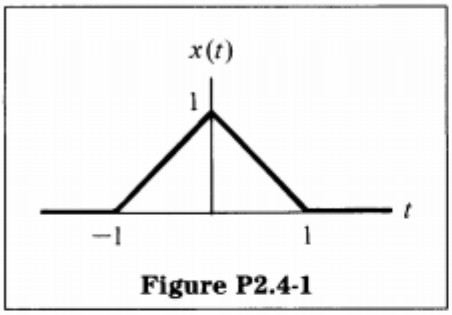
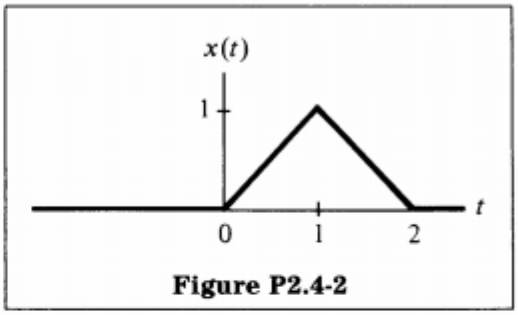
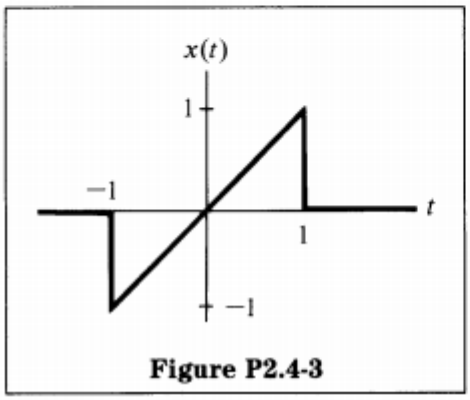
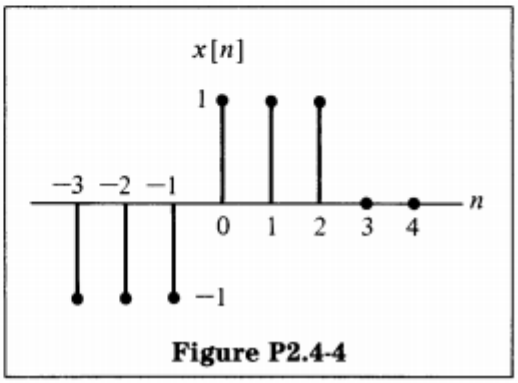
Lecture 30	A basis of eigen functions, region of convergence
Lecture 31	Poles and zeros of system, Laplace domain analysis,
Lecture 32	Solution to differential equations and system behavior.
Lecture 33	The z-Transform for discrete time signals and systems- eigen functions,
Lecture 34	Region of convergence, z-domain analysis.
Lecture 35	State-space analysis and multi-input, multi-output representation.
Lecture 36	The state-transition matrix and its role.
Lecture 37	The Sampling Theorem and its implications- Spectra of sampled signals.
Lecture 38	Reconstruction: ideal interpolator, zero-order hold, first-order hold, and so on
Lecture 39	Aliasing and its effects.
Lecture 40	Relation between continuous and discrete time systems.

Content delivery method:

1. Chalk and Duster
2. PPT
3. Animation
4. Hand-outs

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Assignments:

Assignment	For each of the following signals, determine whether it is even, odd, or neither. **(a)**  **Figure P2.4-1** **(b)**  **Figure P2.4-2** **(c)**  **Figure P2.4-3** **(d)**  **Figure P2.4-4**

Evaluate the following sums:

(a) $\sum_{n=0}^{5} 2\left(\frac{3}{a}\right)^n$

(b) $\sum_{n=2}^{6} b^n$

(c) $\sum_{n=0}^{\infty} \left(\frac{2}{3}\right)^{2n}$

Hint: Convert each sum to the form

$$C\sum_{n=0}^{N-1} \alpha^n = S_N \quad \text{or} \quad C\sum_{n=0}^{\infty} \alpha^n = S_\infty$$

and use the formulas

$$S_N = C\left(\frac{1-\alpha^N}{1-\alpha}\right), \quad S_\infty = \frac{C}{1-\alpha} \quad \text{for } |\alpha| < 1$$

The first-order difference equation $y[n] - ay[n-1] = x[n]$, $0 < a < 1$, describes a particular discrete-time system initially at rest.

(a) Verify that the impulse response $h[n]$ for this system is $h[n] = a^n u[n]$.

(b) Is the system

(i) memoryless?

(ii) causal?

(iii) stable?

Clearly state your reasoning.

(c) Is this system stable if $|a| > 1$?

Assignment

Consider a discrete-time system with impulse response

$$h[n] = (\tfrac{1}{2})^n u[n]$$

Determine the response to each of the following inputs:

(a) $x[n] = (-1)^n = e^{j\pi n}$ for all n

(b) $x[n] = e^{j(\pi n/4)}$ for all n

(c) $x[n] = \cos\left(\frac{\pi n}{4} + \frac{\pi}{8}\right)$ for all n

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Consider two specific periodic sequences $\tilde{x}[n]$ and $\tilde{y}[n]$. $\tilde{x}[n]$ has period N and $\tilde{y}[n]$ has period M. The sequence $\tilde{w}[n]$ is defined as $\tilde{w}[n] = \tilde{x}[n] + \tilde{y}[n]$.

(a) Show that $\tilde{w}[n]$ is periodic with period MN.

(b) Since $\tilde{x}[n]$ has period N, its discrete Fourier series coefficients a_k also have period N. Similarly, since $\tilde{y}[n]$ has period M, its discrete Fourier series coefficients b_k also have period M. The discrete Fourier series coefficients of $\tilde{w}[n]$, c_k, have period MN. Determine c_k in terms of a_k and b_k.

The sequence $x[n] = (-1)^n$ is obtained by sampling the continuous-time sinusoidal signal $x(t) = \cos \omega_0 t$ at 1-ms intervals, i.e.,

$$\cos(\omega_0 nT) = (-1)^n, \qquad T = 10^{-3}\ \text{s}$$

Determine three *distinct* possible values of ω_0.

3EC4-06: Network Theory

4 Credits **Max. Marks: 200 (IA:40, ETE:160)**

3L:1T:0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Node and Mesh Analysis, matrix approach of network containing voltage and current sources, and reactances, source transformation and duality.	7
2	Network theorems: Superposition, reciprocity, Thevenin's, Norton's, Maximum power Transfer, compensation and Tallegen's theorem as applied to AC. circuits.	7
3	Trigonometric and exponential Fourier series: Discrete spectra and symmetry of waveform, steady state response of a network to non-sinusoidal periodic inputs, power factor, effective values, Fourier transform and continuous spectra, three phase unbalanced circuit and power calculation.	8
4	Laplace transforms and properties: Partial fractions, singularity functions, waveform synthesis, analysis of RC, RL, and RLC networks with and without initial conditions with Laplace transforms evaluation of initial conditions..	8
5	Transient behavior, concept of complex frequency, Driving points and transfer functions poles and zeros of immittance function, their properties, sinusoidal response from pole-zero locations, convolution theorem and Two four port network and interconnections, Behaviors of series and parallel resonant circuits, Introduction to band pass, low pass, high pass and band reject filters.	10
	Total	40

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Course Outcome:

Course Code	Course Name	Course Outcome	Details
3EC4-06	Network Theory	CO 1	Apply the basic circuital law and simplify the network using network theorems
		CO 2	Appreciate the frequency domain techniques in different applications.
		CO 3	Apply Laplace Transform for steady state and transient analysis
		CO 4	Evaluate transient response and two-port network parameters
		CO 5	Analyze the series resonant and parallel resonant circuit and design filters

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CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
3EC4-06 Network Theory	CO 1	3	2		3	2							
	CO 2	3	3	1	2	2							1
	CO 3	3	2	2		2							1
	CO 4	2	3	2	2	1							
	CO 5	2	3	3	2	1							

3: Strongly 2: Moderate 1: Weak

Lecture Plan:

Lecture No.	Content to be taught
Lecture 1	Overview of Network Theory and its significance
Lecture 2	Node and Mesh Analysis
Lecture 3	matrix approach of network containing voltage and current sources and reactances
Lecture 4	source transformation and duality
Lecture 5	Network theorems: Superposition and reciprocity
Lecture 6	Thevenin's and Norton's theorem
Lecture 7	Maximum power Transfer theorem
Lecture 8	compensation and Tallegen's theorem as applied to AC. Circuits
Lecture 9	Trigonometric and exponential Fourier series
Lecture 10	Fourier series: Discrete spectra and symmetry of waveform
Lecture 11	Steady state response of a network to non-sinusoidal periodic inputs
Lecture 12	power factor and effective values
Lecture 13	Fourier transform and continuous spectra
Lecture 14	three phase unbalanced circuit and power calculation
Lecture 15	three phase unbalanced circuit and power calculation
Lecture 16	Laplace transforms
Lecture 17	Laplace transforms
Lecture 18	Laplace transforms properties: Partial fractions
Lecture 19	singularity functions and waveform synthesis

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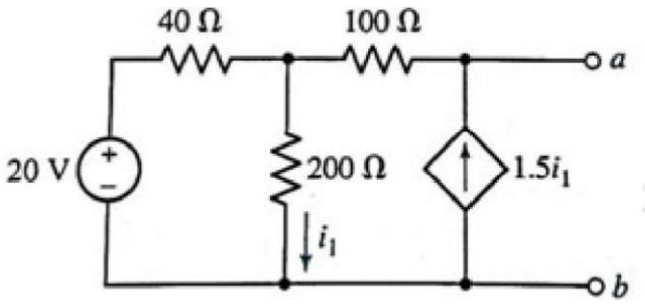
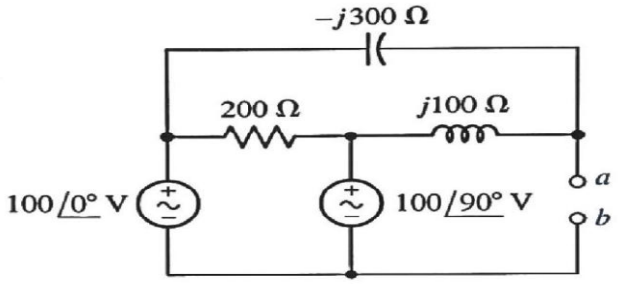
Lecture 20	analysis of RC networks
Lecture 21	analysis of RL networks
Lecture 22	analysis of RLC networks
Lecture 23	Analysis of networks with and without initial conditions
Lecture 24	Analysis of networks with and without initial conditions
Lecture 25	Analysis of networks with and without initial conditions with lapalace transforms evaluation
Lecture 26	Analysis of networks with and without initial conditions with lapalace transforms evaluation of initial condition
Lecture 27	Transient behavior
Lecture 28	concept of complex frequency
Lecture 29	Driving points and transfer functions poles and zeros of immittance function
Lecture 30	Driving points and transfer functions poles and zeros of immittance function: their properties
Lecture 31	sinusoidal response from pole-zero locations
Lecture 32	sinusoidal response from pole-zero locations
Lecture 33	convolution theorem
Lecture 34	sinusoidal response from pole-zero locations
Lecture 35	Two four port network and interconnections
Lecture 36	Two four port network and interconnections
Lecture 37	Behaviors of series and parallel resonant circuits
Lecture 38	Introduction to band pass and low pass
Lecture 39	Introduction to high pass and reject filters
Lecture 40	Spill over class

Content delivery method:

1. Chalk and Duster
2. PPT
3. Hand-outs

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Sample assignments:

Assignment 1	**Q1.**	Elaborate the significance of source transformation with relevant example
	Q2.	State and prove time differentiation theorem in Laplace Transform
	Q3.	Find the Thevenin equivalent of the network shown in figure. What power would be delivered to a load of 100 ohms at *a* and *b*? 
Assignment 2	**Q4.**	Calculate Thevenin equivalent circuit with respect to terminals *a* and *b* 
	Q5.	Derive transient current and voltage responses of sinusoidal driven RL and RC circuits.
	Q6.	Specify the restrictions on pole and zero locations for transfer functions and driving-point functions.

3EC4-07: Electronic Devices

4 Credits **Max. Marks: 200 (IA:40, ETE:160)**

3L:1T:0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	Introduction to Semiconductor Physics: Introduction, Energy band gap structures of semiconductors, Classifications of semiconductors, Degenerate and non-degenerate semiconductors, Direct and indirect band gap semiconductors, Electronic properties of Silicon, Germanium, Compound Semiconductor, Gallium Arsenide, Gallium phosphide & Silicon carbide, Variation of semiconductor conductivity, resistance and bandgap with temperature and doping. Thermistors, Sensitors.	6
2	Review of Quantum Mechanics, Electrons in periodic Lattices, E-k diagrams. Energy bands in intrinsic and extrinsic silicon; Carrier transport: diffusion current, drift current, mobility and resistivity; sheet resistance, design of resistors.	6
3	Generation and recombination of carriers; Poisson and continuity equation P-N junction characteristics, I-V characteristics, and small signal switching models; Avalanche breakdown, Zener diode, Schottky diode.	8
4	Bipolar Junction Transistor, I-V characteristics, Ebers-Moll Model, MOS capacitor, C-V characteristics, MOSFET, I-V characteristics, and small signal models of MOS transistor, LED, photodiode and solar cell.	11
5	Integrated circuit fabrication process: oxidation, diffusion, ion implantation, Photolithography, etching, chemical vapor deposition, sputtering, twin-tub CMOS process.	9
	Total	**40**

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Course Outcome:

Course Code	Course Name	Course Outcome	Details
3EC4-07	Electronic Devices	CO 1	Understanding the semiconductor physics of the intrinsic, P and N materials.
		CO 2	Understanding the characteristics of current flow in a bipolar junction transistor and MOSFET.
		CO 3	Understand and utilize the mathematical models of semiconductor junctions and MOS transistors for circuits and systems.
		CO 4	Analyze the characteristics of different electronic devices such as Amplifiers, LEDs, Solar cells, etc.
		CO 5	Theoretical as well as experimental understanding of Integrated circuit fabrication.

CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
3EC4-07 Electronic Devices	CO 1	3	1		2	1	1						
	CO 2	3	2	1			2						
	CO 3	2	1		2		1	2					
	CO 4	3	1	1				2					
	CO 5	3	1	1	1	1							2

3: Strongly **2: Moderate** **1: Weak**

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Lecture Plan:

Lecture No.	Content to be taught
Lecture 1	Zero Lecture
Lecture 2	Introduction to Semiconductor Physics
Lecture 3	Introduction to Semiconductor Physics
Lecture 4	Introduction to Semiconductor Physics
Lecture 5	Review of Quantum Mechanics
Lecture 6	Electrons in periodic Lattices
Lecture 7	E-k diagrams
Lecture 8	Energy bands in intrinsic and extrinsic silicon
Lecture 9	Carrier transport: diffusion current, drift current, mobility and resistivity
Lecture 10	Sheet resistance and design of resistors
Lecture 11	Generation and recombination of carriers
Lecture 12	Poisson and continuity equation
Lecture 13	P-N junction characteristics and their I-V characteristics
Lecture 14	P-N junction characteristics and their I-V characteristics
Lecture 15	P-N junction small signal switching models
Lecture 16	P-N junction small signal switching models
Lecture 17	Avalanche breakdown
Lecture 18	Zener diode and Schottky diode
Lecture 19	Basics of Bipolar Junction Transistor
Lecture 20	I-V characteristics of BJT
Lecture 21	Ebers-Moll Model
Lecture 22	MOS capacitor
Lecture 23	MOS capacitor

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Lecture 24	C-V characteristics
Lecture 25	Basics of MOSFET
Lecture 26	Basics of MOSFET
Lecture 27	I-V characteristics of MOSFET
Lecture 28	Small signal models of MOS transistor
Lecture 29	Small signal models of MOS transistor
Lecture 30	Light Emitting Diode
Lecture 31	Photodiode and solar cell
Lecture 32	Basics of Integrated Circuits
Lecture 33	Advancement in Integrated Circuits
Lecture 34	Oxidation, diffusion and ion implantation
Lecture 35	Photolithography and etching
Lecture 36	Chemical vapor deposition
Lecture 37	Sputtering
Lecture 38	Twin-tub CMOS process
Lecture 39	Spill over class
Lecture 40	Spill over class

Content delivery method:

1. Chalk and Duster
2. PPT
3. Hand-outs

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Sample assignments:

Assignment 1	**Q1.**	Investigates the input/output characteristics of various diodes?
	Q2.	Investigate the applications of various diodes?
	Q3.	A p-type sample of silicon has a resistivity of 5 Ω-cm. In this sample, the hole mobility, μ_h, is 600 cm^2/V-s and the electron mobility, μ_e, is 1600 cm^2/V-s. Ohmic contacts are formed on the ends of the sample and a uniform electric field is imposedwhich results in a drift current density in the sample is 2 x 10^3 A/cm^2. [1]. What are the hole and electron concentrations in this sample? [2]. What are the hole and electron drift velocities under these conditions? [3]. What is the magnitude of the electric field?
Assignment 2	**Q1.**	Discuss the applications of Ebers-Moll Model.
	Q2.	Discuss different types of fabrication techniques.
	Q3.	Discuss various characteristics of CMOS transistor.

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3EC4-21: Electronics Devices Lab

1 Credit **Max. Marks: 50 (IA:30, ETE:20)**

0L:0T:2P

List of Experiments

Sr. No.	Name of Experiment
1.	Study the following devices: (a) Analog& digital multimeters (b) Function/ Signal generators (c) Regulated d. c. power supplies (constant voltage and constant current operations) (d) Study of analog and digital CRO, measurement of time period, amplitude, frequency & phase angle using Lissajous figures.
2.	Plot V-I characteristic of P-N junction diode & calculate cut-in voltage, reverse Saturation current and static & dynamic resistances.
3.	Plot the output waveform of half wave rectifier and effect of filters on waveform. Also calculate its ripple factor.
4.	Study bridge rectifier and measure the effect of filter network on D.C. voltage output & ripple factor.
5.	Plot and verify output waveforms of different clipper and clamper.
6.	Plot V-I characteristic of Zener diode
7.	Study of Zener diode as voltage regulator. Observe the effect of load changes and determine load limits of the voltage regulator
8.	Plot input-output characteristics of BJT in CB, CC and CE configurations. Find their h-parameters.
9.	Study of different biasing circuits of BJT amplifier and calculate its Q-point.
10.	Plot frequency response of two stage RC coupled amplifier & calculate its bandwidth .
11.	Plot input-output characteristics of field effect transistor and measure I_{dss} and V_p.
12.	Plot frequency response curve for FET amplifier and calculate its gain bandwidth product.

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Course Outcome:

Course Code	Course Name	Course Outcome	Details
3EC4-21	Electronic Devices Lab	CO 1	Understand the characteristics of different Electronic Devices.
		CO 2	Verify the rectifier circuits using diodes and implement them using hardware.
		CO 3	Design various amplifiers like CE, CC, common source amplifiers and implement them using hardware and also observe their frequency responses
		CO 4	Understand the construction, operation and characteristics of JFET and MOSFET, which can be used in the design of amplifiers.
		CO 5	Understand the need and requirements to obtain frequency response from a transistor so that Design of RF amplifiers and other high frequency amplifiers is feasible

CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
3EC4-21 Electronic Devices Lab	CO 1	3	2	3	2	1							1
	CO 2	2	3	1	3	3							2
	CO 3	2	1	2	3	3							
	CO 4	3	2	3	2	2							1
	CO 5	3	2	1	2	2							

3: Strongly **2: Moderate** **1: Weak**

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3EC4-22: Digital System Design Lab

1 Credit **Max. Marks: 50 (IA:30, ETE:20)**

0L:0T:2P

List of Experiments

S.No.	Name of Experiment
Part A: Combinational Circuits	
1.	To verify the truth tables of logic gates: AND, OR, NOR, NAND, NOR, Ex-OR and Ex-NOR
2.	To verify the truth table of OR, AND, NOR, Ex-OR, Ex-NOR logic gates realized using NAND & NOR gates.
3.	To realize an SOP and POS expression.
4.	To realize Half adder/ Subtractor& Full Adder/ Subtractor using NAND & NOR gates and to verify their truth tables
5.	To realize a 4-bit ripple adder/ Subtractor using basic Half adder/ Subtractor& basic Full Adder/ Subtractor.
6.	To design 4-to-1 multiplexer using basic gates and verify the truth table. Also verify the truth table of 8-to-1 multiplexer using IC
7.	To design 1-to-4 demultiplexer using basic gates and verify the truth table. Also to construct 1-to-8 demultiplexer using blocks of 1-to-4 demultiplexer
8.	To design 2x4 decoder using basic gates and verify the truth table. Also verify the truth table of 3x8 decoder using IC
9.	Design & Realize a combinational circuit that will accept a 2421 BCD code and drive a TIL -312 seven-segment display
Part B: Sequential Circuits	
10.	Using basic logic gates, realize the R-S, J-K and D-flip flops with and without clock signal and verify their truth table.
11.	Construct a divide by 2, 4 & 8 asynchronous counter. Construct a 4-bit binary counter and ring counter for a particular output pattern using D flip flop.
12.	Design and construct unidirectional shift register and verify the
13.	Design and construct BCD ripple counter and verify the function.
14.	Design and construct a 4 Bit Ring counter and verify the function
15.	Perform input/output operations on parallel in/Parallel out and Serial in/Serial out registers using clock. Also exercise loading only one of multiple values into the register using multiplexer.

Note: Minimum 6 experiments to be conducted from **Part-A**& 4 experiments to be conducted from **Part-B.**

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Course Outcome:

Course Code	Course Name	Course Outcome	Details
3EC4-22	Digital System Design Lab	CO 1	
		CO 2	To minimize the complexity of digital logic circuits.
		CO 3	To design and analyse combinational logic circuits.
		CO 4	To design and analyse sequential logic circuits.
		CO 5	Able to implement applications of combinational & sequential logic circuits.

CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
3EC4-22 Digital System Design Lab	CO 1	3	3	1									1
	CO 2	3	3	2	1	1							1
	CO 3	3	3	3	2	3	1						2
	CO 4	3	3	3	2	3	1						2
	CO 5	3	3	3	3	3	3						3

3: Strongly 2: Moderate 1: Weak

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3EC4-23: Signal Processing Lab

1 Credit **Max. Marks: 50 (IA:30, ETE:20)**

0L:0T:2P

List of Experiments

Sr. No.	Name of Experiment (Simulate using MATLAB environment)
1.	Generation of continuous and discrete elementary signals (periodic and non periodic) using mathematical expression.
2.	Generation of Continuous and Discrete Unit Step Signal.
3.	Generation of Exponential and Ramp signals in Continuous & Discrete domain.
4.	Continuous and discrete time Convolution (using basic definition).
5.	Adding and subtracting two given signals. (Continuous as well as Discrete signals)
6.	To generate uniform random numbers between (0, 1).
7.	To generate a random binary wave.
8.	To generate and verify random sequences with arbitrary distributions, means and variances for following: (a) Rayleigh distribution (b) Normal distributions: N(0,1). (c) Gaussion distributions: N (m, x)
9.	To plot the probability density functions. Find mean and variance for the above distributions

Course Outcome:

Course Code	Course Name	Course Outcome	Details
3EC4-23	Signal Processing Lab	CO 1	Able to generate different Continuous and Discrete time signals.
		CO 2	Understand the basics of signals and different operations on signals.
		CO 3	Develop simple algorithms for signal processing and test them using MATLAB
		CO 4	Able to generate the random signals having different distributions, mean and variance.
		CO 5	Design and conduct experiments, interpret and analyse data and report results.

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CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
3EC4-23 Signal Processing Lab	CO 1	2		1		2							
	CO 2	3		1									
	CO 3	1	2	3	1	3							
	CO 4	2	1	1		2							
	CO 5	1	1	2	2	2							

3: Strongly **2: Moderate** **1: Weak**

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3EC3-24: Computer Programming Lab-I

1 Credit **Max. Marks: 50 (IA:30, ETE:20)**
0L:0T:2P

1.	Write a simple C program on a 32 bit compiler to understand the concept of array storage, size of a word. The program shall be written illustrating the concept of row major and column major storage. Find the address of element and verify it with the theoretical value. Program may be written for arrays upto 4-dimensions.
2.	Simulate a stack, queue, circular queue and dequeue using a one dimensional array as storage element. The program should implement the basic addition, deletion and traversal operations.
3.	Represent a 2-variable polynomial using array. Use this representation to implement addition of polynomials.
4.	Represent a sparse matrix using array. Implement addition and transposition operations using the representation.
5.	Implement singly, doubly and circularly connected linked lists illustrating operations like addition at different locations, deletion from specified locations and traversal.
6.	Repeat exercises 2, 3 & 4 with linked structures.
7.	Implementation of binary tree with operations like addition, deletion, traversal.
8.	Depth first and breadth first traversal of graphs represented using adjacency matrix and list.
9.	Implementation of binary search in arrays and on linked Binary Search Tree.
10.	Implementation of insertion, quick, heap, topological and bubble sorting algorithms.

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Syllabus of
UNDERGRADUATE DEGREE COURSE

Electronics & Communication Engineering



Rajasthan Technical University, Kota

Effective from session: 2018 – 2019

4EC2-01: Advance Engineering Mathematics-II

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Complex Variable – Differentiation:** Differentiation, Cauchy-Riemann equations, analytic functions, harmonic functions, finding harmonic conjugate; elementary analytic functions (exponential, trigonometric, logarithm) and their properties; Conformal mappings, Mobius transformations and their properties.	7
3	**Complex Variable - Integration:** Contour integrals, Cauchy-Goursat theorem (without proof), Cauchy Integral formula (without proof), Liouville's theorem and Maximum-Modulus theorem (without proof); Taylor's series, zeros of analytic functions, singularities, Laurent's series; Residues, Cauchy Residue theorem (without proof).	8
4	**Applications of complex integration by residues:** Evaluation of definite integral involving sine and cosine. Evaluation of certain improper integrals.	4
5	**Special Functions:** Legendre's function, Rodrigues formula, generating function, Simple recurrence relations, orthogonal property. Bessel's functions of first and second kind, generating function, simple recurrence relations, orthogonal property.	10
6	**Linear Algebra:** Vector Spaces, subspaces, Linear independence, basis and dimension, Inner product spaces, Orthogonality, Gram Schmidt orthogonalization, characteristic polynomial, minimal polynomial, positive definite matrices and canonical forms, QR decomposition.	10
	Total	40

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4EC1-03/3EC1-03: Managerial Economics And Financial Accounting

2 Credit **Max. Marks: 100 (IA:20, ETE:80)**
2L:0T:0P **End Term Exam: 2 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Basic economic concepts:** Meaning, nature and scope of economics, deductive vs inductive methods, static and dynamics, Economic problems: scarcity and choice, circular flow of economic activity, national income-concepts and measurement.	3
3	**Demand and Supply analysis:** Demand-types of demand, determinants of demand, demand function, elasticity of demand, demand forecasting –purpose, determinants and methods, Supply-determinants of supply, supply function, elasticity of supply.	5
4	**Production and Cost analysis:** Theory of production- production function, law of variable proportions, laws of returns to scale, production optimization, least cost combination of inputs, isoquants. Cost concepts-explicit and implicit cost, fixed and variable cost, opportunity cost, sunk costs, cost function, cost curves, cost and output decisions, cost estimation.	5
5	**Market structure and pricing theory:** Perfect competition, Monopoly, Monopolistic competition, Oligopoly.	4
6	**Financial statement analysis:** Balance sheet and related concepts, profit and loss statement and related concepts, financial ratio analysis, cash-flow analysis, funds-flow analysis, comparative financial statement, analysis and interpretation of financial statements, capital budgeting techniques.	8
	Total	26

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4EC1-02/3EC1-02: Technical Communication

2 Credit **Max. Marks: 100 (IA:20, ETE:80)**
2L:0T:0P **End Term Exam: 2 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Introduction to Technical Communication-** Definition of technical communication, Aspects of technical communication, forms of technical communication, importance of technical communication, technical communication skills (Listening, speaking, writing, reading writing), linguistic ability, style in technical communication.	3
3	**Comprehension of Technical Materials/Texts and Information Design & development-** Reading of technical texts, Readingand comprehending instructions and technical manuals, Interpreting and summarizing technical texts, Note-making. Introduction of different kinds of technical documents, Information collection, factors affecting information and document design, Strategies for organization, Information design and writing for print and online media.	6
4	**Technical Writing, Grammar and Editing-** Technical writing process, forms of technical discourse, Writing, drafts and revising, Basics of grammar, common error in writing and speaking, Study of advanced grammar, Editing strategies to achieve appropriate technical style, Introduction to advanced technical communication. Planning, drafting and writing Official Notes, Letters, E-mail, Resume, Job Application, Minutes of Meetings.	8
5	**Advanced Technical Writing**- Technical Reports, types of technical reports, Characteristics and formats and structure of technical reports. Technical Project Proposals, types of technical proposals, Characteristics and formats and structure of technical proposals. Technical Articles, types of technical articles, Writing strategies, structure and formats of technical articles.	8
	Total	26

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4EC4-04: Analog Circuits

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	Diode Circuits, Amplifier models: Voltage amplifier, current amplifier, trans-conductance amplifier and trans-resistance amplifier. Biasing schemes for BJT and FET amplifiers, bias stability, various configurations (such as CE/CS, CB/CG, CC/CD) and their features, small signal analysis, low frequency transistor models, estimation of voltage gain, input resistance, output resistance etc., design procedure for particular specifications, low frequency analysis of multistage amplifiers.	8
3	High frequency transistor models, frequency response of single stage and multistage amplifiers, cascode amplifier. Various classes of operation (Class A, B, AB, C etc.), their power efficiency and linearity issues. Feedback topologies: Voltage series, current series, voltage shunt, current shunt, effect of feedback on gain, bandwidth etc., calculation with practical circuits, concept of stability, gain margin and phase margin.	8
4	Oscillators: Review of the basic concept, Barkhausen criterion, RC oscillators (phase shift, Wien bridge etc.), LC oscillators (Hartley, Colpitt, Clapp etc.), non-sinusoidal oscillators. Current mirror: Basic topology and its variants, V-I characteristics, output resistance and minimum sustainable voltage (VON), maximum usable load. Differential amplifier: Basic structure and principle of operation, calculation of differential gain, common mode gain, CMRR and ICMR. OP-AMP design: design of differential amplifier for a given specification, design of gain stages and output stages, compensation.	8
5	OP-AMP applications: review of inverting and non-inverting amplifiers, integrator and differentiator, summing amplifier, precision rectifier, Schmitt trigger and its applications. Active filters: Low pass, high pass, band pass and band stop, design guidelines.	8
6	Digital-to-analog converters (DAC): Weighted resistor, R-2R ladder, resistor string etc. Analog to digital converters (ADC): Single slope, dual slope, successive approximation, flash etc. Switched capacitor circuits: Basic concept, practical configurations, application in amplifier, integrator, ADC etc.	7
	Total	40

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Course Outcome:

Course Code	Course Name	Course Outcome	Details
4EC4-04	Analog Circuits	CO 1	Understand the characteristics of diodes and transistors
		CO 2	Design and analyze various rectifier and amplifier circuits
		CO 3	Design sinusoidal and non-sinusoidal oscillators
		CO 4	Understand the functioning of OP-AMP and design OP-AMP based circuits
		CO 5	Understanding the designing of ADCs and DACs

CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
4EC4-04 Analog Circuits	CO 1	3		1	1	2							
	CO 2	1	1	2		1							
	CO 3	3	1		1								
	CO 4	2				2							
	CO 5	2	3		2								

3: Strongly **2: Moderate** **1: Weak**

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Lecture Plan:

Lecture No.	Content to be taught
Lecture 1	Zero Lecture
Lecture 2	Diode Circuits and Amplifier models
Lecture 3	Voltage amplifier, current amplifier, trans-conductance amplifier and trans-resistance amplifier
Lecture 4	Biasing schemes for BJT and FET amplifiers
Lecture 5	Bias stability in various configurations such as CE/CS, CB/CG, CC/CD
Lecture 6	Small signal analysis of BJT and FET
Lecture 7	low frequency transistor models
Lecture 8	Estimation of voltage gain, input resistance, output resistance etc.
Lecture 9	Design procedure for particular specifications, low frequency analysis of multistage amplifiers.
Lecture 10	High frequency transistor models
Lecture 11	frequency response of single stage and multistage amplifiers
Lecture 12	Cascode Amplifier
Lecture 13	Various classes of operation (Class A, B, AB, C etc.), their power efficiency and linearity issues
Lecture 14	Feedback topologies: Voltage series, current series, voltage shunt, current shunt
Lecture 15	Effect of feedback on gain, bandwidth etc.,
Lecture 16	Calculation with practical circuits
Lecture 17	Concept of stability, gain margin and phase margin.
Lecture 18	Basics of oscillator
Lecture 19	Barkhausen criterion, RC oscillators (phase shift, Wien bridge etc.)

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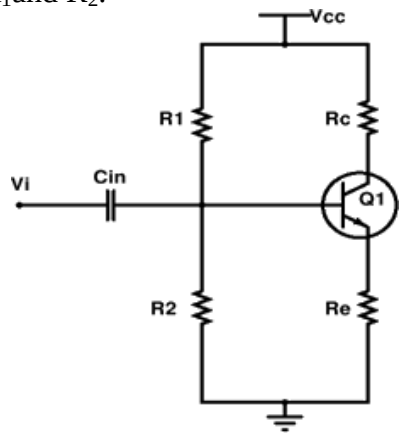
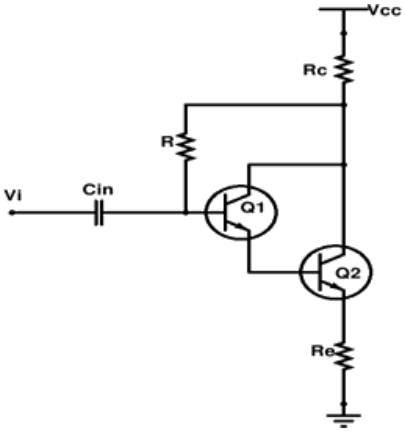
Lecture 20	LC oscillators (Hartley, Colpitt, Clapp etc.)
Lecture 21	Non-sinusoidal oscillators. Current mirror: Basic topology and its variants,
Lecture 22	V-I characteristics, output resistance and minimum sustainable voltage (VON), maximum usable load.
Lecture 23	Differential amplifier: Basic structure and principle of operation, calculation of differential gain, common mode gain, CMRR and ICMR.
Lecture 24	OP-AMP design: design of differential amplifier for a given specification
Lecture 25	Design of gain stages and output stages, compensation
Lecture 26	OP-AMP applications: review of inverting and non-inverting amplifiers
Lecture 27	Integrator and differentiator, summing amplifier
Lecture 28	Precision rectifier, Schmitt trigger and its applications
Lecture 29	Active filters: Low pass, high pass
Lecture 30	Band pass and band stop Filters
Lecture 31	Filter Design guidelines
Lecture 32	Digital-to-analog converters (DAC): Weighted resistor, R-2R ladder, resistor string etc
Lecture 33	Analog to digital converters (ADC): Single slope, dual slope
Lecture 34	successive approximation, flash TYPE ADC
Lecture 35	Switched capacitor circuits: Basic concept
Lecture 36	Switched capacitor circuits: practical configurations
Lecture 37	Switched capacitor circuits: applications
Lecture 38	Spill over classes
Lecture 39	Spill over classes
Lecture 40	Spill over classes

Content delivery method:

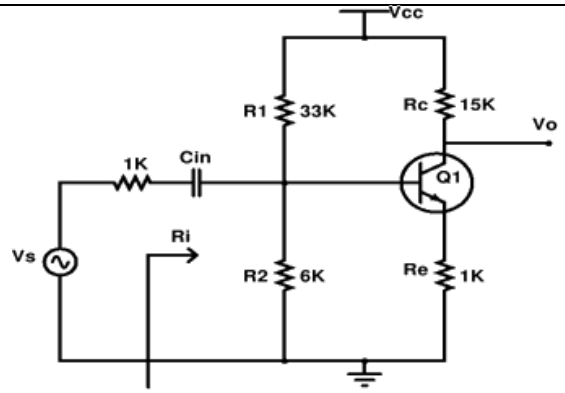
1. Chalk and Duster
2. PPT
3. Hand-outs

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Sample assignments:

Assignment 1	**Q1.** Assume that a silicon transistor with β =50, $V_{BEactive}$=0.7 V, V_{CC} =15V and R_C=10K is used in the Fig.1.It is desired to establish a Q-point at V_{CE}=7.5 V and I_C=5mA and stability factor S≤5.Find Re,$R_1$and R_2. 
	Q2. In the Darlington stage shown in Fig.2 , V_{CC}=15V , β_1=50, β_2=75,V_{BE}=0.7,R_C=750 Ω and R_E=100 Ω. If at the quiescent point V_{CE2}=6V determine the value of R. 
	Q3. For the amplifier shown in Fig.3 using a transistor whose parameters are h_{ie}=1100,h_{re}=2.5×10^{-4},h_{fe}=50,h_{oe}=24μA/V.Find A_I, A_V, A_{VS} and R_i.

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Assignment 2	**Q1.** Discuss the applications of operational amplifier.
	Q2. Discuss different types of filters.
	Q3. Discuss Dual counter type DAC and its applications

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4EC4-05: Microcontrollers

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	Overview of microcomputer systems and their building blocks, memory interfacing, concepts of interrupts and Direct Memory Access, instruction sets of microprocessors (with examples of 8085 and 8086);	10
3	Interfacing with peripherals - timer, serial I/O, parallel I/O, A/D and D/A converters; Arithmetic Coprocessors; System level interfacing design;	8
4	Concepts of virtual memory, Cache memory, Advanced coprocessor Architectures- 286, 486, Pentium; Microcontrollers: 8051 systems,	10
5	Introduction to RISC processors; ARM microcontrollers interface designs.	11
	Total	40

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Course Outcome:

Course Code	Course Name	Course Outcome	Details
4EC4-05	Microcontrollers	CO 1	Develop assembly language programming skills.
		CO 2	Able to build interfacing of peripherals like, I/O, A/D, D/A, timer etc.
		CO 3	Develop systems using different microcontrollers.
		CO 4	Explain the concept of memory organization.
		CO 5	Understand RSIC processors and design ARM microcontroller based systems.

CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
4EC04-05Microcontrollers	CO 1			3	1								
	CO 2			3		1							
	CO 3	1	2	3									
	CO 4	3	2	1									
	CO 5			3	2	1							

3: Strongly 2: Moderate 1: Weak

Lecture Plan:

Lecture No.	Content to be taught
Lecture 1	Zero Lecture
Lecture 2	Overview of microcomputer systems and their building blocks
Lecture 3	Overview of microcomputer systems and their building blocks
Lecture 4	Memory interfacing
Lecture 5	Memory interfacing
Lecture 6	Concepts of interrupts

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Lecture 7	Direct Memory Access
Lecture 8	Direct Memory Access
Lecture 9	Instruction sets of microprocessors (with examples of 8085 and 8086)
Lecture 10	Instruction sets of microprocessors (with examples of 8085 and 8086)
Lecture 11	Instruction sets of microprocessors (with examples of 8085 and 8086)
Lecture 12	Instruction sets of microprocessors (with examples of 8085 and 8086)
Lecture 13	Interfacing with peripherals
Lecture 14	Timer
Lecture 15	Serial I/O
Lecture 16	Parallel I/O
Lecture 17	A/D and D/A converters;
Lecture 18	A/D and D/A converters
Lecture 19	Arithmetic Coprocessors
Lecture 20	System level interfacing design
Lecture 21	Concepts of virtual memory, Cache memory
Lecture 22	Concepts of virtual memory, Cache memory
Lecture 23	Advanced coprocessor Architectures- 286, 486, Pentium
Lecture 24	Advanced coprocessor Architectures- 286, 486, Pentium
Lecture 25	Advanced coprocessor Architectures- 286, 486, Pentium
Lecture 26	Microcontrollers: 8051 systems,
Lecture 27	Microcontrollers: 8051 systems,
Lecture 28	Microcontrollers: 8051 systems,
Lecture 29	Microcontrollers: 8051 systems,
Lecture 30	Microcontrollers: 8051 systems,
Lecture 31	Introduction to RISC processors
Lecture 32	Introduction to RISC processors
Lecture 33	Introduction to RISC processors
Lecture 34	ARM microcontrollers interface designs
Lecture 35	ARM microcontrollers interface designs
Lecture 36	ARM microcontrollers interface designs
Lecture 37	ARM microcontrollers interface designs
Lecture 38	ARM microcontrollers interface designs
Lecture 39	Spill Over Classes
Lecture 40	Spill Over Classes

Content delivery method:

1. Chalk and Duster
2. PPT
3. Hand-outs

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Assignments:

Assignment 1	**Q1.** Compare between microprocessor & microcontroller based on no. of instructions used, registers, memory and applications.
	Q2. Interface external program memory with 8051 & explain how the data is transfer.
	Q3. List the I/O ports of microcontroller 8051. Explain their alternative function?
Assignment 2	**Q1.** Explain RISC and CISC?
	Q2. Without using MUL instruction, perform multiplication operation on any two operands, with both of them being: a. Positive numbers b. One positive and other negative number c. Both negative numbers Verify the values computed.
	Q3. Can you brief up the evolution of ARM architecture?

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4EC3-06: Electronics Measurement & Instrumentation

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**THEORY OF ERRORS** - Accuracy & precision, Repeatability, Limits of errors, Systematic & random errors, Modeling of errors, Probable error & standard deviation, Gaussian error analysis, Combination of errors.	8
3	**ELECTRONIC INSTRUMENTS** - Electronic Voltmeter, Electronic Multimeters, Digital Voltmeter, and Component Measuring Instruments: Q meter, Vector Impedance meter, RF Power & Voltage Measurements, Introduction to shielding & grounding.	8
4	**OSCILLOSCOPES** – CRT Construction, Basic CRO circuits, CRO Probes, Techniques of Measurement of frequency, Phase Angle and Time Delay, Multibeam, multi trace, storage & sampling Oscilloscopes.	7
5	**SIGNAL GENERATION AND SIGNAL ANALYSIS** - Sine wave generators, Frequency synthesized signal generators, Sweep frequency generators. Signal Analysis - Measurement Technique, Wave Analyzers, and Frequency - selective wave analyser, Heterodyne wave analyser, Harmonic distortion analyser, and Spectrum analyser.	8
6	**TRANSDUCERS** - Classification, Selection Criteria, Characteristics, Construction, Working Principles and Application of following Transducers:- RTD, Thermocouples, Thermistors, LVDT, Strain Gauges, Bourdon Tubes, Seismic Accelerometers, Tachogenerators, Load Cell, Piezoelectric Transducers, Ultrasonic Flow Meters.	8
	Total	40

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Course Outcome:

Course Code	Course Name	Course Outcome	Details
4EC3-06	ELECTRONIC MEASUREMENT & INSTRUMENTATION	CO 1	**Describe** the use of various electrical/electronic instruments, their block diagram, applications, dnd principles of operation, standards eorrs and units of measurements.
		CO 2	**Develop** basic skills in the design of electronic equipments
		CO 3	**Analyse** different electrical/electronic parameters using state of equipments of measuring instruments which is require to all types of industries.
		CO 4	**Solve :**Identify electronics/ electrical instruments, understanding associated with the instruments
		CO 5	**Explain** use of transducers in different types of field applications

CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
4EC3-06 ELECTRONIC MEASUREMENT & INSTRUMENTATION	CO 1	3	2	1									
	CO 2	2	2	2	3								
	CO 3	2	3										
	CO 4	2	1	1				2					
	CO 5	3	1										2

3: Strongly **2: Moderate** **1: Weak**

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Lecture Plan:

Lecture No.	Content to be taught
Lecture 1	Zero Lecture
Lecture 2	Theory of errors
Lecture 3	Accuracy & precision, Repeatability
Lecture 4	Limits of Time-Hours errors
Lecture 5	Systematic & random errors
Lecture 6	Modeling of errors
Lecture 7	Probable error
Lecture 8	standard deviation
Lecture 9	Gaussian error analysis
Lecture 10	Combination of errors
Lecture 11	Electronic instruments - Electronic Voltmeter
Lecture 12	Electronic Multimeters
Lecture 13	Digital Voltmeter
Lecture 14	Component Measuring Instruments: Q meter
Lecture 15	Vector Impedance meter
Lecture 16	RF Power & Voltage Measurements
Lecture 17	Introduction to shielding & grounding
Lecture 18	Oscilloscopes - CRT Construction
Lecture 19	Basic CRO circuits, CRO Probes
Lecture 20	Techniques of Measurement of frequency, Phase Angle and Time Delay
Lecture 21	Multibeam, multi trace, storage & sampling Oscilloscopes
Lecture 22	Multibeam, multi trace, storage & sampling Oscilloscopes
Lecture 23	Signal generation and signal analysis - Sine wave generators,
Lecture 24	Frequency synthesized signal generators
Lecture 25	Sweep frequency generators
Lecture 26	Signal Analysis - Measurement Technique
Lecture 27	Wave Analyzers, and Frequency - selective wave analyser
Lecture 28	Heterodyne wave analyser
Lecture 29	Harmonic distortion analyser
Lecture 30	Spectrum analyser
Lecture 31	Transducers – Classification
Lecture 32	Selection Criteria Characteristics
Lecture 33	Construction, Working Principles and Application of following Transducers:- RTD

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Lecture 34	Thermocouples
Lecture 35	Thermistors
Lecture 36	LVDT Strain Gauges, Bourdon Tubes
Lecture 37	Seismic Accelerometers
Lecture 38	Tachogenerators, Load Cell,
Lecture 39	Piezoelectric Transducers
Lecture 40	Ultrasonic Flow Meters

Content delivery method:

1. Chalk and Duster
2. PPT
3. Hand-outs

Sample assignments:

Assignment 1	**Q1.**	Write the principal of an AC Bridge used for the measurement of Unknown capacitor
	Q2.	Distinguish Between Accuracy and Precision?
	Q3.	Explain flow measurement with a suitable example.
Assignment 2	**Q1.**	What are primary sensing elements and transducers?
	Q2.	A Wheatstone Bridge requires to change of 7Ω in unknown arm of bridge to change in deflection of 14 mm. of galvanometer deter mine the sensitivity and deflection factor.
	Q3.	Explain the terms static error, static correction, relative error and percentage relative error.

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4EC4-07: Analog and Digital Communication

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**
3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	Review of signals and systems, Frequency domain representation of signals, Principles of Amplitude Modulation Systems- DSB, SSB and VSB modulations. Angle Modulation, Representation of FM and PM signals, Spectral characteristics of angle modulated signals.	8
3	Review of probability and random process. Gaussian and white noise characteristics, Noise in amplitude modulation systems, Noise in Frequency modulation systems. Pre-emphasis and Deemphasis, Threshold effect in angle modulation.	7
4	Pulse modulation. Sampling process. Pulse Amplitude and Pulse code modulation (PCM), Differential pulse code modulation. Delta modulation, Noise considerations in PCM, Time Division multiplexing, Digital Multiplexers.	8
5	Elements of Detection Theory, Optimum detection of signals in noise, Coherent communication with waveforms- Probability of Error evaluations. Baseband Pulse Transmission- Inter symbol Interference and Nyquist criterion. Pass band Digital Modulation schemes- Phase Shift Keying, Frequency Shift Keying, Quadrature Amplitude Modulation, Continuous Phase Modulation and Minimum Shift Keying.	8
6	Digital Modulation tradeoffs. Optimum demodulation of digital signals over band-limited channels- Maximum likelihood sequence detection (Viterbi receiver). Equalization Techniques. Synchronization and Carrier Recovery for Digital modulation.	8
	Total	40

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Course Outcome:

Course Code	Course Name	Course Outcome	Details
4EC4-07	Analog and Digital Communication	CO 1	Analyze and compare different analog modulation schemes for their efficiency and bandwidth
		CO 2	Analyze the behavior of a communication system in presence of noise
		CO 3	Investigate pulsed modulation system and analyze their system performance
		CO 4	Analyze different digital modulation schemes and can compute the bit error performance
		CO 5	Design a communication system comprised of both analog and digital modulation techniques

CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
4EC4-07 Analog & Digital Communication	CO 1	3	3		3		1				1		
	CO 2	3	2		3		1						
	CO 3	3	2		3		2						
	CO 4	3	3		3		2				1		
	CO 5	3	2	3	3		3			2	2		

3: Strongly 2: Moderate 1: Weak

Content delivery method:

1. Chalk and Duster
2. PPT

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Lecture Plan:

Lecture No.	Content to be taught
Lecture 1	Introduction to the COURSE
Lecture 2	Review of signals and systems, Frequency domain representation of signals
Lecture 3	Principles of Amplitude Modulation Systems- DSB, SSB and VSB modulations
Lecture 4	Principles of Amplitude Modulation Systems- DSB, SSB and VSB modulations
Lecture 5	Principles of Amplitude Modulation Systems- DSB, SSB and VSB modulations
Lecture 6	Angle Modulation, Representation of FM and PM signals
Lecture 7	Angle Modulation, Representation of FM and PM signals
Lecture 8	Spectral characteristics of angle modulated signals.
Lecture 9	Review of probability and random process
Lecture 10	Review of probability and random process
Lecture 11	Noise in amplitude modulation systems
Lecture 12	Noise in amplitude modulation systems
Lecture 13	Noise in Frequency modulation systems
Lecture 14	Pre-emphasis and Deemphasis
Lecture 15	Threshold effect in angle modulation
Lecture 16	Pulse modulation. Sampling
Lecture 17	Pulse Amplitude and Pulse code modulation (PCM)
Lecture 18	Pulse Amplitude and Pulse code modulation (PCM)
Lecture 19	Differential pulse code modulation
Lecture 20	Delta modulation
Lecture 21	Noise considerations in PCM

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Lecture 22	Time Division multiplexing, Digital Multiplexers
Lecture 23	Elements of Detection Theory
Lecture 24	Optimum detection of signals in noise
Lecture 25	Coherent communication with waveforms- Probability of Error evaluations
Lecture 26	Coherent communication with waveforms- Probability of Error evaluations
Lecture 27	Baseband Pulse Transmission- Inter symbol Interference and Nyquist criterion
Lecture 28	Baseband Pulse Transmission- Inter symbol Interference and Nyquist criterion
Lecture 29	Pass band Digital Modulation schemes
Lecture 30	Phase Shift Keying
Lecture 31	Frequency Shift Keying
Lecture 32	Quadrature Amplitude Modulation
Lecture 33	Continuous Phase Modulation and Minimum Shift Keying.
Lecture 34	Digital Modulation tradeoffs
Lecture 35	Optimum demodulation of digital signals over band-limited channels
Lecture 36	Optimum demodulation of digital signals over band-limited channels
Lecture 37	Maximum likelihood sequence detection (Viterbi receiver)
Lecture 38	Equalization Techniques
Lecture 39	Synchronization and Carrier Recovery for Digital modulation
Lecture 40	Synchronization and Carrier Recovery for Digital modulation

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Assignments:

Assignment 1	**Q1.** Design Modulator and Demodulator of SSB-SC Modulation based on its mathematical expression.
	Q2. Derive the figure of merit in a) FM Receiver b) PM Receiver
	Q3. A Carrier signal c(t) = 20 cos ($2\pi 10^6$t) is modulated by a message signal having three frequencies 5 KHz, 10 KHz & 20 KHz. The corresponding modulation indexes are 0.4, 0.5 & 0.6. Sketch the spectrum. Calculate bandwidth, power and efficiency.
Assignment 2	**Q1.** Derive the expression for probability of error in ASK, FSK and PSK systems and compare them.
	Q2. With block diagrams explain about DPCM & DM. also compare them.
	Q3. A message signal m(t) = 4 cos ($2\pi 10^3$t) is sampled at nyquist rate and transmitted through a channel using 3-bit PCM system. i. Calculate all the parameters of the PCM. ii. If the sampled values are 3.8, 2.1, 0.5, -1.7, -3.2 & -4 then determine the quantizer output, encoder output andquantization error per each sample. iii. Sketch the transfer characteristics of the quantizer.

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4EC4-21: Analog and Digital Communication Lab

Credit: 1.5 **Max. Marks: 75(IA:45, ETE:30)**

0L+0T+3P

List of Experiments	
Sr. No.	**Name of Experiment**
1.	Observe the Amplitude modulated wave form & measure modulation index and demodulation of AM signal.
2.	Harmonic analysis of Amplitude Modulated wave form.
3.	Generation & Demodulation of DSB – SC signal.
4.	Modulate a sinusoidal signal with high frequency carrier to obtain FM signal and demodulation of the FM signal.
5.	Verification of Sampling Theorem.
6.	To study & observe the operation of a super heterodyne receiver.
7.	PAM, PWM & PPM: Modulation and demodulation.
8.	To observe the transmission of four signals over a single channel using TDM-PAM method.
9.	To study the PCM modulation & demodulation and study the effect of channel like attenuation, noise in between modulator & demodulator through the experimental setup.
10.	To study the 4 channel PCM multiplexing & de-multiplexing in telephony system.
11.	To study the Delta & Adaptive delta modulation & demodulation and also study the effect of channel like attenuation, noise in between modulator & demodulator through the experimental setup.
12.	To perform the experiment of generation and study the various data formatting schemes (Unipolar, Bipolar, Manchester, AMI etc.)
13.	To perform the experiment of generation and detection of ASK, FSK, BPSK, DBPSK signals with variable length data pattern.

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Course Outcome:

Course Code	Course Name	Course Outcome	Details
4EC4-21	Analog and Digital Communication Lab	CO 1	Understand different analog modulation schemes and evaluate modulation index
		CO 2	Able to understand the principle of superhetrodyne receiver
		CO 3	Develop time division multiplexing concepts in real time applications
		CO 4	Develop and able to comprehend different data formatting schemes
		CO 5	Comprehend and analyze the concepts of different digital modulation techniques in communication.

CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
4EC4-21 Analog and Digital Communication Lab	CO 1	3	2		1								
	CO 2	3	2	1									
	CO 3	3	3	2	2	1							
	CO 4	3	3	2	2	1							
	CO 5	3	3	2	2	1							

3: Strongly **2: Moderate** **1: Weak**

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4EC4-22: Analog Circuits Lab

Credit: 1.5 **Max. Marks: 75(IA:45, ETE:30)**

0L+0T+3P

List of Experiments	
Sr. No.	**Name of Experiment**
1.	Study and implementation of Voltage Series and Current Series Negative Feedback Amplifier.
2.	Study and implementation of Voltage Shunt and Current Shunt Negative Feedback Amplifier.
3.	Plot frequency response of BJT amplifier with and without feedback in the emitter circuit and calculate bandwidth, gain bandwidth product with and without negative feedback.
4.	Study and implementation of series and shunt voltage regulators and calculate line regulation and ripple factor.
5.	Plot and study the characteristics of small signal amplifier using FET.
6.	Study and implementation of push pull amplifier. Measure variation of output power & distortion with load and calculate the efficiency.
7.	Study and implementation of Wein bridge oscillator and observe the effect of variation in oscillator frequency.
8.	Study and implementation of transistor phase shift oscillator and observe the effect of variation in R & C on oscillator frequency and compare with theoretical value.
9.	Study and implementation of the following oscillators and observe the effect of variation of capacitance on oscillator frequency: (a) Hartley (b) Colpitts.
10.	Study and implementation of the Inverting And Non-Inverting Operational Amplifier.
11.	Study and implementation of Summing, Scaling And Averaging of Operational Amplifier
12.	Implementation of active filters using OPAMP.

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Course Outcome:

Course Code	Course Name	Course Outcome	Details
4EC4-22	Analog Circuits Lab	CO 1	Discuss and observe the operation of a bipolar junction transistor and field-effect transistor in different region of operations.
		CO 2	Analyze and design of transistor Amplifier and Oscillators. Importance of negative feedback.
		CO 3	Analyze the frequency response of amplifiers and operational amplifier circuits. Develop an intuition for analog circuit behavior in both linear and nonlinear operation.
		CO 4	Design op-amps for specific gain, speed, or switching performance. Compensate operational amplifiers for stability.
		CO 5	Design and conduct experiments, interpret and analyze data, and report results.

CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
4EC4-22 Analog Circuits Lab	CO 1	3	2	1	2	2							
	CO 2	2	3	1	2	3							
	CO 3	1	3	2	3	2							
	CO 4	1	2	3	2	3							
	CO 5	1	2	3	3	3							

3: Strongly **2: Moderate** **1: Weak**

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4EC4-23: Microcontrollers Lab

Credit: 1.5 **Max. Marks: 75(IA:45, ETE:30)**

0L+0T+3P

List of Experiments	
Sr. No.	**Name of Experiment**
Following exercises has to be Performed on 8085	
1.	Write a program for 1.1 Multiplication of two 8 bit numbers 1.2 Division of two 8 bit numbers
2.	Write a program to arrange a set of data in Ascending and Descending order.
3.	Write a program to find Factorial of a given number.
4.	Write a program to generate a Software Delay. 4.1 Using a Register 4.2 Using a Register Pair
8085 Interfacing Programs	
5.	5.1 Write a program to Interface ADC with 8085.
	5.2 Write a program to interface Temperature measurement module with 8085.
6.	Write a program to interface Keyboard with 8085.
7.	Write a program to interface DC Motor and stepper motor with 8085.
Following exercises has to be Performed on 8051	
8.	Write a program to convert a given Hex number to Decimal.
9.	Write a program to find numbers of even numbers and odd numbersamong 10 Numbers.
10.	Write a program to find Largest and Smallest Numbers among 10 Numbers.
11.	11.1 To study how to generate delay with timer and loop.
	11.2 Write a program to generate a signal on output pin using timer.
8051 Interfacing Programs	
12	12.1 Write a program to interface Seven Segment Display with 8051.
	12.2 Write a program to interface LCD with 8051.
13	Write a program for Traffic light Control using 8051.
14	Write a program for Elevator Control using 8051.

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Course Outcome:

Course Code	Course Name	Course Outcome	Details
4EC4-23	Microcontrollers Lab	CO 1	Develop skills related to assembly level programming of microprocessors and microcontroller.
		CO 2	Interpret the basic knowledge of microprocessor and microcontroller interfacing, delay generation, waveform generation and Interrupts.
		CO 3	Interfacing the external devices to the microcontroller and microprocessor to solve real time problems.
		CO 4	Illustrate functions of various general purpose interfacing devices.
		CO 5	Develop a simple microcontroller and microprocessor based systems

CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
4EC4-23 Microcontrollers Lab	CO 1	2	1	2	1	3							
	CO 2	3	2	1	2	1							
	CO 3	1	1	3	1	3							
	CO 4	2	2	1									
	CO 5	1	1	3	2	2		2					

3: Strongly **2: Moderate** **1: Weak**

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4EC4-24: Electronics Measurement & Instrumentation Lab

Credit: 1.5 **Max. Marks: 75(IA:45, ETE:30)**

0L+0T+3P

List of Experiments	
Sr. No.	**Name of Experiment**
1.	Measure earth resistance using fall of potential method.
2.	Plot V-I characteristics & measure open circuit voltage & short circuit current of a solar panel.
3.	Measure unknown inductance capacitance resistance using following bridges (a) Anderson Bridge (b) Maxwell Bridge
4.	To measure unknown frequency & capacitance using Wein's bridge.
5.	Measurement of the distance with the help of ultrasonic transmitter & receiver.
6.	Measurement of displacement with the help of LVDT.
7.	Draw the characteristics of the following temperature transducers (a) RTD (Pt-100) (b) Thermistors.
8.	Draw the characteristics between temperature & voltage of a K type thermocouple
9.	Calibrate an ammeter using D.C. slide wire potentiometer
10.	Measurement of strain/force with the help of strain gauge load cell.
11.	Study the working of Q-meter and measure Q of coils.
12.	Calibrate a single-phase energy meter (Analog and Digital) by phantom loading at different power factor by: (i) Phase shifting transformer (ii) Auto transformer.

Course Outcome:

Course Code	Course Name	Course Outcome	Details
4EC4-24	Electronic Measurement & Instrumentation Lab	**CO 1**	Understanding of the fundamentals of Electronic Instrumentation. Explain and identify measuring instruments.
		CO 2	Able to measure resistance, inductance and capacitance by various methods.
		CO 3	Design an instrumentation system that meets desired specifications and requirements.
		CO 4	Design and conduct experiments, interpret and analyze data, and report results.
		CO 5	Explain the principle of electrical transducers. Confidence to apply instrumentation solutions for given industrial applications.

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CO-PO Mapping:

Subject	Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
4EC4-24 Electronic Measurement & Instrumentation Lab	**CO 1**	3	2	1	2	2							
	CO 2	2	3	1	2	3							
	CO 3	1	3	2	3	2							
	CO 4	1	2	3	2	3							
	CO 5	1	2	3	3	3							

3: Strongly **2: Moderate** **1: Weak**

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5EC1A SIGNALS AND SYSTEMS

B. Tech. (EC) 5th Sem. **Max. Marks: 100**

3L + 1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**Introduction**: Continuous time and discrete time signals and systems, Properties of systems**.**	**3**
	Linear time invariant systems- continuous time and discrete time. Properties of LTI systems and their block diagrams.	**5**
	Convolution, Discrete time systems described by difference equations.	**1**
II	**Fourier series representation of signals:** Fourier series representation of continuous periodic signal & its properties.	**3**
	Fourier series representation of Discrete periodic signal & its properties.	**3**
	Continuous time filters & Discrete time filters described by Diff. equation.	**2**
III	**Fourier transform**: The continuous time Fourier transform for periodic and non-periodic signals, Properties of CTFT.	**4**
	Discrete time Fourier transform for periodic and non-periodic signals Properties of DTFT.	**4**
IV	**Z-transform & laplace transform:** The region of convergence for the Z-transform. The Inverse Z-transform. Two dimensional Z transform.	**3**
	Properties of Z transform.	**1**
	Laplace transform: Properties of Laplace Transform,	**2**
	Application of Laplace transform to system analysis.	**2**
V	**SAMPLING:** Mathematical theory of sampling. Sampling theorem. Ideal & Real sampling.	**2**
	Interpolation technique for the reconstruction of a signal from its samples. Aliasing.	**2**
	Sampling in freq. domain. Sampling of discrete time signals.	**3**
	TOTAL	**40**

TEXT BOOKS

1. Signals And Systems, Oppenheim, Willsky, Nawab, PHI.(1992)
2. Signals And Systems M J Roberts, Mc-Graw Hill.(2004)

REFERENCE BOOKS

S.No.	Name of Authors /Books /Publisher	Publication year
1	Principles Of Linear Systems And Signals, 2e (Intl. Version), Lathi 2nd, Oxford	2002
2	Signal & Systems 3e, Chen 3rd, Oxford	2004
3	Fundamentals Of Signals And Systems, Wiley	2009
4	Signals And Systems, P Rao, Mc-Graw Hill	2011
5	Signals And Systems: A Simplified Approach, Ganesh Rao, 4e, Pearson	2012
6	Signals And Systems: Continuous And Discrete, Roger E Ziemer, 4e, PHI	1998
7	Signals And Systems, Ravi Kumar, PHI	2009
8	Signals & Systems, Iyer, Cengage Learning	2009

5EC2A LINEAR INTEGRATED CIRCUITS

B.Tech. (EC) 5th Sem. **Max. Marks: 100**

3L+0T **Exam Hours: 3**

Unit	Contents	Contact Hours
I	**OPERATIONAL AMPLIFIERS:** Basic differential amplifier analysis, Basic structure and principle of operation, Single ended and double ended configurations ,, calculation of differential gain, common mode gain, Op-amp configurations with feedback, Op-amp parameters, Inverting and Non-Inverting configuration, Comparators, Adder.	08
II	**OPERATIONAL AMPLIFIER APPLICATIONS:** Integrator, Differentiator, Voltage to frequency & Frequency to voltage converters. Oscillators: Phase shift, Wien bridge, Quadrature, precision rectifier, half and full wave rectifiers, square wave, triangular wave, sawtooth oscillators. Voltage controlled oscillators.	08
III	**ACTIVE FILTERS:** Low pass, high pass, band pass and band reject filters, All pass filter, Switched capacitor filter, Butterworth filter design, Chebyshev Filter design.	08
IV	**LINEAR ICs:** Four quadrant multiplier & its applications, Basic blocks of linear IC voltage regulators, Three terminal voltage regulators, Positive and negative voltage regulators. , A/D and D/A converters, analog switches, The 555 timer as astable and monostable multivibrators. Zero crossing detector, Schmitt trigger and its applications.	08
V	**Non- linear Applications of OP-AMP:** log and antilog amplifiers, and multipliers. Solution of differential equation and analog computer **PHASE-LOCKED LOOPS:** Operating Principles of PLL, Linear Model of PLL, Lock range, Capture range, Applications of PLL as FM detector, FSK demodulator, AM detector, frequency translator, phase shifter, tracking filter, signal synchronizer and frequency synthesizer, Building blocks of PLL, LM 565 PLL.	08
	TOTAL	**40**

TEXT BOOKs

S.No.	Name of Authors /Books /Publisher	Publication Year
1	OP-AMP and linear integrated circuits 2nd edition, PLHI by Ramakant A. Gayakwad.	1992
2	Design with operation amplifiers and Analog Integrated circuits by Sergei Franco.	2007
3	Integrated Electronics: Analog and Digital circuits & system by Millman & Halkias.	1972
4	Linear Integrated Circuits by D.R.Chaudhary (WEL).	2007

Reference Books:

1. Operational amplifier with linear integrated circuits, 4th edition, W.D. Stanley, Pearson.(2002)
2. Op Amps and Linear Integrated Circuits: Concepts and Applications, Fiore, Cengage learning (2010)

5EC3A TELECOMMUNICATION ENGINEERING

B.Tech. (EC) 5th Sem. **Max. Marks: 100**

3L+0T **Exam Hours: 3**

Unit	Contents	Contact Hours
I	**TRANSMISSION LINE**: Types of transmission lines, general transmission line equation, line constant, equivalent circuits, infinite line, and reflection on a line, SWR of line with different type of terminations. Distortion less and dissipation less lines, Coaxial cables, Transmission lines at audio and radio frequencies, Losses in transmission line, Characteristics of quarter wave, half wave and lines of other lengths.	08
II	**TRANSMISSION LINE APPLICATIONS:** Smith chart and its application. Transmission line applications, Impedance matching Network. Single & double Stub matching. Measurement of parameters of transmission line, measurement of attenuation, insertion loss, reflection coefficient and standing wave ratio.	08
III	**ATTENUATORS & FILTERS:** Elements of telephone transmission networks, symmetrical and Asymmetrical two port networks. Different Attenuators, π-section & T-section attenuators, stub matching, Transmission equalizers Filters, constant K-section, Ladder type, π-section, T-section filter, m-derived filter sections, Lattice filter section.	08
IV	**TELEPHONY:** Voice transmission, Two wire/ Four wire transmission, Multi-channel systems: Frequency division & time division multiplexing, Echo suppressors & cancellers, cross talk. Telephone set, Touch tone dial types, Numbering Concept for Telephony, Essentials of Traffic Engineering, Telephone Traffic Measurements, Subscriber loop Design.	08
V	**Switching and Signaling for Analog and Digital Telephone Networks**: Introduction to switching Concepts, De-generation, Availability and Grading, Principle of Electronic Exchange, EPABX and SPC Digital telephone Exchange, Fascimile services. Approaches to PCM Switching: Multistage switches, Time Switch, Space Switch, STS and TST Switches, Concept of Supervisory and AC signaling.	08
	TOTAL	**40**

TEXT BOOKs:

1. Telecommunication System Engineering, Roger L. Freeman, Wiley (2004)
2. Telecommunication Switching Systems & Networks, Thiagrajan Vishwanathan, PHI(1994)

REFERENCE BOOKs:

S.No.	Name of Authors /Books /Publisher	Publication Year
1.	Introduction to Telecommunications, Gokhale ,Cengage learning	2011
2.	Telecommunication, W.Fraser., PHI	1970
3.	Digital Telephony, Bellamy, Wiley.	2000
4.	Schaum's Outline of Theory and Problems of Transmission Lines,TMH.	2007
5.	Transmission Lines and Networks, Umesh Sinha, Satya Prakashan.	2010
6.	E.Keiser&E.Strange, Digital Telephony and Network Integration, (2/e), Van Nostrand,.	1995
7.	Principles of Telephony, N.N. Biswas, Sangam Books Limited.	1993
8.	Transmission Lines and Networks, Umesh Sinha, Satya Prakashan	
9.	Telecommunications Switching, Traffic and Networks, J.E. Flood, Pearson	

5EC4A ANALOG COMMUNICATION

B.Tech. (EC) 5th Sem. **Max. Marks: 100**

3L+1T **Exam: 3 Hours**

Unit	Contents	Contact Hours
I	**NOISE EFFECTS IN COMMUNICATION SYSTEMS**: Representation of Band Limited and Band Pass Process, Noise Sources and Classification, Resistor noise, Networks with reactive elements, Noise temperature, Noise bandwidth, effective input noise temperature, Noise figure. Noise figure & equivalent noise temperature in cascaded circuits. Narrow band Noise Representation.	08
II	**AMPLITUDE MODULATION**: Frequency translation, Single Tone Modulation, Recovery of base band signal, Spectrum & power relations in AM systems. Methods of Modulation & Demodulation of AM-DSB, DSB-SC and SSB signals. AM Broadcast Transmitters & Receivers, Single Sideband Transmission and Reception, Super heterodyne receivers, Vestigial Sideband Modulation.	09
III	**FREQUENCY MODULATION:** Phase & freq. modulation & their relationship, Spectrum & band width of a Sinusoidal modulated FM signal, Phasor diagram, Narrow band & wide band FM. Generation & demodulation of FM signals. Effect of Channel Non-Linearity, Comparison of AM, FM & PM, Threshold in FM, PLL demodulator. FM Broadcasting transmitters & Receivers.	08
IV	**NOISE IN AM AND FM**: Noise in CW modulation systems, SNR calculations for synchronous detection of DSB and SSB and envelope detection of AM, SNR calculations for angle modulation system, Pre-emphasis and de-emphasis, Threshold effect, Noise in Communication subsystems - Internal and external noise.	08
V	**PULSE ANALOG MODULATION**: Practical aspects of sampling: Natural and flat top sampling. Reconstruction, PAM, PWM, PPM modulation and demodulation methods, Noise Performance of Pulse Analog Modulation Systems.	07
	TOTAL	**40**

TEXT BOOKs:

1. Analog Communications Systems, P RamaKrishna Rao, Mc Graw Hill (2008)
2. Communications Systems, 5ed-, Haykins, Wiley (2009)

REFERENCE BOOKs:

S.No.	Name of Authors /Books /Publisher	Year of Publication
1	Principles of Communication Systems, Herbert Taub, Donald Schilling, Goutam Saha, TMH	2009
2	Analog and Digital Communication, Schum Series, TMH	2006
3	Digital & Analog Communication Systems, Leon W. Couch, Pearson	2013
4	Analog & Digital Communication Systems, Singal, TMH	2001
5	An Introduction To Analog & Digital Communications, Haykins, Wiley	2009
6	Electronic Communication Systems-, Kennedy Devis, TMH	1999
7	Digital And Analog Communication Systems, Shanmugam, Wiley	1994
8	Proakis J. G. and Salehi M., "Communication Systems Engineering", Pearson Education	2002
9	Electronic Communication Systems, Blake, Cengage learning	2006

5EC5A MICROWAVE ENGINEERING – I

B.Tech. (EC) 5th semester **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**MICROWAVE TRANSMISSION LINES:** Introduction of Microwaves and their applications. Rectangular Waveguides: TE and TM wave solutions, Field patterns, Wave impedance and Power flow.	5
	PLANAR TRANSMISSION LINES: Stripline and microstrip lines – Dominant mode of propagation, Field patterns, Characteristic impedance, Basic design formulas and characteristics. Parallel coupled striplines and microstrip lines- Even- and odd- mode excitations. Slot lines and Coplanar lines. Advantages over waveguides.	5
II	**MICROWAVE NETWORK ANALYSIS:** Impedance and Admittance matrices, Scattering matrix, Parameters of reciprocal and Loss less networks, ABCD Matrix, Scattering matrices of typical two- port, three-port and four-port networks, Conversion between two- port network matrices.	8
III	**MICROWAVE PASSIVE COMPONENTS**: Waveguide Components: *E*-plane and *H*- plane Tees, Magic Tee, Shorting plunger, Directional couplers, and Attenuator.	4
	Stripline and Microstrip line Components*:* Open and shorted ends. Half wave resonator, Lumped elements (inductors, capacitors and resistors) in microstrip. Ring resonator, 3-dB branchline coupler, backward wave coupler, Wilkinson power dividers and rat- race hybrid ring. Low pass and band pass filters.	5
IV	**MICROWAVE MEASUREMENTS:** Detection of microwaves, Microwave power measurement, Impedance measurement, Measurement of reflection loss (VSWR), and transmission loss in components. Passive and active circuit measurement & characterization using network analyser, spectrum analyser and noise figuremeter.	6
V	**MICROWAVE INTEGRATED CIRCUIT TECHNOLOGY:** Substrates for Microwave Integrated Circuits (MICs) and their properties. Hybrid technology – Photolithographic process, deposited and discrete lumped components. Microwave Monolithic Integrated Circuit (MMIC) technology- Substrates, MMIC process, comparison with hybrid integrated circuit technology (MIC technology).	7
	TOTAL	**40**

TEXT BOOKs

1. A. Das and S. Das, Microwave Engineering, Tata McGraw-Hill, 2000.
2. D. M. Pozar, *Microwave Engineering*, John Wiley & Sons, 1998. (Use the latest version)

REFERENCE BOOKs

SN	Name of Authors /Books /Publisher	Year of
		Publication
1	B. Bhat and S. K. Koul, Stripline-like Transmission Lines for Microwave Integrated Circuits, Wiley Eastern Ltd.,	1989
2	T. C. Edwards, Foundations for Microstrip Circuit Design, John Wiley & Sons,.	1981
3	K. C. Gupta, R. Garg, I.Bahl and P. Bhartia, Microstrip Lines and Slotlines, Second Edition, Artech House	1996
4	K.C Gupta, Microwaves, Wiley Eastern Ltd.	1979
5	P.A. Rizzi, Microwave Engineering- Passive Circuits, Prentice Hall,.	1988
6	Robert E. Collin, Foundations for microwave engineering 2ed	2012
7	Microwave Engineering , Raghuvanshi,Cengage learning	

5EC6.1A BIOMEDICAL INSTRUMENTATION

B.Tech. (EC) 5th Sem **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**HUMAN BODY SUBSYSTEMS-** Brief description of neural, muscular, cardiovascular and respiratory systems; their electrical, mechanical and chemical activities.	3
	TRANSDUCERS AND ELECTRODES- Principles and classification of transducers for Bio-medical applications, Electrode theory, different types of electrodes, Selection criteria for transducers and electrodes.	3
II	**BIOPOTENTIALS-** Electrical activity of excitable cells, ENG, EMG, ECG, ERG, ECG. Neuron potential.	2
	CARDIOVASCULAR SYSTEM MEASUREMENTS- Measurement of blood pressure, blood flow, cardiac output, cardiac rate, heart sounds, Electrocardiograph, phonocardiograph, Plethysmograph, Echocardiograph.	5
III	**INSTRUMENTATION FOR CLINICAL LABORATORY-** Measurement of pH value of blood, ESR measurement, hemoglobin measurement, O2 and CO2 concentration in blood, GSR measurement. Spectrophotomentry, chromatography, Hematology,	4
	MEDICAL IMAGING: Diagnostic X-rays, CAT, MRI, thermography, ultrasonography, medical use of isotopes, endoscopy.	4
IV	**PATIENT CARE, BIOTELEMETRY AND SAFETY MEASURES** Elements of Intensive care monitoring basic hospital systems and components, physiological effects of electric current shock hazards from electrical equipment, safety measures, Standards & practices. Biomedical Telemetry: Introduction, block diagram and description of single channel/multi channel telemetry systems.	7
	THERAPEUTIC AND PROSTHETIC DEVICES - Introduction to cardiac pacemakers, defibrillators, ventilators, muscle stimulators, diathermy, heart lung machine, Hemodialysis, Applications of Laser.	5
V	**APPLICATIONS OF BIOPOTENTIALS:** Electrocardiographic diagnostic criteria for Identification of cardiac disorders, Electrocardiographic pattern of ischemia, Atrial abnormalities, Ventricular enlargement, Abnormal ECG patterns, Clinical applications of EEG, EMG, ERG	4
	COMPUTER APPLICATIONS: data acquisition and processing, remote data recording and management. Real time computer applications	3
	TOTAL	**40**

TEXT BOOKs:

1. L. Cromwell, F. J. Weibell, and L. A. Pfeiffer, Biomedical Instrumentation and Measurements, Pearson Education, Delhi, (1990)

2. J. J. Carr and J. M. Brown, Introduction to Biomedical Equipment Technology, 4th ed., Pearson Education, Delhi, (2001)

REFERENCE BOOKs:

SN	Name of Authors /Books /Publisher	Year of Publication
1	Biomedical Instrumentation Systems , Chatterjee, Cengage learning	**2011**
2	Aston, "Principles of Biomedical Instrumentation and measurements", McGraw Hill publishing Co,	1990
3	L.A. Geddes and L.E. Baker, Principles of Applied Biomedical Instrumentation , John Wiley & Sons, Inc,	1989
4	Richard Aston, Principles of Biomedical Instrumentation and Measurement , Merrill Publishing Company,.	1990
5	Jacobson B. and Webster J.G., Medical Clinical Engineers , Prentice Hall Inc.,.	1979
6	J. G. Webster, Medical Instrumentation Application and Design, 3rd ed., John Wiley & Sons, N.Y.,	1998
7	R. S. Khandpur, Handbook of Biomedical Instrumentation, 2nd ed., Tata McGraw Hill,	2003
8	R. Anandanatarajan, "Biomedical Instrumentation", PHI Learning,	2009

5EC6.2A ADVANCED DATA STRUCTURES AND ALGORITHMS

B.Tech. (EC) 5th Sem. **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**Basic Concepts of OOPs** – Templates Function and class templates – Algorithms: performance analysis: time complexity and space complexity– ADT – List (Singly– Doubly and Circular) Implementation – Array – Pointer – Cursor Implementation	6
II	**Stacks and Queues** – ADT– Implementation and Applications – Trees – General– Binary – Binary Search – Expression Search – AVL – Introduction to Red Black trees and Splay tree – B Trees – Implementations – Tree Traversals	6
III	**Set** – Implementation – Basic Operations on Set – Priority Queue – Implementation – Graphs – Directed Graphs – Shortest Path Problem – Undirected Graph – Spanning Trees – Graph Traversals: hash table representation: hash functions: collision resolution: separate chaining: open addressing: linear probing: quadratic probing: double hashing: rehashing	10
IV	**Issues** – Managing Equal Sized Blocks – Garbage Collection Algorithms for Equal Sized Blocks – Storage Allocation for Objects with Mixed Sizes – Buddy Systems – Storage Compaction	8
V	**Searching Techniques** – Sorting – Internal Sorting – Bubble Sort – Insertion Sort – Quick Sort – Heap Sort – Bin Sort – Radix Sort – External Sorting – Merge Sort – Multiway Merge Sort – Polyphase Sorting – Design Techniques – Divide and Conquer – Dynamic Programming – Greedy Algorithm – Backtracking – Local Search Algorithms	10
	TOTAL	**40**

TEXT BOOKs

1. Tanenbaum A.S, Langram Y, Augestien M.J., Data Structures using C & C++, Prentice Hall of India, (2002).
2. Data structures, Algorithms and Applications in C++, S.Sahni, University Press (India) Pvt.Ltd, 2nd edition, Universities Press Orient Longman Pvt. Ltd. (2005)

REFERENCE BOOKS

SN	Name of Authors /Books /Publisher	Year of Publication
1	Mark Allen Weiss:Data Structures and Algorithm Analysis in C++,Pearson Education	2002
2	Aho Hopcroft Ullman, ―Data Structures and Algorithms, Pearson Education,	2002
3	Horowitz Sahni, Rajasekaran, ―Computer Algorithms, Galgotia,.	2000
4	Data structures and Algorithms in C++, Michael T.Goodrich, R.Tamassia and Mount, Wiley student edition, John Wiley and Sons.	1989
5	Data Structure and Algorithm in C++ , Drozdek, Cengage learning	1990
6	Data Structures: A Pseudocode Approach with C, Gilberg,Cengage learning	1996

5EC6.3A COMPUTER ORIENTED NUMERICAL & STATISTICAL METHODS

B.Tech. (EC) 5th Sem. **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**SOLUTION OF LINEAR EQUATIONS-** Cramer's Rule, Gauss Elimination, Gauss Jordan Elimination and Gauss Seidal Iterative Methods	8
	and their Computer Programming in C. Matrix Inversion by Gauss Elimination, Computer Programs for Matrix Inversion.	
II	**SOLUTION OF NON-LINEAR EQUATIONS-** Interval Bisection Method, Secant Method, Regula- Falsi Method, Curve Fitting, Method of Least Squares and their Computer Programming in C	7
III	**SOLUTION OF PARTIAL DIFFERENTIAL EQUATIONS-** Solution of Partial Differential Equations with Special Reference to Heat Equation, Laplace Equation and Wave Equation Milne's and their Computer Programming in C. Curve Fitting Methods – Method of Least Squares, Fitting a Straight Line, Parabola..	9
IV	**STOCHASTIC PROCESSES -** Definitions, Random Process Concept, Statistics of Stochastic Processes: Mean, Autocorrelation, Autocovariance. Stationary Processes, Strict and Wide Sense Stationary, Random Processes and Linear Systems.	9
V	**STOCHASTIC PROCESSES IN FREQUENCY DOMAIN -** Power Spectrum of Stochastic Processes, Transmission over LTI systems, Gaussian and White Processes, Properties of Power Spectral Density	7
	TOTAL	40

TEXT BOOKs:

1. Numerical methods for Scientific and Engineering Computation by M.K.Jain, S.R.K.Iyengar, R.K. Jain. (2003)

2. Computer based numerical algorithms by E.V. Krishnamoorthy.(2004)

REFERENCE BOOKS:

SN	Name of Authors /Books /Publisher	Year of Publication
1	Introduction to Numerical Analysis by E. Atkinson	1984
2	Peebles, P. Probability, random variables and random signal principles. Mc Graw Hill.	2007
3	Computer Oriented Numerical & Statistical Methods by Dr. Gokhroo & Others	2012
4	Elementary Numerical Analysis by Samuel D.Conte and Cart de Boor, McGraw Hill International Edition.	19100
5	Numerical methods for Science and Engineering, PHI by R.G.Stanton	1963
6	Papoulis, A. Probability, random variables and stochastic processes. Mc Graw Hill (international Students' edition), Singapore.	2012
7	Childers, D. G. Probability and random processes using MATLAB. Mc Graw Hill,	1997
8	Smith, G. D. Numerical Solution of PDE, Oxford Uni. Press	2002
9	Numerical Analysis, Burden, Cengage learning	2004
10	Numerical Mathematics and Computing , Cheney , Cengage learning	2005

5EC7A ELECTRONIC ENGINEERING DESIGN LAB

B.Tech. (EC) 5th Sem. **Max. Marks: 75**

2P **Exam: 3 H**

S.No.	Contents
	To design the following circuits, assemble these on bread board and test them. Simulation of these circuits with the help of appropriate software.
1	Op-Amp characteristics and get data for input bias current measure the output-offset voltage and reduce it to zero and calculate slew rate.
2	Op-Amp in inverting and non-inverting modes.
3	Op-Amp as scalar, summer and voltage follower.
4	Op-Amp as differentiator and integrator.
5	Design LPF and HPF using Op-Amp 741
6	Design Band Pass and Band reject Active filters using Op-Amp 741.
7	Design Oscillators using Op-Amp (i) RC phase shift (ii) Hartley (iii) Colpitts
8	Design (i) Astable (ii) Monostable multivibrators using IC-555 timer
9	Design Triangular & square wave generator using 555 timer.
10	Design Amplifier (for given gain) using Bipolar Junction Transistor.

5EC8A MICROWAVE ENGINEERING LAB

B.Tech. (EC) 5th Sem. **Max. Marks: 75**

2P **Exam: 3 H**

S.No.	Contents
1.	Study of various microwave components and instruments like frequency meter, attenuator, detector and VSWR meter. (a) Measurement of guide wavelength and frequency using a X-band slotted line setup. (b) Measurement of low and high VSWR using a X-band slotted line setup.
2.	Measurement of SWR and shift in the standing wave minimum with unknown load, and calculation of unknown load impedance using Smith chart.
3.	(a) Draw the V-I characteristics of a Gunn diode, and determine the output power and frequency as a function of voltage (b) Study the square wave modulation of microwave signal using PIN diode modulator.
4.	Measurement of resonance characteristics of a microstrip ring resonator using power meter and Determination of the substrate dielectric constant.
5.	(a) To study the coupling characteristics of (i) a microstrip 3dB branchline coupler, and (ii) a stripline backward wave coupler as a function of frequency. Compare the bandwidth in the two cases. (b) Measure the microwave input, direct, coupled and isolated powers of a backward wave stripline coupler at the centre frequency using a power meter. From the measurements calculate the coupling, isolation and directivity of the coupler.
6.	Measure the power division and isolation characteristics of a microstrip 3dB power divider.
7.	Study of rat race hybrid ring (equivalent of waveguide Magic-Tee) in microstrip
8.	(a) Study of low pass and band pass microstrip filters (b) Measurement of gain versus frequency of a microwave amplifier using power meter.

5EC9A COMMUNICATION LAB – I

B.Tech. (EC) 5th Sem. **Max. Marks: 75**
2P **Exam: 3H**

S.No.	Contents
1.	To generate the different type of waveforms using fundamental frequency and its harmonics and also analysis these waveforms.
2.	Perform the experiment of the amplitude modulation and calculate the modulation index. Also study the DSB, DSB-SC, SSB modulation & demodulation and also observe the effect of AGC.
3.	To Study the properties of Super Heterodyne receiver in AM transmitting & receiving. Sensitivity, selectivity through experimental setup.
4.	To perform the different type of FM modulation & demodulation schemes and study the effect of Amplitude limiter in FM demodulator.
5.	To find the characteristics impedance, input impedance, losses, standing waves, phase shifting in transmission line.
6.	To perform the PAM (natural, flat top, sample & hold sampling), PWM, PPM Modulation & demodulation techniques.
7.	To perform the experiment of and observe the effect of change in type of modulating signal as sine , square, arbitrary and change in frequency of sampling signal in PAM, PWM, PPM modulation and demodulation.
8.	To Study & observation of the four channel analog TDM using DSB-SC through experimental setup.
9.	To Study& observation of the FDM through experimental setup.
10.	To perform the experiment of the various sampling (natural, flat top & sample & hold) of analog signal of type sine, square & arbitrary.

5EC10A SIGNAL PROCESSING LAB

B.Tech. (EC) 5th Sem. **Max. Marks: 75**

2P **Exam: 3 H**

S.No.	Contents
1	Simulation in MATLAB Environment: Generation of continuous and discrete elementary signals (periodic and non-periodic) using mathematical expression.
2	Generation of Continuous and Discrete Unit Step Signal.
3	Generation of Exponential and Ramp signals in Continuous & Discrete domain.
4	Continuous and discrete time Convolution (using basic definition).
5	Adding and subtracting two given signals. (Continuous as well as Discrete signals)
6	To generate uniform random numbers between (0, 1).
7	To generate a random binary wave.
8	To generate random sequences with arbitrary distributions, means and variances for following : (a) Rayleigh distribution (b) Normal distributions: N(0,1). (c) Gaussion distributions: N (m_x, σ_x^2)
9	To plot the probability density functions. Find mean and variance for the above distributions

5EC11A PROFESSIONAL ETHICS AND DISASTER MANAGEMENT

B.Tech. (EC) 5th Sem. **Max. Marks:50**
2P **Exam: 3 H**

S.No.	CONTENTS
1.	**Issues on ethics and values:** Moral and ethical values, classification of values , value system, deterioration of social values, social norms & social control.
2.	**Profession , professionalism &ethics:** Professional responsibilities, competencies and expectations .Role of a professional , person, professional accountability and professional ethics .
3.	**Ethics in engineering and disaster management:** Engineering professionals , role of engineers ,technology & society ,engineering as social experimentation , engineering ethics.
4.	**Types of disasters:** Environmental, economic & social disasters ; causes , impact and prevention , Case studies.
5.	**Thoughts of ethics**

TEXT BOOKs:

1. Engineering Ethics: Concepts & Cases by Harris, Cengage Learning (2013)

6EC1A MICROWAVE ENGINEERING – II

B.Tech. (EC) 6th semester **Max. Marks: 100**
Exam Hours: 3

3L+1T

UNIT	CONTENTS	CONTACT HOURS
I	**IMPEDANCE TRANSFORMATION AND MATCHING:** Lumped elements for MICs and MMICs- printed inductors, capacitors and resonant elements. The Smith chart- combined impedance-admittance chart. Impedance matching with lumped elements (L networks) and Smith chart solutions. Single stub tuning in microstrip circuits using shunt stub. Single section quarter-wave transformer.	**8**
II	**MICROWAVE DIODES AND DIODE CIRCUITS**: *Detector Diodes* – Silicon crystal diode and Schottky diode. V-I characteristic of detector diode, basic operation of detection and mixing, single diode mixer circuit. *PIN diode* - Equivalent circuit and characteristics of PIN diode, single-pole PIN diode switches and single bit phase shifters. *Varactor diode*- Device characteristics and circuit applications. *Gunn diode*- Gunn effect, principle of operation and characteristics, typical oscillator circuit using Gunn diode. *IMPATT diode*- Characteristics, negative resistance, power output and efficiency.	**9**
III	**MICROWAVE TRANSISTORS AND CIRCUITS**: *Bipolar Junction Transistors (BJTs)* – Geometry of silicon bipolar transistor, DC biasing, microwave equivalent circuit and characteristics. *Microwave Field Effect Transistors* (*FETs*) - Physical structure and principle of operation of JFET, MOSFET and MESFET, characteristics, comparison of FET devices and circuit applications. Single stage FET amplifier – Block schematic of a single stage FET amplifier circuit, Stability considerations, analysis and derivation of expression for transducer gain with unilateral transistor, design criteria for maximum gain.	**8**
IV	***Klystrons***- Limitations of conventional vacuum tubes. Reflex klystron – Basic schematic, mechanism of operation, modes of oscillation and modulation. Velocity modulation and electron bunching (analytical treatment), ***Magnetrons-*** Types of magnetron. Basic structure of magnetron, analysis, resonant modes in magnetron, operation, mechanism of oscillations, practical consideration of cavity magnetron. Introduction to coaxial, frequency angle and voltage tunable magnetrons.	**8**

V	***Two cavity klystron amplifier***- Basic schematic and mechanism of operation. ***Travelling Wave Tube Amplifier***- Basic schematic of helix type TWT tube. Introduction to CW power pulsed dual mode TWT. TWT amplifier operational characteristics. Applications of TWT. Crossed- field amplifier.	**7**
	TOTAL	**40**

TEXT BOOK:

1. A. Das and S. Das, Microwave Engineering, Tata McGraw-Hill, (2000).
2. S.Y.Liao, Microwave devices and circuits, Prentice Hall, 2nd Edition (1985).

REFERENCE BOOKS:

SN	Name of Authors /Books /Publisher	Year of Publication
1	B. Bhat and S. K. Koul, Stripline-like Transmission Lines for Microwave Integrated Circuits, Wiley Eastern Ltd.,	1989
2	T. C. Edwards, Foundations for Microstrip Circuit Design, John Wiley & Sons,.	1981
3	K. C. Gupta, R. Garg, I. Bahl and P. Bhartia, Microstrip Lines and Slotlines, Second Edition, Artech House	1996
4	K.C Gupta, Microwaves, Wiley Eastern Ltd.	1979
5	P.A. Rizzi, Microwave Engineering- Passive Circuits, Prentice Hall,.	1988
6	D. M. Pozar, Microwave Engineering, John Wiley & Sons,. (Use the latest version)	1998
7	Dennis Roddy, Microwave Technology, Prentice-Hall,	1986
8	Roy Mitra, Microwave semiconductor devices, PHI,	2004
9.	Robert E. Collin, Foundations for microwave engineering 2ed	2012
10	Microwave and Radar Engineering, G.S.B. Rao, Pearson	2013
11.	Microwave Engineering, Raghuvanshi, cengage learning	2013

6EC2A MICROPROCESSORS

B.Tech. (EC) 6th Sem. **Max. Marks: 100**

3L+0T **Exam Hours: 3**

Unit	Contents	Contact Hours
1	INTRODUCTION: The 8085 architecture, memory, I/O Devices, Logic devices for interfacing, Memory, Interfacing, addressing modes, Instructions, programming techniques.	8
2	INTRODUCTION TO 8085 INSTRUCTIONS: Looping, counting and Indexing, Data Transfer and Arithmetic instructions, Counters and Time Displays, stacks & subroutines, conditional call and return instructions.	8
3	Code Conversion, BCD Arithmetic and 16 Bit Data Operations additional data transfer & 16 bit arithmetic Instructions, arithmetic operations related to memory counter and time delays.	8
4	Interrupts: The 8085 Interrupt, vectored interrupts, additional I/O concepts and processes, serial I/O & data communication.	8
5	General purpose programming peripheral devices: 8255 Programmable peripheral interface, Interfacing keyboard and Seven Segment display. The 8254 programmable Interval timer, the 8259 programmable Interrupt controller.8257 DMA controller.	8
	TOTAL	**40**

TEXT BOOKS:

1. Microprocessors Architecture, Programming &Application, Ramesh S. Gaonkar, (2000)
2. A Textbook of Microprocessors and Microcontrollers, R.S. Kaler I.K International Publishing House Pvt. Ltd.

REFERENCE BOOKS:

SN	Name of Authors /Books /Publisher	Year of Publication
1.	Introduction to Microprocessors, A.P. Mathur, Mc Graw Hill	2002

6EC3A INDUSTRIAL ELECTRONICS

B.Tech. (EC) 6th Sem. **Max. Marks: 100**
Exam Hours: 3

3L+0T

UNIT	CONTENTS	CONTACT HOURS
I	**SEMICONDUCTOR POWER DEVICES -** Basic characteristics & working of Power Diodes, Diac, SCR, Triac, Power Transistor, MOSFETs, IGBT, and GTO	7
II	**RECTIFIERS & INVERTERS - Working principles of single and three phase bridge rectifiers, Voltage and current source inverters**	8
III	**POWER SUPPLIES:** Principle of operation of choppers. Step up, Step down and reversible choppers. High frequency electronic ballast, Switch Mode Power Supply: Fly back converter, forward/buck converter, Boost converter and buck-boost converter. Uninterruptible Power Supply.	10
IV	**MOTOR CONTROL:** Introduction to speed control of DC motors using phase controlled converters and choppers, Basic idea of speed control of three phase induction motors using voltage and frequency control methods.	8
V	**Stepper Motors:** Variable reluctance, Permanent magnet and hybrid stepper motors. Induction and dielectric heating control.	7
	TOTAL	40

TEXT BOOKs:

1. Power Electronics Principles & Applications, Joseph Vithayathil, TMH , (2010).
2. Power Eletronics, Ravish Singh, TMH, (2012).

REFERENCE BOOKs:

SN	Name of Authors /Books /Publisher	Year of Publication
1	Industrial Electronics And Control, Ttti, TMH	2001
2	Power Electronics: Converters Applications., Mohan, Robbins, Wiley	1995
3	Power Electronics, Moorthi, Oxford	2005
4	Elements Of Power Electronics, Krein, Oxford	1998
5	Power Electronics, R.S.Murthy, Pearson	2012
6	Power Electronics: Circuits, Devices And Applications	2004

6EC4A DIGITAL COMMUNICATION

B.Tech. (EC) 6th sem. **Max. Marks: 100**
Exam Hours: 3

3L+1T

UNIT	CONTENTS	CONTACT HOURS
I	**Digital Transmission Of Analog Signals:** Uniform and Non-uniform quantization. PCM and delta modulation, Signal to quantization noise ratio in PCM and delta modulation. DPCM, ADM, T1 Carrier System, Error probability in PCM system.	8
II	**Base Band Transmission:** Line coding (RZ, NRZ): Polar, Bipolar, Manchester, AMI. Inter symbol interference, Pulse shaping, Nyquist criterion, Raised cosine spectrum. Optimum detection, Matched filter.	8
III	**Digital Modulation Techniques:** Geometric interpretation of signals and Orthogonalization. ASK, BPSK, BFSK, QPSK, M-ary PSK, MSK and GMSK modulation techniques and Coherent detection of these techniques. Signal constellation and calculation of error probabilities.	8
IV	**Information Theory:** Measure of Information, Average Information, Entropy, Information rate, Increase in Average information per bit by coding, Shannon's Theorem and Shannon's bound, Capacity of a Gaussian Channel, BW-S/N trade off,	8
V	**Source & Error Control Coding**: Coding and decoding of Information Source coding, Entropy coding, Hamming code, Single Parity- Bit Code, Linear Block code, Cyclic code & Convolutional code.	8
	TOTAL	**40**

TEXT BOOKs:

1. Digital Communications Systems, P RamaKrishna Rao, Mc Graw Hill
2. Digital Communications Systems, Simon Haykins, Wiley

REFERENCE BOOKs:

SN	Name of Authors /Books /Publisher	Year of Publication
1	Digital Communications: Fundamentals and Applications, Bernard Sklar, Pearson Education	2002
2	Analog and Digital Communication, Schum Series, TMH	2006
3	Digital & Analog Communication Systems, Leon W. Couch, Pearson	2013
4	Analog & Digital Communication Systems, Singal, TMH	2012
5	An Introduction To Analog & Digital Communications, Haykins, Wiley	2010
6	Digital And Analog Communication Systems, Shanmugam, Wiley	2004
7	Proakis J. G. and Salehi M., "Communication Systems Engineering", Pearson Education	2008
8	Electronic Communication Systems-, Kennedy Devis, TMH	2013

6EC5A CONTROL SYSTEM

B.Tech. (EC) 6th Sem. **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**CONTROL SYSTEM AND THEIR REPRESENTATION**: Terminology and basic structure of control system, Open loop and Closed loop systems, servomechanism, regulatory system, analogous systems. Physical Systems and their models, Electromechanical systems, electrical analogy of physical systems.	**4**
	Transfer function, Block diagram representation of physical systems, Block diagram algebra, Signal Flow graph and Mason's formula.	**3**
II	**TIME RESPONSE**: Types of test inputs, Response of first and second order system, Time domain specifications, Error coefficients, generalized error series	**4**
	STABILITY: Concepts of stability, location of roots in s-plane for stability, asymptotic stability and relative stability, Routh-Hurwitz stability criterion.	**3**
III	**ROOT LOCUS**: Root locus plot, Properties of Root loci and applications, Stability range from the loci. Determination of roots of the closed loop system, Effect of pole zero addition	**4**
	NYQUIST PLOTS: Polar plots, Nyquist plots and Nyquist stability criterion	**4**
IV	**BODE PLOTS**: Concepts of Gain margin and phase margin, Bode plots .	**5**
	Frequency-domain specifications	**1**
	M and N loci ,Nichols chart	**2**
V	**CONTROLLERS**: Introduction to PID and Lag-lead type Controllers	**2**
	STATE VARIABLE ANALYSIS: Concepts of state, state variable and state model. State variable models for LTI systems. Canonical representations, Transfer function to state-space and vice-versa. Solution to state equations. Concepts of controllability & observability.	**6**
	COMPENSATION DESIGN: compensation design using frequency domain techniques	**2**
	TOTAL	**40**

TEXT BOOKs: TEXT BOOK

1. Modern control Engineering, Ogata, Pearson.(2009)
2. Nise's Control System Engineering, Rajeev Gupta, Wiley (2011)

REFERENCE BOOKS

SN	**Name of Authors /Books /Publisher**	**Year of Publication**
1	Control Systems: Principles & Design, M. Gopal, TMH	2002
2	Automatic Control System,B. C. Kuo,Wiley	2009
3	Singh & Janardhanan - Modern control engineering, Cengage learning	2010
4	Control Systems,Srivastava,TMH	2009
5	Systems and Control - Stanislawhizak, Oxford	2002
6	Control System Engineering,S. K. Bhattacharya,Pearson	2009
7	Control Systems: Theory And Applications,Ghosh,Pearson	2004
8	Manik – Control systems, Cengage learning	2012

6EC6.1A NEURAL NETWORKS

B.Tech. (EC) 6th Sem. **Max. Marks: 100**
Exam Hours: 3

3L+0T

UNIT	CONTENTS	CONTACT HOURS
I	**NEUROPHYSIOLOGY:** Introduction: Elementary neurophysiology - From neurons to ANNs - Neuron model McCulloch-Pitts model, Hebbian Hypothesis; limitations of single-layered neural networks.	5
II	**THE PERCEPTRON**: The Perceptron and its learning law. Classification of linearly separable patterns.	2
	LINEAR NETWORKS: Adaline --- the adaptive linear element. Linear regression. The Wiener-Hopf equation. The Least-Mean-Square (Widrow-Hoff) learning algorithm. Method of steepest descent. Adaline as a linear adaptive filter. A sequential regression algorithm.	4
III	**MULTI-LAYER FEEDFORWARD NEURAL NETWORKS**: Multi-Layer Perceptrons. Supervised Learning. Approximation and interpolation of functions. Back-Propagation Learning law. Fast training algorithms. Applications of multilayer perceptrons: Image coding, Paint-quality inspection, Nettalk.	5
	SELF-ORGANISING SYSTEMS: Unsupervised Learning. Local learning laws. Generalised Hebbian Algorithm. The Oja's and Sanger's rules. Principal component analysis --- Karhunen-Loeve transform.	4
IV	**COMPETITIVE LEARNING:** MinNet and MaxNet networks. Clustering. Learning Vector Quantisation. Codebooks. Application in data compression. **SELF-ORGANISING FEATURE MAPS**: Kohonen networks.	4
	RADIAL-BASIS FUNCTION NETWORKS: Radial-Basis function (RBF) networks and their application in function interpolation, approximation and modelling probability distributions. **RECURRENT NETWORKS**: Hopfield networks	5
V	**APPLICATIONS OF NEURAL NETWORKS**: Pattern classification, Associative memories, Optimization, Applications in Image Processing-Iris, finger print & face, Applications in decision making, TSP Problem.	6
	TOTAL	40

TEXT BOOKs:

1. Jacek M. Zurada, **Introduction to artificial neural systems,** Jaico Publ. House, (1994)**.**
2. S.N. Sivanandam**,** S. Sumathi and S.N. Deepa, **Introduction to Neural Networks using MATLAB 6.0,** Tata McGraw-Hill, (2006).

REFERENCE BOOKS

SN	Name of Authors /Books /Publisher	Year of Publication
1	Artificial Neural Network,Robert Schalloff,TMH	1997
2	Fundamental Of Neural Network Architecture And Application,Laurene V. Fausett,Pearson	1993
3	Neural Network Algorithm And Programing Tech,James A Freeman,Pearson	1991
4	Neural N/W For Pattern Recognition,Cristopher, M.Bhishop,Oxford	1995
5	Fuzzy Neuro Approach To Agent Application,Lee ,Raymond S.T.,New Age	2008
6	Fuzzy Logic and Neural Networks: Basic Concept And Application,A Lavala, Chemakesava R.,New Age	2012
7	Neural Network Design w/CD, Hagan, Cengage Learning	2007

6EC6.2A PARALLEL COMPUTATION & ARCHITECTURE

B.Tech. (EC) 6th Sem. **Max. Marks: 100**

3L+0T **Exam Hours: 3**

Unit	Content	Hours
1	Overview Of Parallel Processing: Constraints of conventional architecture, Parallelism in uniprocessor system, Architectural Classification.	5
	Applications of parallel processing, Sorting networks, PRAM models, interconnection network, memory consistency models, shared memory multiprocessors	4
2	Hardware taxonomy: Flynn's classification, Handler's classification, Software taxonomy, Kung's taxonomy, SPMD,	5
	Basic Algorithms – Fast Fourier Transform, Linear System Solution etc.	3
3	Instruction level Parallelism and Thread Level Parallelism, Explicitly Parallel Instruction Computing (EPIC) Architecture,	3
	Principles of scalable performance: Performance Metrics and Measures, Speedup Performance Laws efficiency utilization, overheads	4
4	Vector and Array Processor Basic vector architecture, Issues in Vector Processing, Vector performance modeling, SIMD Computer Organization Masking and Data network mechanism.	4
	Inter PE Communication, Interconnection networks of SIMD, Static V s Dynamic network, cube hyper cube and Mesh Interconnection network, Parallel Algorithms For Array Processors.	5
5	Multiprocessor Architecture Loosely and Tightly coupled multiprocessors, Processor characteristics of multiprocessors,	4
	Inter Processor communication network, Time shared bus, Crossbar switch.	3

Text Books:

1. V.Rajaraman, L Sivaram Murthy, "Parallel Computers", PID. (2004)

2. William Stallings, "Computer Organization and Architecture, Designing for performancePrentice Hall, Sixth edition.(2007)

Reference Books:

SN	Name of Authors /Books /Publisher	Year of Publication
1	Kai Hwang, Scalable Parallel Computing.	1998
2	Harrold Stone, High performance computer Architectures.	1992
3	Richard Y. Kain , Advanced Computer Architecture.	2000
4	Parallel Computing in C and OpenMPI , M. J. Quinn, McGraw-Hill	2004
5	Assembly Language and Computer Architure Using C++ and JAVA w/CD, Reis, Cengage Learning	2011

6EC6.3A OPTICAL FIBER COMMUNICATION

B.Tech. (EC) 6th sem. **Max. Marks: 100**
Exam Hours: 3

3L+0T

UNIT	CONTENTS	CONTACT HOURS
I	**Optical Fiber Overview-** Introduction, Ray theory, Optical fibers: multimode, single mode, step index, graded index, plastic & glass fibers.	**3**
	Transmission Characteristics of Optical Fibers - Introduction, Attenuation, Material absorption loss, Fiber bend loss, scattering, Dispersion (intermodal & intramodal), Dispersion Shifted Fibers, Dispersion Compensating Fiber. **Manufacturing of Optical Fibers –** preparation of optical fiber, Liquid phase techniques and vapor phase depositions techniques.	**6**
II	**OPTICAL FIBER SOURCES-** **Laser-** Emission and absorption of radiation, Einstein relation, Absorption of radiation, Population inversion, Optical feedback, Threshold condition. Population inversion and threshold, working of three levels & four level lasers. Basic idea of solid state, semiconductors, gas & liquid laser. Basic concept of Q-switching and mode locking.	**5**
	Light Emitting Diode - Structure, Material, Characteristics, Power & Efficiency.	**2**
III	**Optical Detectors -** Optical detection principles, quantum efficiency, Responsivity, PIN photo diode, Avalanche photo diodes, Noise in Detectors, Photo Diode Materials.	**4**
	Optical Connectors - Fiber Alignment, fiber splices, fiber connectors, expanded beam connectors, fiber couplers.	**4**
IV	**Optical Fiber Measurements -** Measurements of Fiber Attenuation, Dispersion, Refractive Index Profile, Cut off Wave Length, Numerical Aperture & Diameter.	**5**
	Optical Time Domain Reflectometry (OTDR) - Field measurement through optical time domain reflectometry, Laser based systems for measurement of distance, Velocity, Holography.	**3**
V	**Optical Fiber Systems –** Wavelength division multiplexing, DWDM, active and passive components, optical sensors, optical amplifiers.	**5**
	Optical Fiber Applications - public network applications, military, civil and industrial applications.	**3**
	TOTAL	**40**

TEXT Books:

1. J.M. Senior, Optical Fiber Communication: Principles and Practice, Pearson Education. (2013)
2. R.P. Khare, Fiber Optics & Optoelectronics, Oxford Publications. (2014)

REFERENCE Books:

SN	Name of Authors /Books /Publisher	Year of Publication
1	R.P. Khare, Fiber Optics & Optoelectronics, Oxford Publications.	2004
2	J.Gowar, Optical Communication Systems, PHI.	1999
3	A.Ghatak & K.Thygarajan, Introduction to Fiber Optics, Cambridge University Press.	2006
4	Joseph C Palais, Fiber Optics Communication, PHI.	2010
5	Harold Kolimbris, Fiber Optics Communication, Pearson Education.	2009
6	D. Anuradha, Optical Fiber and Laser, Principles and Applications, New Age.	2008

6EC7A COMMUNICATION LAB – II

B.Tech. (EC) 6th sem. **Max. Marks: 75**
3P **Exam Hours: 3**

S.No.	Contents
1	To identify & solve the aliasing problem and verify the Nyquist criteria through the experimental setup.
2	To perform the experiment of observe the transmission of four signals over a single channel using TDM-PAM method.
3	To study the 4 channel PCM multiplexing & de-multiplexing in telephony system and calculate line speed and baud rate through the experimental setup.
4	To study the PCM, DPCM modulation & demodulation and study the effect of channel like as attenuation, noise in between modulator & demodulator through the experimental setup.
5	To study the Delta, Adaptive delta & sigma delta modulation & demodulation and also study the effect of channel like as attenuation, noise in between modulator & demodulator through the experimental setup.
6	To perform the experiment of generation and study the various data formatting schemes (Uni-polar, Bi-polar, Manchester, AMI etc.)
7	To perform the experiment of generation and detection of ASK, FSK, BPSK, DBPSK signals with variable length data pattern
8	To perform the experiment of generation and detection of QPSK, OQPSK, DQPSK signals with variable length data pattern.
9	To study the working of MSK modulation and its demodulation through the experimental setup.
10	To study the working of cyclic code, block code error check methods in communication system through the experimental setup.
Simulation using any virtual Instrumentation Software:	
11	To carry out convolution in both continuous time and discrete time systems.
12	Companding and multiplexing of PCM signals.
13	Perform various keying Techniques: PSK, ASK, FSK & MSK.

6EC8A MICROPROCESSOR LAB

B.Tech. (EC) 6th sem. **Max. Marks: 75**
2P **Exam: 3 H**

S.No.	Contents
	Following exercises are to be done in 8085 assembly language.
1	Arranging a set of data in Ascending order.
2	Arranging a set of data in Descending order.
3	Finding out number of Positive, Negative and Zeros from a Data Set.
4	Searching the Existence of a certain data in a given data.
5	BCD to Binary conversion.
6	Binary to BCD conversion
7	Design a Up/Down Counter
8	Multiply Two 8 Bit Numbers using Successive Addition and Shifting method.
9	Find Factorial of a number.
10	Solve the given Algebraic Equation
11	Generate a Software Delay.
12	Division of 8 bit Unsigned Numbers.
13	A program to display real time clock. Assume a periodic signal is interrupting RST 7.5 signal after every 0.5 seconds,
14	Generate a square wave and rectangular wave of given frequency at the Output pin of 8255 chip.

6EC9A RF SIMULATION LAB

B.Tech. (EC) 6th sem. **Max. Marks: 75**
3P **Exam Hours: 3**

S.No.	Contents
1	Study of field pattern of various modes inside a rectangular waveguide.
2	Study of field pattern of various modes inside a rectangular waveguide cavity.
3	Observe the transient phenomenon of terminated coaxial transmission lines in order to study their time domain behaviour.
4	Study the behavior of terminated coaxial transmission lines in frequency domain.
5	Introduction to Smith chart and its application for the unknown impedance measurement.
6	Study the behavior of impedance matching for passive networks (RL, RC, RLC, T- and Π-network) using Smith chart.
7	Find the change in characteristics impedance and reflection coefficients of the transmission line by changing the dielectric properties of materials embedded between two conductors.
8	Design and simulate the following *Planar Transmission Lines:* 1. Stripline and microstrip lines 2. Parallel coupled striplines and microstrip lines 3. Slot lines and Coplanar lines Determine their Field patterns and Characteristic impedance.
9	Design and simulate the following; 1. 3-dB branchline coupler, 2. backward wave coupler, 3. Wilkinson power dividers 4. rat- race hybrid ring. 5. Low pass filters 6. band pass filters.
10	Design RF amplifier using microwave BJT.
11	Design RF frequency doublers using microwave FET.

6EC10A INDUSTRIAL ELECTRONICS LAB

B.Tech. (EC) 6th Sem. **Max. Marks: 75**
2P **Exam Hours: 3**

S.No.	Contents
1.	Study the characteristics of SCR and observe the terminal configuration, Measure the breakdown voltage, latching and holding current. Plot V-I characteristics.
2.	Perform experiment on triggering circuits for SCR. i.e. R-triggering,R-C triggering and UJT triggering circuit.
3.	Study and test AC voltage regulators using triac, antiparallel thyristors and triac & diac.
4.	Study and obtain the waveforms for single-phase bridge converter.
5.	Perform experiment on single phase PWM inverter.
6.	Perform experiment on buck, boost and buck-boost regulators.
7.	Control speed of a dc motor using a chopper and plot armature voltage versus speed characteristic.
8.	Control speed of a single-phase induction motor using single phase AC voltage regulator.
9.	i) Study single-phase dual converter. ii) Study speed control of dc motor using single-phase dual converter.
10.	Study single-phase cycloconverter.
11.	Perform experiment on Motor control – open loop & closed loop.
12.	Design, observe and perform experiment on various type of pulse generation from DSP/ FPGA Platform. Perform experiment for PWM inverters and choppers.

6EC11A PERSONALITY DEVELOPMENT AND GENERAL APTITUDE

B.Tech. (EC) 6th sem. **Max. Marks: 50**
2P **Exam Hours: 3**

S.No.	CONTENTS
	PURPOSE 1. To make students appreciate the notion and components of personality, thereby to apply the acquired information to themselves and to march towards brilliance in their respective academic and professional careers. 2. To enable students to keep themselves abreast of general knowledge and current affairs. 3. To bring out creativity and other latent talents with right goal setting so that self- esteem gets enhanced. 4. To sharpen memory skills and other study skills vital for academic and professional excellence. 5. To give training for positive thinking to keep the students in a good stead at the time of crisis.
1.	**Introduction** • Meaning of Personality • Determinants of Personality- biological, psychological and socio- cultural factors. • Misconceptions and clarifications • Need for personality development
2.	**Self-Awareness and Self Motivation** • Self analysis through SWOT and Johari window • Elements of motivation • Seven rules of motivation • Techniques and strategies for self motivation • Motivation checklist and Goal setting based on principle of SMART • Self motivation and life • Importance of self-esteem.
3.	**Memory and study skills** • Definition and importance of memory • Causes of forgetting • How to forget (thought stopping), how to remember (techniques for improving memory) • The technique of passing exams-management of examination fear.
4.	**Power of positive thinking** • Nurturing creativity, decision-making and problem solving. • Thinking power- seven steps for dealing with doubt • Traits of positive thinkers and high achievers

	• Goals and techniques for positive thinking • Enhancement of concentration through positive thinking • Practicing a positive life style.
5.	**General Knowledge and Current Affairs** • Regional, national and international events • Geographical, political and historical facts • Information on sports and other recreational activities • Basic knowledge with regard to health and health promotion
PRACTICAL TRAINING The course would include the following practical exercises. Ice-breaking. Brainstorming and simulation exercises. Thought stopping. Memory and study skills training	

TEXTBOOKs:

1. Hurlock, E.B, Personality Development, 28th Reprint. New Delhi: Tata Mc Graw Hill. (2006).
2. Mikew, Martin, schinzinger, Ethics in engineering, TMH. (2010)

REFERENCES BOOKs:

SN	Name of Authors /Books /Publisher	Year of Publication
1	Mile, D.J., Power of positive thinking. Delhi: Rohan Book Company.	2004
2	Pravesh Kumar, All about self- Motivation. New Delhi: Goodwill Publishing House.	2005
3	Dudley, G.A. ,Double your learning power. Delhi: Konark Press. Thomas Publishing Group Ltd.	2004
4	Lorayne, H. ,How to develop a super power memory. Delhi: Konark Press. Thomas Publishing Group Ltd	2004
5	Personal Development for Life and Work, Wallace, Cengage Learning	2013
6	Soft Skills for Everyone w/CD, Butterfield, Cengage Learning	2013

7EC1A ANTENNAS & WAVE PROPAGATION

B.Tech. (EC) 7th sem. **Max. Marks: 100**

3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**ANTENNA FUNDAMENTALS** – Review of Electromagnetic theory. Short elementary dipole and its fields. Basic antenna parameters - Radiation pattern, beamwidth, beam solid angle, directivity, efficiency, gain, radiation intensity, radiation resistance, input impedance and polarization. Receiving antenna- Reciprocity, effective length and aperture, antenna temperature.	8
II	**ANTENNA ARRAYS –** Point source, Array of two isotropic point sources. Uniform array of N point sources and array factor –Examples of 4 element broadside and endfire arrays. N-element linear array of elementary dipoles and principle of pattern multiplication. Two element array of elementary dipoles - Excitation with different relative phase shift and for different spacing.	7
III	**DIFFERENT TYPES OF ANTENNAS –** Thin linear antenna and thin half- wave dipole. V- and Rhombic antennas. Monopole antenna, Small loop antenna, Folded dipole and Yagi-Uda antenna. Broadband antennas – Broadband basics, log-periodic dipole array. Reflector antennas –Flat sheet, corner and parabolic reflector antennas. Slot, Horn and Lens antennas, Helical antennas. Microstrip patch antennas- Rectangular patch antenna, Two- element microstrip patch. *Antenna Measurements* - Antenna radiation pattern, gain, directivity and polarization. .	10
IV	**GROUND WAVE PROPAGATION** - Mechanism of radio wave propagation. Theory of ground reflection- Plane earth reflection, reflection factors for horizontal and vertical polarizations. Refraction and diffraction of radio waves. Space and surface waves. Tropospheric propagation, duct propagation and tropospheric scattering. .	7
V	**IONOSPHERIC PROPAGATION:** Various ionospheric layers, Electrical properties of the ionosphere and their effects on wave propagation. Critical frequency, virtual height, skip distance, maximum usable frequency. Multiple hop transmission. Effect of earth's magnetic field and Faraday rotation. Solar activity and meteorological conditions on wave propagation.	8
	TOTAL	**40**

TEXT BOOK

1. Sisir. Das and A. Das, Antenna and wave propagation, Tata McGraw-Hill Education Pvt. Ltd, (2013).
2. A.R. Harish and M. Sachidananda, Antennas and Wave Propagation, Oxford Univ. Press, Edition (2011).

REFERENCE BOOKS

SN	Name of Authors /Books /Publisher	Year of Publication
1.	J.D. Kraus, *Antennas*, Tata McGraw-Hill, 2nd Edition,	1999
2.	E.C. Jordan and K.G. Balmain, *Electromagnetic Waves and Radiating Systems*, Prentice-Hall of India, 2nd Edition,	1986
3.	T. Milligan, Microstrip Antenna Design, Wiley,	2005
4.	J.D. Kraus and R.J. Marhefka, Antennas for All Applications, Tata McGraw-Hill, Edition	2004
5.	R. Chatterjee, *Antenna Theory and Practice*,Wiley Eastern Ltd.,	1988
6.	Balanis Constantine A, Antenna theory, Analysis and design, 3rd edition, A John Wiley & Sons Inc. Publication	2005
7.	Collin R. E. And F. J. Zucker, Antenna Theory: Part I, McGraw-Hill, New York	1969
8.	Collin R. E. And F. J. Zucker, Antenna Theory: Part II, McGraw-Hill, New York	1969
9.	Ramesh Garg, P. Bhartia, Inder Bahl, and A Ittipiboon, Microstrip Antenna Design Hand, Artech House, Inc.	2001

7EC2A DIGITAL SIGNAL PROCESSING

B.Tech. (EC) 7th Sem. **Max. Marks: 100**

3L+1T **Exam Hours: 3**

UNIT	CONTENTS	Contact Hours
I	**SAMPLING** - Discrete time processing of Continuous-time signals, continuous time processing of discrete-time signals,	5
	Changing the sampling rate using discrete-time processing.	2
II	**TRANSFORM ANALYSIS OF LTI SYSTEMS** - Introduction, The frequency response of LTI systems, System functions for systems characterized by LCCD (Linear Constant Coefficient Difference) equations,	4
	All-pass system, Minimum-Phase systems, Linear systems with linear phase.	3
III	**STRUCTURES FOR DISCRETE-TIME SYSTEMS**- Block diagram and signal flow graph representation of LCCD equations,	2
	Basic structures for IIR and FIR systems, Transposed forms.	5
IV	**FILTER DESIGN TECHNIQUES -** Introduction, Analog filter Design: Butterworth & Chebyshev,	3
	IIR filter design by impulse invariance & Bilinear transformation,	2
	Design of FIR filters by Windowing: Rectangular, Hanning, Hamming & Kaiser.	5
V	**DFT, FFT**- The Discrete Fourier transform (DFT), Properties of the DFT, Linear Convolution using DFT,	5
	Efficient computation of the DFT: Decimation–in-Time and Decimation-in frequency FFT Algorithms.	4
	Total	**40**

TEXT BOOKs:

1. Proakis, Manolakis, "Digital Signal Processing: Principals, Algorithms And Applications", 4th ed., Pearson Education. (2006)
2. Oppenheim, Schafer, "Discrete Time Signal Processing", 3rd ed. , PHI (2010)

REFERENCE BOOKs:

S.No.	Name of Authors/Book/Publisher	Publication Year
1.	Digital Signal Processing: A Modern Introduction, Ambardar, cengage learning	2011
2.	Introduction to Digital Signal Processing using MATLAB, Schilling	2011
3	Sanjit K Mitra, "Digital Signal Processing", 4th ed., TMH	2013
4	Tan, Jiang, "Digital Signal Processing: Fundamentals and Applications",2nd ed., Elsevier	2008
5	Ifeachor, Jervis, "Digital Signal Processing", 2nd ed., Pearson Education	2009

7EC3A DIGITAL IMAGE PROCESSING

B.Tech. (EC) 7th Sem. **Max. Marks: 100**

3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**DIGITAL IMAGE FUNDAMENTALS**: Image sensing and acquisition, Image sampling and quantization, Representing digital images, Spatial and gray-level resolution,	3
	Spatial operations, Vector & matrix operations, Zooming and Shrinking of digital images. RGB and HSI Color models	3
II	**BASIC IMAGE OPERATIONS:** Intensity transformation functions, Histogram equalization, Spatial filtering for image smoothing,	3
	Image sharpening by first and second order derivatives, Image smoothing and sharpening using frequency domain filters	5
III	**IMAGE RESTORATION:** Image restoration model, Noise Models, Spatial and frequency properties of noise, noise probability density functions,	3
	Noise only- spatial filter, Mean, order Statistic and adaptive filters, Concepts of inverse and Wiener filtering	5
IV	**MORPHOLOGICAL IMAGE PROCESSING:** Erosion and Dilation, Opening and closing,	3
	morphological algorithms for Boundary extraction, thinning, pruning, smoothing and thickening	5
V	**IMAGE SEGMENTATION AND COMPRESSION:** Edge based segmentation, Edge detection masks, Gradient operators,	3
	Thresholding, Region growing, Watershed transform,	3
	Fundamentals of image compression; Loss-less compression techniques; Lossy compression techniques, compression standards	4
	Total	**40**

TEXT BOOKs:

1. Gonzalez, Woods and Eddins, "Digital Image Processing", 3rd ed. , Pearson Education (2010)
2. Anil K Jain, "Fundamentals of Digital Image Processing", 4th ed., Prentice Hall (2010)

REFERENCE BOOKs:

SN	Name of Authors/Book/Publisher	Publication Year
1	Tamal Bose, "Digital Signal and Image Processing", ", 3rd ed. , John Wiley	2005
2	Sonaka,Hlavac and Boyle, "Image Processing, Analysis and Machine Vision", 3rd ed. , Cengage Learning	2013
3	Pratt, "Digital Image Processing", 4th ed. , John Wiley	2001
4.	Image Processing, Analysis, and Machine Vision, Sonka, cengage learning	2006

7EC4A WIRELESS COMMUNICATION

B.Tech. (EC) 7th sem. **Max. Marks: 100**
Exam Hours: 3

3L+0T

UNIT	CONTENTS	CONTACT HOURS
I	**Spread Spectrum Modulation Techniques –** Concept of spread spectrum, system processing gain, Spread Spectrum signals: Direct-sequence spread spectrum signals, Frequency-hopped spread spectrum signals, Code-division multiplexing, Spreading codes	8
II	**Wireless Microwave Communication**- Link Engineering, Frequency planning, Free space loss, Fresnel zone clearance, bending of radio beam, Effective earth radius, Fundamentals of fading, types and effects, Multipath channels; parameters, measurements, Building blocks of Transmitter & Receiver.	8
III	**Multiple Access Techniques and Networks** - FDMA, TDMA and CDMA with reference to mobile radio and satellite systems. TDMA based networks, OFDM and its characteristics, Packet radio multiple access techniques. CDMA based networks: Architecture, Air interface, Call processing, power control, Rake receiver concept and performance of CDMA system.	8
IV	**Cellular Wireless Networks**-, GSM: Introduction, overview of the GSM systems, GSM codec, channel coding and interleaving, radio like control. Cordless systems and WLL, Mobile IP, Wireless access protocol. Wireless LAN's: Technology, IEEE 1002.11 standards, Broadband Wireless 1002.16, Blue tooth, Wi-Fi, Wi-Max, Zigbee & RFID technology.	8
V	**Satellite Communication** - Elements of satellite communication: Frequency bands, Transmission and Multiple access. Satellite orbit and description- orbital period and velocity, effects of orbital inclination, Azimuth and elevation, Coverage angle and slant range, Satellite Link: basic link design and analysis, Geostationary orbit, Satellite subsystems. Earth Station antenna, high-power amplifier, low-noise amplifier, up converter, down converter, monitoring and control, reliability.	8
	TOTAL	**40**

TEXT Books:

1. William Stallings, Wireless Communication and Networks, Pearson Education (2013)
2. Rappaport, T.S., Wireless Communications, Pearson Education (2013)

REFERENCE Books:

SN	Name of Authors /Books /Publisher	Year of Publication
1	Gottapu Sasibhushana Rao, Mobile Cellular Communications, Pearson Education	2013
2	Singal, T.L, Wireless Communication, Tata McGraw Hill	2011
3	Vijay Kr. Garg, Wireless Communications and Networking, Morgan Kaufmann, Elsevier	2013
4	Blake, Wireless Communication Technology, Cengage Learning	2013
5	W.C.Y. Lee , Mobile Cellular Telecommunications , Tata McGraw Hill	2011
6	Wireless Communications and Networking, Price, TMH	2014
7	Pratt, Bostain, Satellite Communications, Wiley India	2011
8	Mark Zhuang, Wireless Communications and Networking, Prentice Hall of India	2003
9	Simon Haykin, Modern Wireless Communications, Pearson Education	2005
10	Price, Fundamentals of Wireless Networking, Tata McGraw Hill	2012

7EC5A VLSI DESIGN

B.Tech. (EC) 7th Sem. **Max. Marks: 100**

3L+0T **Exam: 3 H**

Unit	Contents	Contact hours
I	**INTRODUCTION TO MOSFET: -** Basic MOS transistors, Enhancement Mode transistor action, Depletion Mode transistor action, NMOS and CMOS fabrication. Aspects of threshold voltage, threshold voltage with body effect. Ids versus Vds relationship, channel length modulation. Transistor Trans-conductance gm. MOS transistor circuit Model, Model parameter (oxide and junction capacitor, channel resistance) variation with scaling and biasing. High order effects (i.e. subthreshold conduction, hot electron effect, narrow channel effect and punch through effect.	10
II	**CMOS LOGIC CIRCUITS-** nMOS inverter (resistive and active load), Pull up to Pull-down ratio for a NMOS Inverter and CMOS Inverter (Bn/Bp), , determination of inverter parameter ($V_{IL,}$ V_{IH} V_{OL} V_{OH}) and Noise Margin. **Speed and power dissipation analysis of CMOS inverter.** Combinational Logic, NAND Gate, NOR gate, XOR gate, Compound Gates, 2 input CMOS Multiplexer, Memory latches and registers, Transmission Gate, estimation of Gate delays, Power dissipation and Transistor sizing.	8
III	Basic physical design of simple Gates and Layout issues. Layout issues for CMOS inverter, Layout for NAND, NOR and Complex Logic gates, Layout of TG, Layout optimization using Eular path. DRC rules for layout and issues of interconnects, Latch up problem.	8
IV	**Dynamic CMOS circuits**: Clocked CMOS (C^2MOS) logic, DOMINO logic, NORA logic, NP(ZIPPER) logic, PE(pre-charge and Evaluation) Logic. Basic Memory circuits, SRAM and DRAM.	8
V	**Physical Design**: Introduction to ECAD tools for first and back end design of VLSI circuits. Custom /ASIC design, Design using FPGA and VHDL. VHDL Code for simple Logic gates, flip-flops, shift registers	6
	Total	**40**

Text Books:

1. **CMO S DIGITAL INTEGRATE**D **CIRCUITS Analysis and Design.** SUNG-MO (STEVE) ANG, YUSUF LEBLEBIGI, McGraw Hill (2008)

Reference Books:

SN	Name of Authors/Book/Publisher	Publication Year
1	N.Weste and K. Eshraghian, Principles of CMOS VLSI, 2e, Pearson Education.	2011
2	VLSI Design , P P Sahu , , McGraw.	2013
3	VLSI Design, D.P. Das, Oxford.	2011
4	Chip Design for Submicron VLSI: CMOS Layout & Simulation, Uyemura, cengage learning	2009

7EC6.1A ADVANCED MICROPROCESSORS

B.Tech. (EC) 7th sem. **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**The 8086 Microprocessor Family:** 8086 ARCHITECTURE- Hardware specifications, Pins and signals, Internal data operations and Registers, Minimum and maximum mode, System Bus Timing, Linking and execution of Programs,	8
II	**Software & Instruction Set:** Assembly language programming: addressing mode and instructions of 8086, Strings, Procedures and Macros, 8086 interrupts. Assembler Directives and operators.	8
III	**Analog Interfacing:** A/D and D/A converter interfacing, keyboard and display interfacing, RS 232 & IEEE 488 communication standards. An 8086 based Process Control Systems	8
IV	**Digital Interfacing:** Programmable parallel ports, Interfacing microprocessor to keyboard and alphanumeric displays, Memory interfacing and Decoding , DMA controller.	8
V	**Multiprocessor Configurations:** - Multiuser / Multi tasking operating system concepts, 8086 based Multiprocessor systems. Introduction and basic features of 286, 386, 486 & Pentium processors.	8
	TOTAL	**40**

TEXT BOOK

1. A Nagoor Kani “Microprocessors and Microcontrollers” Mc Graw Hill Education 2ed. (2012)
2. Douglas V. Hall “Microprocessors and Interfacing Programming and Hardware” Tata Mc Graw Hill.(2000)

REFERENCE BOOKS

SN	Name of Authors /Books /Publisher	Year of Publication
1	A. Ray & K. Bhurchandi. “Advanced Microprocessors and Peripherals. Tata Mc Graw Hill,	2012
2	A Nagoor Kani “Microprocessors and Microcontrollers” Mc Graw Hill Education 2ed.	2012
3	Introduction to Microprocessors, A. P. Mathur Mc Graw Hill	2011
4	The Intel Family of Microprocessors: Hardware and Software Principles and Applications, Antonakos, cengage learning	2012
	The 8086 Microprocessor: Programming & Interfacing the PC, Ayala, cengage learning	2007

7EC6.2A ARTIFICIAL INTELLIGENCE AND EXPERT SYSTEMS

B.Tech. (EC) 7th Sem. **Max. Marks: 100**

3L+0T **Exam: 3 Hours**

Unit	Contents	Contact Hours
I	**Introduction to Artificial Intelligence:** Intelligent Agents, State Space Search, Uninformed Search, Informed Search, Two Players Games, Constraint Satisfaction Problems.	09
II	**Knowledge Representation:** Knowledge Representation And Logic, Interface in Propositional Logic, First Order Logic, Reasoning Using First Order Logic, Resolution in FOPL	05
III	**KNOWLEDGE ORGANIZATION:** Rule based System, Semantic Net, Reasoning in Semantic Net Frames, Planning	10
IV	**KNOWLEDGE SYSTEMS:** Rule Based Expert System, Reasoning with Uncertainty, Fuzzy Reasoning	07
V	**KNOWLEDGE ACQUISITION:** Introduction to Learning, Rule Induction and Decision Trees, Learning Using neural Networks, Probabilistic Learning Natural Language Processing	09
	TOTAL	**40**

TEXT BOOK:

1. Elaine Rich and Kevin Knight, Artificial Intelligence 3/e, TMH (1991)
2. PADHY: ARTIFICIAL INTELLIGENCE & INTELLIGENT SYSTEMS, Oxford(2005)

REFERENCE BOOKS:

SN	Name of Authors /Books /Publisher	Year of Publication
1	James A Anderson, An introduction to Neural Networks. Bradford Books	1995
2	Dan. W Patterson, Artificial Intelligence and Expert Systems,PHI	1990
3	Kumar Satish, "Neural Networks" Tata Mc Graw Hill	2004
4	S. Rajsekaran & G.A. Vijayalakshmi Pai, "Neural Networks,Fuzzy Logic and Genetic Algorithm:Synthesis and Applications" Prentice Hall of India.	2006
6	Siman Haykin,"Neural Netowrks"Prentice Hall of India	1990
7	Artificial Intelligence, Kaushik, cengage learning	1997

7EC4.3A VHDL

B.Tech. (EC) 7th Sem. **Max. Marks: 100**

3L+0T **Exam: 3 Hours**

Unit	Contents	Contact Hours
I	**INTRODUCTION:**VHDL/PLD Design Methodology, Advantages, Requirement Analysis and specification, VHDL description, Verification Using simulations, Functional Simulation, Logic Synthesis, Place and route and timing Simulation Fundamental & history of various hardware description language, VHDL for Synthesis V/s Simulation, Design flow of ASICs and standard logic circuits. Implementation Details for SPLDs, CPLDs and FPGAs	06
II	**LANGUAGE FUNDAMENTALS:** Entities, Architectures and coding Styles, Signals and Data types, Packages, Dataflow, Structural, Behavioral and RTL Style of Combinational design, Event- Driven Simulation: Simulation Approaches, Elaboration Signal Drivers Simulator Kernel process, Signals verses Variables.	07
III	**COMBINATIONAL and SEQUENCIAL CIRCUITS BUILDING BLOCKS:** Multiplexer, Synthesis using Shannon's expansions, Decoders, encoders, Code Converters, VHDL Code for Combinational Circuits. VHDL code for Flip-Flops, shift registers, Counters.	09
IV	**SYNCHRONOUS/ ASYNCHRONOUS SEQUENCIAL CIRCUITS:** Mealy & Moore type FSMs, VHDL Code for Mealy & Moore Machines, VHDL Codes for Serial Adder, Vending Machine.	09
V	**DIGITAL SYSTEM DESIGN:** Building Block circuits, Memory organization, SRAM, Design examples of divider, Multiplier, Shifting & Sorting Operations, Clock Synchronization, CPU organization and design concepts.	09
	TOTAL	**40**

TEXT BOOK:

1. Digital Logic with VHDL Design, Brown, TMH.(2007)
2. VHDL for Engineers, Short, Pearson. (2011)

REFERENCE BOOKS:

SN	Name of Authors /Books /Publisher	Year of Publication
1	VHDL (Text BOOK Binding), Douglas L. Perry, TMH	2002
2	VHDL , A design oriented Approach, S S Limaye, TMH	2008
3	VHDL: Programming By Example, Douglas Perry, Oxford	2002
4	The Designer's Guide To VHDL, Peter J. Ashenden, Oxford	2010
5	Circuit Design With VHDL , By Volnei A Pedroni, PHI	2004
6	VHDL Bascis to programming, Gaganpreet Kaurt, Pearson	2013
7	Digital System Design Using VHDL, cengage learning	2008
8	HDL Programming Fundamentals VHDL & VERILOG. Botros. cengage learning	2002

7EC7A SIGNAL AND IMAGE PROCESSING LAB

B.Tech. (EC) 7th sem. **Max. Marks: 100**
2P **Exam Hours: 3**

S.No.	Contents
1	To simulate the transmitter and receiver for BPSK
2	To design and simulate FIR digital filter (LP/HP).
3	To design and simulate IIR digital filter (LP/HP).
4	Reading and displaying Gray/ Colour images of different formats
5	RGB/HSI conversions in an image, Image arithmetic operations.
6	Image Histogram and histogram equalization
7	Image filtering in Spatial and frequency domain
8	Morphological operations in analyzing image structures
9	Thresholding-based image segmentation
10	Study of image compression

7EC8A WIRELESS COMMUNICATION LAB

B.Tech. (EC) 7th Sem. **Max. Marks: 100**

3P **Exam: 3 Hours**

S.No.	Contents
1	Measurement of antenna input characteristics: Measure the input return loss versus frequency in the operating band for (i) Half wave dipole (printed dipole/strip dipole), (ii) Folded dipole and (ii) Log-periodic antenna.
2	Measurement of radiation characteristics of a (i) Half wave dipole (printed dipole/strip dipole), and (ii) Printed Yagi antenna -. Measure radiation patterns in the two principal planes and plot on polar chart. Determine beam width, directivity and antenna efficiency.
3	Measurement of antenna gain using absolute gain and relative gain measurements: • Measure gain of Bi-quad antenna using absolute gain measurements. • Measure gain of log-periodic antenna and printed slot antenna using relative gain measurements.
4	Circular polarization measurements on helical antenna.
5	Antenna array theory demonstration using single EM coupled rectangular patch, 2x1 EM coupled and 2x2 EM coupled rectangular patch antennas.
6	Communication link budget calculations- Friis formula and demonstration with transmit and receive antenna setup.
7	Radar Trainer: Working of Doppler radar, velocity of moving object, time and frequency measurement and other applications.
8	To perform Modulation, Demodulation and BER measurement using CDMA – DSSS Trainer.
9	To establish analog/digital communication link and transmit & receive three signals (audio, video, tone) simultaneously using Satellite Communication Trainer.
10	To study GPS Receiver, establishing link between GPS satellite & GPS trainer and measure of latitude & longitude

8EC1A IC TECHNOLOGY

B.Tech. (EC) 8th Sem. **Max. Marks: 100**

3L+0T **Exam: 3 H**

Unit	Contents	Contact hours
I	**INTRODUCTION TO IC TECHNOLOGY-** Semiconductor Substrate-Crystal defects, Electronic Grade Silicon, Czochralski Growth, Float Zone Growth, Characterization & evaluation of Crystals; Wafer Preparation- Silicon Shaping, Etching and Polishing, Chemical cleaning.	8
II	**DIFFUSION & OXIDATION -** Ficks diffusion Equation in One Dimension, Atomic model, Analytic Solution of Ficks Law, correction to simple theory, Diffusion in SiO_2. Ion Implantation and Ion Implantation Systems. Oxidation Growth mechanism and Deal-Grove Model of oxidation, Linear and Parabolic Rate co-efficient, Structure of SiO_2, Oxidation techniques and system, Oxide properties.	9
III	**CHEMICAL VAPOUR DEPOSITION AND EPITAXIAL LAYER GROWTH-** CVD for deposition of dielectric and polysilicon thick Layer – a simple CVD system, Chemical equilibrium and the law of mass action, Introduction to atmospheric CVD of dielectric, low pressure CVD of dielectric and semiconductor. Epitaxy-Vapour Phase Expitaxy, Defects in Epitaxial growth, Metal Organic Chemical Vapor Deposition, Molecular beam epitaxy.	9
IV	**PATTERN TRANSFER & ETCHING -** Introduction to photo/optical lithography, Contact/ proximity printers Projection printers, Mask generation, photo resists. Dry & Wet etching, methods for anisotropic etching, Plasma etching, Reaction ion etching (RIE).	8
V	**VLSI PROCESS INTEGRATION-** Junction and Oxide Isolation, LOCOS methods, Trench Isolation, SOI; Metallization, Planarization. Fundamental consideration for IC Processing, NMOS IC Technology, CMOS IC Technology, Bipolar IC Technology. Fault diagnosis and characterization techniques.	6
	Total	**40**

Text Books:

1. S.M. Sze (Ed), VLSI Technology, 2nd Edition, McGraw Hill (1988).

Reference Books:

S.No.	Name of Book/publication/Authors	Publication Year
1	S.K. Ghandhi, VLSI Fabrication Principles, John Wiley Inc., New York,	1983
2	IC Technology, Gouranga Bose, McGraw Hill	2013
3	C.Y. Chang and S.M.Sze (Ed),ULSI Technology, McGraw Hill Companies Inc	1996

8EC2A RADAR & TV ENGINEERING

B.Tech. (EC) 8th Sem. **Max. Marks: 100**
3L+1T **Exam Hours: 3**

Unit	Contents	Contact hours
I	**RADAR -** Radar Block diagram, frequencies and applications. Radar range equation. Continuous wave (CW) & FM radar; Moving target indicator (MTI): Delay line cancellers, blind velocity Pulse Doppler Radar. Tracking radar sequential lobbing, Conical scan and monopulse radar, Types of display, Radar receivers, Noise figure. NAVIGATIONAL AIDS - Principle of operation of Radar direction finder & range system. LORAN system, DME, TACAN, Aircraft landing systems.	10
II	T.V. systems. Block diagram of T.V. transmitters. Principles of Monochrome and colour T.V.system (PAL, SECAM, NTSC). Theory of scanning standards, Composite video signal analysis. T.V Cameras : Image orthicon, plumbicon, vidicon and CCD camera tubes. Types of Analog Monochrome and colour picture tubes,	8
III	**Processing and transmission of TV signals:** Modulation of video and sound signals, Vestigial side band transmission, Compatibility of colour and monochrome frequency interleaving & transmission of colour signals, Picture, sound and colour sub carriers. Encoding picture information. Generation of colour, colour difference and Chrominance signal modulation.TV transmission & reception antennas.	8
IV	**Basic circuits of TV RECEIVER:** Functional block diagram of T.V. receiver, R.F. Tuner, I.F. amplifier, Video detector, video amplifier, AGC, Synch. Separation, Sync. Processing and AFC. Deflection oscillators, vertical & horizontal deflection and sound system circuits. EHT generation. Common faults and their diagnosis. Basic idea of HDTV, DBS-TV and 3D-TV.	8
V	**MODERN TV SYSTEM:** Digital transmission and reception of TV signals, DISHTV, DTH and cable TV, transmission of TV signals through Satellite and Transponders, working principles of HDTV, DBS-TV, IPTV and 3D-TV. Modern TV receiver with LCD, LED and Plasma displays.	6
	Total	**40**

Text Books:

1. Monochrome and colour Television , R R Gulathi, Wiley Eastern Ltd. (2007).
2. Introduction to Radar System, 3rd, M I Skolink, MGH. (2003).

Reference Book:

S.No.	Name of Book/Publication/writer	Publication Year
1.	Television Engineering and Video System, R G Gupta, MGH	2005
2.	Television and Video Engineering , A M Dhake, MGH	1995
3.	Nathanson, F.E., "*Radar Design Principles*", McGraw-Hill Inc.,	1991
4.	Principles, Technology, Applications, Prentice Hall	1993
5.	D.K.Barton, Modern radar systems analysis, Artech House,	1988
6.	Microwave and radar engineering, G.S.B. Rao, Pearson P.	2013

8EC3A MEMS AND NANOTECHNOLOGY

B.Tech. (EC) 8th sem. **Max. Marks: 100**
3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**Introduction to Nanoelectronics:** Top Down and Bottom UP Approach, Nanotechnology Potentials, Idea of band structure – Metals, Insulators and Semiconductors. Effect of crystal size on density of states and band gap, Electronic structure of nanoparticles. Nanostructured crystals, Size and dimensionality effects – Single electron tunneling – Applications – Superconductivity, Graphenes and CNT.	08
II	**Nano Fabrication and Patterning Techniques:** Si processing methods, Cleaning/etching, Oxidation, Gettering, doping, Epitaxy. CVD & MOCVD, Physical Vapor Deposition (PVD), Liquid Phase Techniques, Self assembly and catalysis. Etching: Wet and Dry, Nanolithography, Nanoimprinting, X-Ray Lithography(XRL), Particle beam lithography(e-beam, FIB, shadow mask evaporation),	09
III	**General Characterization Techniques:** X- Ray Diffraction studies – Bragg's law – particle size – Scherrer's equation, Infrared Spectroscopy of Semiconductors, Raman Spectroscopy, Dynamic Light Scattering (DLS), NMR Spectroscopy, ESR Spectroscopy. photo electron spectroscopy(XPS)-SEM,TEM,STM, Atomic force microscopy(AFM).	09
IV	**Electrical, Magnetic, Mechanical and Optical Properties and Applications :** Electronic and electrical properties -One dimensional systems-Metallic nanowires, Quantum dots -Two dimensional systems - Quantum wells. Magnetic properties -Transport in a magnetic field. Mechanical properties, Optical properties, Evolving interfaces of Nano in NanoBiology, Nano Sensors and Nanomedicines	07
V	**MEMS and Microsystems:** Evolution of Micro Fabrication – Micro Systems and Microelectronics. Application of MEMS in Various Fields. Introduction – Substrate and Wafer, Active Substrate Material. Silicon as a substrate material, MEMS packaging. Case study on pressure sensor with packaging.	07
	TOTAL	**40**

TEXT BOOKs

1. Nano Essentials, T Pradeep, Mc Graw Hill, (2008).
2. Nanotechnology-Enabled Sensors, Kourosh Kalantar-zadehand Benjamin Fry, Springer, (2007).

REFERENCE BOOKs

SN	Name of Authors /Books /Publisher	Year of Publication
1	Fundamental of Nanoelectronics, George W. Hanson, Pearson	2009
2	Principal of Nanotechnology, G. A. Mansoori, Wiley	2005
3	Mems and Micro Systems, Mahalik, TMH	2007
4	MEMS, Gabriel, Wiley	2006
5	MEMS, A.R. Jha, CRC	2008
6	Nano Fabrication, CRC	2012
7	MEMS & Microsystems, Design and Manufacture, Tai-Ran HSU, TMH	2013

8EC4.1A COMPUTER NETWORKS

B.Tech. (EC) 8th Sem. **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**Queuing Theory**- Pure birth, Pure death & Birth-death processes,	2
	Mathematical models for M/M/1, M/M/∞, M/M/m, M/M/1/K and M/M/m/m queues. Little's formula.	5
II	**Physical and Data link layer** – OSI model & TCP/IP reference models, Line coding schemes, Packet & Circuit switching, Virtual circuit network,	3
	Framing, Simplex protocol, Simplex stop & wait protocol, Sliding window protocol, Go back N protocol, selective repeat,	3
	HDLC, PPP	2
III	**MAC Sublayer-** Static & dynamic channel allocation, Multiple Access Protocols: ALOHA, slotted ALOHA, CSMA, Token Bus, Token Ring, FDDI	4
	IEEE standards 1002.3 & 1002.5, Virtual circuit network: frame relay & ATM frame and protocol architecture,	3
	Network connection devices: Hubs, Bridges, switches, Routers and Gateways	2
IV	**Network Layer-** IPv4 & IPv6 addressing and datagram, Internetworking, Non-adaptive & Adaptive routing algorithms,	4
	Distance vector routing and Link state routing algorithms, OSPF and BGP	4
V	**Transport and Application Layer**- Client server paradigm, TCP frame format, Data traffic descriptors, QoS, Congestion and its control algorithms,	4
	Improving QoS by different queuing schemes, leaky bucket and token bucket implementation, Domain name, DNS in the internet, SMTP, FTP, WWW, HTTP	4
	Total	**40**

Text Book:

1. Forouzan, "Data Communications and Networks", 5th ed., Mcgraw-Hill, (2006).

REFERENCE BOOKS:

SN	Name of Authors/Book/Publisher	Publication Year
1	Tanenbaum, "Computer Network", 5th ed., Pearson Education	2012
2	Leon Garcia, Widjaja, "Communication Networks', 2nd ed., Mcgraw-Hill	2003
3	Stallings, "Data and Computer Communications", 10th ed., Pearson Education	2013
4	Bertsekas, Gallager, "Data Networks", 2nd ed., PHI	1992
5	Computer Networks, Dave, cengage learning	2003
6	Fundamentals of Networking and Data Communications, White, cengage learning	2013

8EC4.2A OPERATING SYSTEMS

B.Tech. (EC) 8th Sem. **Max. Marks: 100**

Exam Hours: 3

3L+0T

UNIT	CONTENTS	CONTACT HOURS
I	**INTRODUCTION** – History, Operating system services, types, responsibilities, generations, LINUX, WINDOWS.	6
II	**PROCESS MANAGEMENT**- Operations on process, Process state, Scheduling, Criteria, scheduling algorithms, Evaluation, Synchronization, Semaphores, Monitors.	8
III	**MEMORY MANAGEMENT**- Swapping, Continuous memory allocation, Paging, Pure paging, Demand paging, Page-replacement algorithms, thrashing, Example-Pentium, Disk Scheduling.	10
IV	**INFORMATION MANAGEMENT**- File and directory concept, Access methods, Protection, Free space management, Efficiency and performance, Access matrix, Capability-based systems, Program-threats, User authentication, Firewall.	10
V	**DEAD LOCKS**- System model, Dead lock characterization, Deadlock prevention, Avoidance, Detection, Recovery, Classic problems of synchronization.	6
	TOTAL	**40**

TEXT BOOKs:

1. Operating Systems, Dhamdhere, TMH, (2006).
2. Operating System:, Crowley, TMH, (1996).

REFERENCE BOOKS:

SN	Name of Authors /Books /Publisher	Year of Publication
1	Modern Operating Systems, Andrew S Tanenbaum, PHI	2008
2	Operating Systems:, Pal Chaudhury, PHI	2009
3	Operating System Principles, Peter B. Galvin, Greg Gagne, John Wiley & Sons	2002
4	Operating Systems, Gary Nutt, Pearson	2003
5	Operating Systems: Internals And Design Principles, William Stallings, Pearson	2012
6	Understanding Operating System , Flynn, cengage learning	2009
7	Operating Systems: Principles, Design and Applications , cengage learning	2002

8EC4.3A MICROCONTROLLERS & EMBEDDED SYSTEMS

B.Tech. (EC) 8th sem. **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**THE 8051 MICROCONTROLLER:** Introduction, The 8051 microcontroller hardware, I/O pins, Ports, External memory, Counters and Timers, Serial data.	8
II	**8051 ASSEMBLY LANGUAGE PROGRAMMING:** Addressing modes, External data moves, Stack,Push and Pop opcodes, Logical operations, Byte level and bit level logical operations. Arithmetic operations, Jump and call instructions, Interrupts & returns.	8
III	**REAL TIME CONTROL:** Interrupts, Multiple sources of interrupts, Non maskable sources of interrupts, Interrupt structure in 8051,Timers,Free running counter & Real Time control .	8
IV	**SYSTEM DESIGN**: Serial I/O interface, Parallel I/O ports interface, Digital and Analog interfacing methods, LED array, keyboard, Printer, Flash memory interfacing.	8
V	**INTRODUCTION TO EMBEDED SYSTEM**: Application of Microcontrollers in interfacing, MCU based measuring instruments. Real Time Operating System for System Design,Multitasking System, Task Definition in a Multitasking System, Round Robin Scheduling, Full PreemptiveScheduling, Basic study and Features of Commercial RTOS : WINCE and Embedded Linux.	8
	TOTAL	**40**

TEXT BOOKs:

1. Kenneth J.Ayala, "The 8051 Micro controller", Penram Interfacing Publishing, 1996.
2. Rajkamal, "Embedded Systems" TMH

REFERENCE BOOKS:

SN	Name of Authors /Books /Publisher	Year of Publication
1	A Textbook of Microprocessors and Microcontrollers, R. Thegarajan, Scitech Publishers	2002
2	The 8051 Microcontroller and Embedded Systems, Using Assembly and C, K Ayala & D. Gadre, Cengage Learning	2004
3	The 8051 Microcontrollers & Embedded Systems,Mazidi, ,PHI	2004
4	The 8051 Microcontroller, Ayala, cengage learning	2009
5	The 8051 Microcontroller & Embedded Systems using Assembly and C, Ayala /Gadre cengage learning	2005
6	A Textbook of Microprocessors and Microcontrollers, R.S. Kaler I. K. International Publishing House Pvt. Ltd.	2009
7	Embedded Systems & Robots: Projects Using the 8051 Microcontroller , cengage learning	
8	Embedded System Design Using C8051 , Huang , cengage learning	
9	Embedded Microcomputer System Real Time Interfacing, Valvano , cengage learning	

8EC5A RF FABRICATION LAB

B.Tech. (EC) 8th sem. **Max. Marks: 100**
3P **Exam Hours: 3**

S.No.	Contents
1.	Design and fabricate the following *Planar Transmission Lines:* • Stripline and microstrip lines • Parallel coupled striplines and microstrip lines • Slot lines and Coplanar lines Measure their S-parameters and Characteristic impedance.
2	Design and Fabricate the following; • 3-dB branchline coupler, • backward wave coupler, • Wilkinson power dividers • Low pass filters • band pass filters. Measure their S-parameters & frequency responses.
3	Design, fabrication, and measurement of RF amplifier using microwave BJT.

8EC6 INDUSTRIAL ECONOMICS & MANAGEMENT

B.Tech. (EC) 8th sem. **Max. Marks:50**
Exam Hours:3
2P

S.No.	CONTENTS
1.	Framework of industrial economics – organizational forms and alternative motives of the firm, industrial efficiency, theory of profitability, market structure, principles of costing.
2.	Approaches to industrial location analysis, Productivity analysis, Input-Output analysis, Concentration of economic power. New Industrial Policy – Critical analysis, Role of technology and entrepreneurship in industrial development.
3.	Industrial project appraisal- classification of industries, industrial legislations in India, recent trends in MNCs, LPG, FDI &joint ventures, methods of project evaluation-NPV,CBA,IRR, break-even analysis.
4.	Management – Principles of management, functions of management-planning,organizing,staffing,directing,controlling,co-ordinating,decision making
5.	Emerging issues – Total quality management, JIT , quality circle, KANBAN, benchmarking, six sigma, quality management, ,ISO 9000, ISO 14000 , Customer relationship management (CRM) .

TEXT BOOKs:

1. Subburay, Total quality management, TMH. (2011)
2. Barthwal R.R- industrial economics . wiley eastern limited

Reference Books:

1. Tirole jean – the theory of industrial organization . MIT PRESS
2. Ahluwalia I.J – industrial growth in india . Oxford university press
3. Divine P.J and R.M jones et Al- an introduction In industrial economics .George allen &Unwin limited London.
4. Peter F. drucker – principles and practice of management . Prentice hall ltd .

8EC7A VLSI DESIGN & OPTICAL FIBER LAB

B.Tech. (EC) 8th sem. **Max. Marks: 100**

3P **Exam Hours: 3**

S.No.	Contents
PART-I: Design and simulation of following VLSI circuits using EDA Tools (Software) Schematic design and make Device Level Layout of following circuits.	
1.	Design 2-input NAND, NOR and XOR using CMOS logic. Obtain its static and dynamic analysis for speed and power dissipation.
2.	Design 2X1 and 4X1 Multiplexer using Transmission Gate (TG. Obtain its static and dynamic analysis for speed and power dissipation.
3.	Design a SR-latch and D-latch using CMOS. Obtain its static and dynamic analysis for speed and power dissipation.
4.	Design a SRAM and DRAM Memory Cell. Obtain its static and dynamic analysis for speed and power dissipation.
PART-II Design and simulation of following VLSI circuits using VHDL and then burn/implement the circuits on FPGA kit for real input.	
5.	Design a 4- bit parallel Adder. Obtain its number of gates, area, and speed and power dissipation.
6.	Design a 4- bit Serial in-serial out shift register. Obtain its number of gates, area, and speed and power dissipation.
7.	Design a 4 bit binary synchronous counter. Obtain its number of gates, area, and speed and power dissipation.
PART-III. To perform following experiments based on Fiber Optic Trainer.	
8.	To set up Fiber Optic Analog link.
9.	To set up fiber Optic Digital link.
10.	Measurement of Propagation loss and numerical aperture.
11.	Characterization (VI Characteristics) of laser diode and light emitting diode.

Scheme and Syllabus

Rajasthan Technical University, Kota

B. Tech. in Civil Engineering

(Session 2018-19)

Scheme of

UNDERGRADUATE DEGREE COURSE

Computer Science and Engineering

Rajasthan Technical University, Kota

Effective from session: 2018 – 2019

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Teaching & Examination Scheme
B.Tech. : Computer Science & Engineering
2nd Year - III Semester

SN	Category	Course		Contact hrs/week			Marks				Cr
		Code	Title	L	T	P	Exm Hrs	IA	ETE	Total	
THEORY											
1	BSC	3CS2-01	Advanced Engineering Mathematics	3	0	0	3	30	120	**150**	**3**
2	HSMC	3CS1-02/ 3CS1-03	Technical Communication/ Managerial Economics and Financial Accounting	2	0	0	2	20	80	**100**	**2**
3	ESC	3CS3-04	Digital Electronics	3	0	0	3	30	120	**150**	**3**
4	PCC	3CS4-05	Data Structures and Algorithms	3	0	0	3	30	120	**150**	**3**
5		3CS4-06	Object Oriented Programming	3	0	0	3	30	120	**150**	**3**
6		3CS4-07	Software Engineering	3	0	0	3	30	120	**150**	**3**
			Sub Total	**17**	**0**	**0**		**170**	**680**	**850**	**17**
PRACTICAL & SESSIONAL											
7	PCC	3CS4-21	Data Structures and Algorithms Lab	0	0	3		45	30	**75**	**1.5**
8		3CS4-22	Object Oriented Programming Lab	0	0	3		45	30	**75**	**1.5**
9		3CS4-23	Software Engineering Lab	0	0	3		45	30	**75**	**1.5**
10		3CS4-24	Digital Electronics Lab	0	0	3		45	30	**75**	**1.5**
11	PSIT	3CS7-30	Industrial Training	0	0	1		0	0	**50**	**1**
12	SODECA	3CS8-00	Social Outreach, Discipline & Extra Curricular Activities							**25**	**0.5**
			Sub- Total	**0**	**0**	**13**		**180**	**120**	**375**	**7.5**
			TOTAL OF III SEMESTER	**17**	**0**	**13**		**350**	**800**	**1225**	**24.5**

L: *Lecture,* ***T:*** *Tutorial,* ***P:*** *Practical,* ***Cr:*** *Credits*

ETE: *End Term Exam,* ***IA:*** *Internal Assessment*

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Teaching & Examination Scheme
B.Tech. : Computer Science & Engineering
2nd Year - IV Semester

SN	Category	Course		Contact hrs/week			Marks				Cr
		Code	Title	L	T	P	Exm Hrs	IA	ETE	Total	
THEORY											
1	BSC	4CS2-01	Discrete Mathematics Structure	3	0	0	3	30	120	**150**	**3**
2	HSMC	4CS1-03/ 4CS1-02	Managerial Economics and Financial Accounting /Technical Communication	2	0	0	2	20	80	**100**	**2**
3	ESC	4CS3-04	Microprocessor & Interfaces	3	0	0	3	30	120	**150**	**3**
4	PCC	4CS4-05	Database Management System	3	0	0	3	30	120	**150**	**3**
5		4CS4-06	Theory of Computation	3	0	0	3	30	120	**150**	**3**
6		4CS4-07	Data Communication and Computer Networks	3	0	0	3	30	120	**150**	**3**
			Sub Total	**17**	**0**	**0**		**170**	**680**	**850**	**17**
PRACTICAL & SESSIONAL											
7	PCC	4CS4-21	Microprocessor & Interfaces Lab	0	0	2		30	20	**50**	**1**
8		4CS4-22	Database Management System Lab	0	0	3		45	30	**75**	**1.5**
9		4CS4-23	Network Programming Lab	0	0	3		45	30	**75**	**1.5**
10		4CS4-24	Linux Shell Programming Lab	0	0	2		30	20	**50**	**1**
11		4CS4-25	Java Lab	0	0	2		30	20	**50**	**1**
12	SODECA	4CS8-00	Social Outreach, Discipline & Extra Curricular Activities							**25**	**0.5**
			Sub- Total	**0**	**0**	**12**		**180**	**120**	**325**	**6.5**
			TOTAL OF IV SEMEESTER	**17**	**0**	**12**		**350**	**800**	**1175**	**23.5**

L: *Lecture,* ***T:*** *Tutorial,* ***P:*** *Practical,* ***Cr:*** *Credits*

ETE: *End Term Exam,* ***IA:*** *Internal Assessment*

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

V	Computer Science and Engineering	Teaching Hrs			Exm Hrs	Max. Marks			Comments
Course Code	Subject Name (Revised)	L	T	P		IA	TE	TOTAL	Common to
5CS1A	Computer Architecture	3			3	20	80	100	CS, IT
5CS2A	Digital Logic Design	3	-		3	20	80	100	
5CS3A	Telecommunication Fundamentals	3	-	-	3	20	80	100	CS, IT
5CS4A	Database Management Systems	3			3	20	80	100	CS, IT
5CS5A	Operating Systems	3	-		3	20	80	100	CS, IT
5CS6.1A	Advanced Data Structure	3	-		3	20	80	100	CS, IT
5CS6.2A	Digital Signal Processing								CS, IT
5CS6.3A	Information Theory & Coding								CS, IT
5CS7A	DataBase Lab			3		60	40	100	CS, IT
5CS8A	System Design in UML Lab.	-	-	3		45	30	75	
5CS9A	Operating Systems Simulation Lab			3		60	40	100	CS, IT
5CS10	Digital Hardware Design Lab			3		45	30	75	CS, IT
5CSDC	Discipline & Extra Curricular Activities							50	CS, IT
		18	0	12				1000	

VI	Computer Science and Engineering	Teaching Hrs			Exm Hrs	Max. Marks			Comments
Course Code	Subject Name (Revised)	L	T	P		IA	TE	TOTAL	Common to
6CS1A	Computer Networks	3	-		3	20	80	100	CS, IT
6CS2A	Design and Analysis of Algorithms	3	-		3	20	80	100	CS, IT
6CS3A	Theory Of Computation	3			3	20	80	100	CS, IT
6CS4A	Computer Graphics and Multimedia Techniques	3			3	20	80	100	
6CS5A	Embedded System Design	3	-		3	20	80	100	
6CS6.1A	Advance Topics in Operating Systems	3	-		3	20	80	100	CS, IT
6CS6.2A	Artificial Intelligence								
6CS6.3A	Human Computer Interface								CS, IT
6CS7A	Java Programming Lab			3		45	30	75	CS, IT
6CS8A	Computer Graphics & Multimedia Lab			2		30	20	50	
6CS9A	Design and Analysis of Algorithms Lab.			3		60	40	100	CS, IT
6CS10A	Embedded System Design Lab.			2		45	30	75	
6CS11A	Humanities and Social Sciences			2		30	20	50	CS, IT
6CSDC	Discipline & Extra Curricular Activities							50	CS, IT
		18	0	12				1000	

VII	Computer Science and Engineering	Teaching Hrs			Exm Hrs	Max. Marks			Comments
Course Code	Subject Name (Revised)	L	T	P		IA	TE	TOTAL	Common to
7CS1A	Cloud Computing	3	-		3	20	80	100	
7CS2A	Information System Security	3			3	20	80	100	CS, IT
7CS3A	Data Mining & Ware Housing	3			3	20	80	100	CS, IT
7CS4A	Computer Aided Design for VLSI	3	-		3	20	80	100	
7CS5A	Compiler Construction	3			3	20	80	100	
7CS6.1A	Advance DataBase Management Systems	3	-		3	20	80	100	
7CS6.2A	Robotics								
7CS6.3A	Data Compression Techniques								
7CS7A	Web Development Lab			2		30	20	50	
7CS8A	VLSI Physical Design Lab			3		45	30	75	
7CS9A	Compiler Design Lab			3		45	30	75	
7CSPR	Project-I			2		50		50	CS, IT
7CSTR	Practical Training*			2			100	100	CS, IT
7CSDC	Discipline & Extra Curricular Activities							50	CS, IT
		18	0	12				1000	

VIII	Computer Science and Engineering	Teaching Hrs			Exm Hrs	Max. Marks			Comments
Course Code	Subject Name (Revised)	L	T	P		IA	TE	TOTAL	Common to
8CS1A	Mobile Computing	3			3	20	80	100	CS, IT
8CS2A	Digital Image Processing	3	-		3	20	80	100	CS, IT
8CS3A	Distributed Systems	3			3	20	80	100	
8CS4.1A	Hardware Testing & Fault Tolerance	3	-		3	20	80	100	CS, IT
8CS4.2A	Real Time Systems								CS, IT
8CS4.3A	Information Retrieval								CS, IT
8CS5A	Unix Network Programming & Simulation Lab			3		60	40	100	
8CS6A	FPGA Lab.			3		60	40	100	
8CS7A	Digital Image Processing lab			2		30	20	50	CS, IT
8CSPR	Project-II			2		120	80	200	CS, IT
8CSSM	Seminar			2		60	40	100	CS, IT
8CSDC	Discipline & Extra Curricular Activities							50	CS, IT
		12	0	12				1000	

Syllabus of
UNDERGRADUATE DEGREE COURSE

Computer Science and Engineering



Rajasthan Technical University, Kota

Effective from session: 2018 – 2019

3CS2-01: Advanced Engineering Mathematics

Credit-3 **Max. Marks : 150 (IA:30,ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	**Random Variables:** Discrete and Continuous random variables, Joint distribution, Probability distribution function, conditional distribution.Mathematical Expectations: Moments, Moment Generating Functions, variance and correlation coefficients, Chebyshev's Inequality, Skewness and Kurtosis.	7
2	**Binomial distribution**, Normal Distribution, Poisson Distribution and their relations, Uniform Distribution, Exponential Distribution. Correlation: Karl Pearson's coefficient, Rank correlation. Curve fitting. Line of Regression.	5
3	**Historical development**, Engineering Applications of Optimization, Formulation of Design Problems as a Mathematical Programming Problems, Classification of Optimization Problems	8
4	**Classical Optimization using Differential Calculus:** Single Variable and Multivariable Optimization with & without Constraints, Langrangian theory, Kuhn Tucker conditions	6
5	**Linear Programming:** Simplex method, Two Phase Method and Duality in Linear Programming. Application of Linear Programming: Transportation and Assignment Problems.	14
	TOTAL	40

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Syllabus

II Year-III Semester: B.Tech. Computer Science and Engineering

3CS1-02/4CS1-02: Technical Communication

Credit-2 **Max. Marks : 100 (IA:20,ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**Introduction to Technical Communication-** Definition of technical communication, Aspects of technical communication, forms of technical communication, importance of technical communication, technical communication skills (Listening, speaking, writing, reading writing), linguistic ability, style in technical communication.	4
2	**Comprehension of Technical Materials/Texts and Information Design & development-** Reading of technical texts, Readingand comprehending instructions and technical manuals, Interpreting and summarizing technical texts, Note-making. Introduction of different kinds of technical documents, Information collection, factors affecting information and document design, Strategies for organization, Information design and writing for print and online media.	6
3	**Technical Writing, Grammar and Editing**- Technical writing process, forms of technical discourse, Writing, drafts and revising, Basics of grammar, common error in writing and speaking, Study of advanced grammar, Editing strategies to achieve appropriate technical style, Introduction to advanced technical communication. Planning, drafting and writing Official Notes, Letters, E-mail, Resume, Job Application, Minutes of Meetings.	8
4	**Advanced Technical Writing**- Technical Reports, types of technical reports, Characteristics and formats and structure of technical reports. Technical Project Proposals, types of technical proposals, Characteristics and formats and structure of technical proposals. Technical Articles, types of technical articles, Writing strategies, structure and formats of technical articles.	8
	TOTAL	**26**

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Syllabus

II Year-III Semester: B.Tech. Computer Science and Engineering

3CS1-03/ 4CS1-03: Managerial Economics and Financial Accounting

Credit-2 **Max. Marks : 100 (IA:20,ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**Basic economic concepts-** Meaning, nature and scope of economics, deductive vs inductive methods, static and dynamics, Economic problems: scarcity and choice, circular flow of economic activity, national income-concepts and measurement.	4
2	**Demand and Supply analysis-** Demand-types of demand, determinants of demand, demand function, elasticity of demand, demand forecasting –purpose, determinants and methods, Supply-determinants of supply, supply function, elasticity of supply.	5
3	**Production and Cost analysis-** Theory of production- production function, law of variable proportions, laws of returns to scale, production optimization, least cost combination of inputs, isoquants. Cost concepts-explicit and implicit cost, fixed and variable cost, opportunity cost, sunk costs, cost function, cost curves, cost and output decisions, cost estimation.	5
4	**Market structure and pricing theory-** Perfect competition, Monopoly, Monopolistic competition, Oligopoly.	4
5	**Financial statement analysis-** Balance sheet and related concepts, profit and loss statement and related concepts, financial ratio analysis, cash-flow analysis, funds-flow analysis, comparative financial statement, analysis and interpretation of financial statements, capital budgeting techniques.	8
	TOTAL	**26**

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

3CS3-04: Digital Electronics

Credit-3 **Max. Marks : 150 (IA:30,ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	**Fundamental concepts:** Number systems and codes, Basic logic Gates and Boolean algebra: Sign & magnitude representation, Fixed point representation, complement notation, various codes & arithmetic in different codes & their inter conversion. Features of logic algebra, postulates of Boolean algebra.Theorems of Boolean algebra.	8
2	**Minimization Techniques and Logic Gates**: Principle of Duality - Boolean expression -Minimization of Boolean expressions — Minterm – Maxterm - Sum of Products (SOP) – Product of Sums (POS) – Karnaugh map Minimization – Don't care conditions – Quine - McCluskey method of minimization.	8
3	**Digital Logic Gate Characteristics:** TTL logic gate characteristics. Theory & operation of TTL NAND gate circuitry. Open collector TTL. Three state output logic. TTL subfamilies.MOS& CMOS logic families. Realization of logic gates in RTL, DTL, ECL, C-MOS & MOSFET.	8
4	**Combinational Circuits:** Combinational logic circuit design, adder, subtractor, BCD adder encoder, decoder, BCD to 7-segment decoder, multiplexer demultiplexer.	8
5	**Sequential Circuits:** Latches, Flip-flops - SR, JK, D, T, and Master-Slave Characteristic table and equation,counters and their design, Synchronous counters – Synchronous Up/Down counters – Programmable counters – State table and state transition diagram ,sequential circuits design methodology. Registers –shift registers.	8
	TOTAL	**40**

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

3CS4-05: Data Structures and Algorithms

Credit-3 **Max. Marks: 150 (IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	Stacks: Basic Stack Operations, Representation of a Stack using Static Array and Dynamic Array, Multiple stack implementation using single array, Stack Applications: Reversing list, Factorial Calculation, Infix to postfix Transformation, Evaluating Arithmetic Expressions and Towers of Hanoi.	8
2	Queues: Basic Queue Operations, Representation of a Queue using array, Implementation of Queue Operations using Stack, Applications of Queues- Round Robin Algorithm. Circular Queues, DeQueue Priority Queues. Linked Lists:Introduction, single linked list, representation of a linked list in memory, Different Operations on a Single linked list, Reversing a single linked list, Advantages and disadvantages of single linked list, circular linked list, double linked list and Header linked list.	10
3	Searching Techniques: Sequential and binary search.Sorting Techniques: Basic concepts, Sorting by: bubble sort, Insertion sort, selection sort, quick sort, heap sort, merge sort, radix sort and counting sorting algorithms.	7
4	Trees: Definition of tree, Properties of tree, Binary Tree, Representation of Binary trees using arrays and linked lists, Operations on a Binary Tree, Binary Tree Traversals (recursive), Binary search tree, B-tree , B+ tree, AVL tree, Threaded binary tree.	7
5	Graphs: Basic concepts, Different representations of Graphs, Graph Traversals (BFS & DFS), Minimum Spanning Tree(Prims &Kruskal), Dijkstra's shortest path algorithms.Hashing: Hash function, Address calculation techniques, Common hashing functions, Collision resolution: Linear and Quadratic probing, Double hashing.	8
	TOTAL	**40**

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

3CS4-06: Object Oriented Programming

Credit-3 **Max. Marks : 150 (IA:30,ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	Introduction to different programming paradigm, characteristics of OOP, Class, Object, data member, member function, structures in C++, different access specifiers, defining member function inside and outside class, array of objects.	8
2	Concept of reference, dynamic memory allocation using new and delete operators, inline functions, function overloading, function with default arguments, constructors and destructors, friend function and classes, using this pointer.	8
3	Inheritance, types of inheritance, multiple inheritance, virtual base class, function overriding, abstract class and pure virtual function	9
4	Constant data member and member function, static data member and member function, polymorphism, operator overloading, dynamic binding and virtual function	9
5	Exception handling, Template, Stream class, File handling.	6
	TOTAL	40

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

3CS4-07: Software Engineering

Credit-3
3L+0T+0P

Max. Marks : 150 (IA:30,ETE:120)
End Term Exam: 3 Hours

SN	CONTENTS	Hours
1	Introduction, software life-cycle models, software requirements specification, formal requirements specification, verification and validation.	8
2	Software Project Management: Objectives, Resources and their estimation, LOC and FP estimation, effort estimation, COCOMO estimation model, risk analysis, software project scheduling.	8
3	Requirement Analysis: Requirement analysis tasks, Analysis principles. Software prototyping and specification data dictionary, Finite State Machine (FSM) models. Structured Analysis: Data and control flow diagrams, control and process specification behavioral modeling	8
4	Software Design: Design fundamentals, Effective modular design: Data architectural and procedural design, design documentation.	8
5	Object Oriented Analysis: Object oriented Analysis Modeling, Data modeling. Object Oriented Design: OOD concepts, Class and object relationships, object modularization, Introduction to Unified Modeling Language	8
	TOTAL	**40**

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

3CS4-21: Data Structures and Algorithms Lab

Credit-1.5 **Max. Marks :75 (IA:45,ETE:30)**

0L+0T+3P

SN	CONTENTS
1	Write a simple C program on a 32 bit compiler to understand the concept of array storage, size of a word. The program shall be written illustrating the concept of row major and column major storage. Find the address of element and verify it with the theoretical value. Program may be written for arrays up to 4-dimensions.
2	Simulate a stack, queue, circular queue and dequeue using a one dimensional array as storage element. The program should implement the basic addition, deletion and traversal operations.
3	Represent a 2-variable polynomial using array. Use this representation to implement addition of polynomials
4	Represent a sparse matrix using array. Implement addition and transposition operations using the representation.
5	Implement singly, doubly and circularly connected linked lists illustrating operations like addition at different locations, deletion from specified locations and traversal.
6	Repeat exercises 2, 3 & 4 with linked structure.
7	Implementation of binary tree with operations like addition, deletion, traversal.
8	Depth first and breadth first traversal of graphs represented using adjacency matrix and list.
9	Implementation of binary search in arrays and on linked Binary Search Tree.
10	Implementation of different sorting algorithm like insertion, quick, heap, bubble and many more sorting algorithms.

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

3CS4-22 : Object Oriented Programming Lab

Credit-1.5 **Max. Marks :75 (IA:45,ETE:30)**

0L+0T+3P

SN	CONTENTS
1	Understand the basics of C++ library, variables, data input-output.
2	C++ program using with the concept of structures.
3	Implement class and object concepts and function overloading.
4	Write programs to understand dynamic memory allocation and array of objects.
5	Program to understand different types of constructors and destructor.
6	Implement friend function to access private data of a class and usage of this pointer.
7	Write programs to understand the usage of constant data member and member function, static data member and member function in a class.
8	Implement different types of inheritance, function overriding and virtual function
9	Implement Operator overloading concepts.
10	Write programs to understand function template and class template.
11	Write programs to understand exception handling techniques.
12	Write programs to understand file handling techniques.

Office of Dean Academic Affairs
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3CS4-23: Software Engineering Lab

Credit-1.5 **Max. Marks :75 (IA:45,ETE:30)**

0L+0T+3P

SN	CONTENTS
1	Development of requirements specification, function oriented design using SA/SD, object-oriented design using UML, test case design, implementation using Java and testing. Use of appropriate CASE tools and other tools such as configuration management tools, program analysis tools in the software life cycle.
2	Develop Software Requirements Specification (SRS) for a given problem in IEEE template.
3	Develop DFD model (level-0, level-1 DFD and Data dictionary) of the project.
4	Develop structured design for the DFD model developed.
5	Developed all Structure UML diagram of the given project.
6	Develop Behavior UML diagram of the given project.
7	Manage file, using ProjectLibre project management software tool.

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3CS4-24: Digital Electronics Lab

Credit-1.5 **Max. Marks :75 (IA:45,ETE:30)**

0L+0T+3P

SN	CONTENTS
1	To verify the truth tables of basic logic gates: AND, OR, NOR, NAND, NOR. Also to verify truth table of Ex-OR, Ex-NOR (For 2, 3, & 4 inputs using gateswith 2, 3, & 4 inputs).
2	To verify the truth table of OR, AND, NOR, Ex-OR, Ex-NOR realized usingNAND& NOR gates.
3	To realize an SOP and POS expression.
4	To realize Half adder/ Subtractor& Full Adder/ Subtractor using NAND & NOR gatesand to verify their truth tables.
5	To realize a 4-bit ripple adder/ Subtractor using basic Half adder/ Subtractor& basic Full Adder/ Subtractor.
6	To verify the truth table of 4-to-1 multiplexer and 1-to-4 demultiplexer. Realize the multiplexer using basic gates only. Also to construct and 8-to-1 multiplexer and 1-to-8 demultiplexer using blocks of 4-to-1 multiplexer and 1-to-4 demultiplexer.
7	Design & Realize a combinational circuit that will accept a 2421 BCD code and drive a TIL -312 seven-segment display.
8	Using basic logic gates, realize the R-S, J-K and D-flip flops with and without clock signal and verify their truth table.
9	Construct a divide by 2,4& 8 asynchronous counter. Construct a 4-bit binary counter and ring counter for a particular output pattern using D flip flop.
10	Perform input/output operations on parallel in/Parallel out and Serial in/Serial out registers using clock. Also exercise loading only one of multiple values into the register using multiplexer. Note: As far as possible, the experiments shall be performed on bread board. However, experiment Nos. 1-4 are to be performed on bread board only.

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Syllabus of
UNDERGRADUATE DEGREE COURSE

Computer Science and Engineering



Rajasthan Technical University, Kota

Effective from session: 2018 – 2019

4CS2-01: Discrete Mathematics Structure

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Set Theory:** Definition of sets, countable and uncountable sets, Set operations, Partition of set, Cardinality (Inclusion-Exclusion & Addition Principles) Venn Diagrams, proofs of some general identities on sets. **Relation:** Definition, types of relation, composition of relations, Pictorial representation of relation, Equivalence relation, Partial ordering relation, Job-Scheduling problem. **Function:** Definition, type of functions, one to one, into and onto function, inverse function, composition of functions, recursively defined functions, pigeonhole principle. Theorem proving Techniques: Mathematical induction, Proof by contradiction. Composition of Functions. The Pigeonhole and Generalized Pigeonhole Principles.	7
3	**Propositional Logic:** Proposition, First order logic, Basic logical operation, truth tables, tautologies, Contradictions, Algebra of Proposition, logical implications, logical equivalence, predicates, Normal Forms, Universal and existential quantifiers. 2 way predicate logic. Introduction to finite state machine Finite state machines as models of physical system equivalence machines, Finite state machines as language recognizers.	8
4	**Posets, Hasse Diagram and Lattices:** Introduction, ordered set, Hasse diagram of partially, ordered set, isomorphic ordered set, well ordered set, properties of Lattices, bounded and complemented lattices. **Combinatorics:** Introduction, Permutation and combination, Binomial Theorem, Multimodal Coefficients Recurrence Relation and Generating Function: Introduction to Recurrence Relation and Recursive algorithms, linear recurrence relations with constant coefficients, Homogeneous solutions, Particular solutions, Total solutions, Generating functions, Solution by method of generating functions.	8
5	**Algebraic Structures:** Definition, Properties, types: Semi Groups, Monoid, Groups, Abelian group, properties of groups, Subgroup, cyclic groups, Cosets, factor group, Permutation groups, Normal subgroup, Homomorphism and isomorphism of Groups, example and standard results, Rings and Fields: definition and standard results.	8
6	**Graph Theory:** Introduction and basic terminology of graphs, Planer graphs, Multigraphs and weighted graphs, Isomorphic graphs, Paths, Cycles and connectivity, Shortest path in weighted graph, Introduction to Eulerian paths and circuits, Hamiltonian paths and circuits, Graph coloring, chromatic number, Isomorphism and Homomorphism of graphs, matching, vertex/edge covering.	8
	Total	40

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RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Syllabus

II Year-IV Semester: B.Tech. Computer Science and Engineering

4CS1-03/3CS1-03: Managerial Economics and Financial Accounting

Credit-2 **Max. Marks : 100 (IA:20,ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Basic economic concepts-** Meaning, nature and scope of economics, deductive vs inductive methods, static and dynamics, Economic problems: scarcity and choice, circular flow of economic activity, national income-concepts and measurement.	3
3	**Demand and Supply analysis-** Demand-types of demand, determinants of demand, demand function, elasticity of demand, demand forecasting –purpose, determinants and methods, Supply-determinants of supply, supply function, elasticity of supply.	5
4	**Production and Cost analysis-** Theory of production- production function, law of variable proportions, laws of returns to scale, production optimization, least cost combination of inputs, isoquants. Cost concepts-explicit and implicit cost, fixed and variable cost, opportunity cost, sunk costs, cost function, cost curves, cost and output decisions, cost estimation.	5
5	**Market structure and pricing theory-** Perfect competition, Monopoly, Monopolistic competition, Oligopoly.	4
6	**Financial statement analysis-** Balance sheet and related concepts, profit and loss statement and related concepts, financial ratio analysis, cash-flow analysis, funds-flow analysis, comparative financial statement, analysis and interpretation of financial statements, capital budgeting techniques.	8
	TOTAL	26

Office of Dean Academic Affairs
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4CS1-02/3CS1-02: Technical Communication

Credit-2 **Max. Marks : 100 (IA:20,ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Introduction to Technical Communication-** Definition of technical communication, Aspects of technical communication, forms of technical communication, importance of technical communication, technical communication skills (Listening, speaking, writing, reading writing), linguistic ability, style in technical communication.	3
3	**Comprehension of Technical Materials/Texts and Information Design & development-** Reading of technical texts, Reading and comprehending instructions and technical manuals, Interpreting and summarizing technical texts, Note-making. Introduction of different kinds of technical documents, Information collection, factors affecting information and document design, Strategies for organization, Information design and writing for print and online media.	6
4	**Technical Writing, Grammar and Editing**- Technical writing process, forms of technical discourse, Writing, drafts and revising, Basics of grammar, common error in writing and speaking, Study of advanced grammar, Editing strategies to achieve appropriate technical style, Introduction to advanced technical communication. Planning, drafting and writing Official Notes, Letters, E-mail, Resume, Job Application, Minutes of Meetings.	8
5	**Advanced Technical Writing**- Technical Reports, types of technical reports, Characteristics and formats and structure of technical reports. Technical Project Proposals, types of technical proposals, Characteristics and formats and structure of technical proposals. Technical Articles, types of technical articles, Writing strategies, structure and formats of technical articles.	8
	TOTAL	26

Office of Dean Academic Affairs
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4CS3-04: Microprocessor & Interfaces

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	Introduction to Microprocessors, microcontroller; 8085 Microprocessor Architecture, pin description, Bus concept and organization; concept of multiplexing and de-multiplexing of buses; concept of static and dynamic RAM, type of ROM, memory map.	7
3	Software architecture registers and signals, Classification of instruction, Instruction set, addressing modes, Assembly Language Programming and Debugging, Programming Technique, instruction Format and timing.	8
4	Advance Assembly Language Programming, Counter and time delay; types of Interrupt and their uses, RST instructions and their uses, 8259 programmable interrupt controller; Macros, subroutine; Stack-implementation and uses with examples; Memory interfacing.	8
5	8085 Microprocessor interfacing:, 8255 Programmable Peripheral Interface, 8254 programmable interval timer, interfacing of Input/output device, 8279 Key board/Display interface.	8
6	Microprocessor Application: Interfacing scanned multiplexed display and liquid crystal display, Interfacing and Matrix Keyboard, MPU Design; USART 8251, RS232C and RS422A, Parallel interface-Centronics and IEEE 488.	8
	Total	40

Office of Dean Academic Affairs
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4CS4-05: Database Management System

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Introduction to database systems:** Overview and History of DBMS. File System v/s DBMS.Advantage of DBMS Describing and Storing Data in a DBMS.Queries in DBMS.Structure of a DBMS. **Entity Relationship model:** Overview of Data Design Entities, Attributes and Entity Sets, Relationship and Relationship Sets. Features of the ER Model- Key Constraints, Participation Constraints, Weak Entities, Class Hierarchies, Aggregation, Conceptual Data Base, and Design with ER Model- Entity v/s Attribute, Entity vs Relationship Binary vs Ternary Relationship and Aggregation v/s ternary Relationship Conceptual Design for a Large Enterprise.	7
3	**Relationship Algebra and Calculus:** Relationship Algebra Selection and Projection, Set Operations, Renaming, Joints, Division, Relation Calculus, Expressive Power of Algebra and Calculus. **SQL queries programming and Triggers:** The Forms of a Basic SQL Query, Union, and Intersection and Except, Nested Queries, Correlated Nested Queries, Set-Comparison Operations, Aggregate Operators, Null Values and Embedded SQL, Dynamic SQL, ODBC and JDBC, Triggers and Active Databases.	8
4	**Schema refinement and Normal forms:** Introductions to Schema Refinement, Functional Dependencies, Boyce-Codd Normal Forms, Third Normal Form, Normalization-Decomposition into BCNF Decomposition into 3-NF.	8
5	**Transaction Processing:** Introduction-Transaction State, Transaction properties, Concurrent Executions. Need of Serializability, Conflict vs. View Serializability, Testing for Serializability, Recoverable Schedules, Cascadeless Schedules.	8
6	**Concurrency Control: Implementation of Concurrency:** Lock-based protocols, Timestamp-based protocols, Validation-based protocols, Deadlock handling, **Database Failure and Recovery:** Database Failures, Recovery Schemes: Shadow Paging and Log-based Recovery, Recovery with Concurrent transactions.	8
	Total	40

Office of Dean Academic Affairs
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4CS4-06: Theory Of Computation

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	Finite Automata & Regular Expression: Basic machine, Finite state machine, Transition graph, Transition matrix, Deterministic and non-deterministic finite automation, Equivalence of DFA and NDFA, Decision properties, minimization of finite automata, Mealy & Moore machines. Alphabet, words, Operations, Regular sets, relationship and conversion between Finite automata and regular expression and vice versa, designing regular expressions, closure properties of regular sets, Pumping lemma and regular sets, Myhill- Nerode theorem , Application of pumping lemma, Power of the languages.	7
3	Context Free Grammars (CFG), Derivations and Languages, Relationship between derivation and derivation trees, leftmost and rightmost derivation, sentential forms, parsing and ambiguity, simplification of CFG, normal forms, Greibach and Chomsky Normal form , Problems related to CNF and GNF including membership problem.	8
4	Nondeterministic PDA, Definitions, PDA and CFL, CFG for PDA, Deterministic PDA, and Deterministic PDA and Deterministic CFL , The pumping lemma for CFL's, Closure Properties and Decision properties for CFL, Deciding properties of CFL.	8
5	Turing Machines: Introduction, Definition of Turing Machine, TM as language Acceptors and Transducers, Computable Languages and functions, Universal TM & Other modification, multiple tracks Turing Machine. Hierarchy of Formal languages: Recursive & recursively enumerable languages, Properties of RL and REL, Introduction of Context sensitive grammars and languages, The Chomsky Hierarchy.	8
6	Tractable and Untractable Problems: P, NP, NP complete and NP hard problems, Un-decidability, examples of these problems like vertex cover problem, Hamiltonian path problem, traveling sales man problem.	8
	Total	40

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4CS4-07: Data Communication and Computer Networks

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	Introductory Concepts: Network hardware, Network software, topologies, Protocols and standards, OSI model, TCP model, TCP/IP model, Physical Layer: Digital and Analog Signals, Periodic Analog Signals, Signal Transmission, Limitations of Data Rate, Digital Data Transmission, Performance Measures, Line Coding, Digital Modulation, Media and Digital Transmission System	7
3	Data Link Layer: Error Detection and Correction, Types of Errors, Two dimensional parity check, Detection verses correction, Block Coding, Linear Block Coding, Cyclic Codes, Checksum, Standardized Polynomial Code, Error Correction Methods, Forward Error Correction, Protocols: Stop and wait, Go-back-N ARQ, Selective Repeat ARQ, Sliding window, Piggy backing, Pure ALOHA, Slotted ALOHA, CSMA/CD, CSMA/CA	9
4	Network Layer: Design issues, Routing algorithms: IPV4, IPV6, Address mapping: ARQ, RARQ, Congestion control, Unicast, Multicast, Broadcast routing protocols, Quality of Service, Internetworking	8
5	Transport Layer: Transport service, Elements of transport protocols, User Datagram Protocol, Transmission Control Protocol, Quality of service, Leaky Bucket and Token Bucket algorithm	8
6	Application Layer**:** WWW, DNS, Multimedia, Electronic mail, FTP, HTTP, SMTP, Introduction to network security	7
	Total	40

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4CS4-21: Microprocessor & Interfaces Lab

Credit: 1 **Max. Marks: 50(IA:30, ETE:20)**

0L+0T+2P

List of Experiments:

1. Add the contents of memory locations XX00 &XX01 & place the result in memory location XX02.
2. Add the 16 bit numbers stored in memory location & store the result in another memory location.
3. Transfer a block of data from memory location XX00 to another memory location XX00 in forward & reverse order.
4. Write a program to swap two blocks of data stored in memory.
5. Write a program to find the square of a number.
6. Write a main program and a conversion subroutine to convert Binary to its equivalent BCD.
7. Write a program to find largest & smallest number from a given array.
8. Write a program to Sort an array in ascending & descending order.
9. Write a program to multiply two 8 bit numbers whose result is 16 bit.
10. Write a program of division of two 8 bit numbers.
11. Generate square wave from SOD pin of 8085 & observe on CRO.
12. Write a program to perform traffic light control operation.
13. Write a program to control the speed of a motor.

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4CS4-22: Database Management System Lab

Credit: 1.5 **Max. Marks: 75(IA:45, ETE:30)**

0L+0T+3P

List of Experiments:

1. Design a Database and create required tables. For e.g. Bank, College Database
2. Apply the constraints like Primary Key, Foreign key, NOT NULL to the tables.
3. Write a SQL statement for implementing ALTER,UPDATE and DELETE.
4. Write the queries to implement the joins.
5. Write the query for implementing the following functions: MAX (), MIN (), AVG () and COUNT ().
6. Write the query to implement the concept of Integrity constrains.
7. Write the query to create the views.
8. Perform the queries for triggers.
9. Perform the following operation for demonstrating the insertion , updation and deletion
10. Using the referential integrity constraints.
11. Write the query for creating the users and their role.

Data Base Designing Project:

For better understanding students (group of 3-4 students) should design data base for any data base project, understand the requirement and design methodology of project by its own.

Some example of data base design project like:

College management system, Inventory management system and Hospital management system.

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4CS4-23: Network Programming Lab

Credit: 1.5 **Max. Marks: 75(IA:45, ETE:30)**

0L+0T+3P

List of Experiments:

1. Study of Different Type of LAN& Network Equipments.
2. Study and Verification of standard Network topologies i.e. Star, Bus, Ring etc.
3. LAN installations and Configurations.
4. Write a program to implement various types of error correcting techniques.
5. Write a program to implement various types of framing methods.
6. Write two programs in C: hello_client and hello_server
    a. The server listens for, and accepts, a single TCP connection; it reads all the data it can from that connection, and prints it to the screen; then it closes the connection
    b. The client connects to the server, sends the string “Hello, world!”, then closes the connection
7. Write an Echo_Client and Echo_server using TCP to estimate the round trip time from client to the server. The server should be such that it can accept multiple connections at any given time.
8. Repeat Exercises 6 & 7 for UDP.
9. Repeat Exercise 7 with multiplexed I/O operations.
10. Simulate Bellman-Ford Routing algorithm in NS2.

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4CS4-24: Linux Shell Programming Lab

Credit: 1 **Max. Marks: 50(IA:30, ETE:20)**

0L+0T+2P

List of Experiments:

1. Use of Basic Unix Shell Commands: ls, mkdir, rmdir, cd, cat, banner, touch, file, wc, sort, cut, grep, dd, dfspace, du, ulimit.
2. Commands related to inode, I/O redirection and piping, process control commands, mails.
3. Shell Programming: Shell script based on control structure- **If-then-fi, if-then-else-if, nested if-else, to find:**
   - 3.1 Greatest among three numbers.
   - 3.2 To find a year is leap year or not.
   - 3.3 To input angles of a triangle and find out whether it is valid triangle or not.
   - 3.4 To check whether a character is alphabet, digit or special character.
   - 3.5 To calculate profit or loss.
4. Shell Programming - Looping- while, until, for loops
   - 4.1 Write a shell script to print all even and odd number from 1 to 10.
   - 4.2 Write a shell script to print table of a given number
   - 4.3 Write a shell script to calculate factorial of a given number.
   - 4.4 Write a shell script to print sum of all even numbers from 1 to 10.
   - 4.5 Write a shell script to print sum of digit of any number.
5. Shell Programming - case structure, use of break
   - 5.1 Write a shell script to make a basic calculator which performs addition, subtraction, Multiplication, division
   - 5.2 Write a shell script to print days of a week.
   - 5.3 Write a shell script to print starting 4 months having 31 days.
6. Shell Programming - Functions
   - 6.1 Write a shell script to find a number is Armstrong or not.
   - 6.2 Write a shell script to find a number is palindrome or not.
   - 6.3 Write a shell script to print Fibonacci series.
   - 6.4 Write a shell script to find prime number.
   - 6.5 Write a shell script to convert binary to decimal and decimal to binary
7. Write a shell script to print different shapes- Diamond, triangle, square, rectangle, hollow square etc.
8. Shell Programming – Arrays
   - 8.1 Write a C program to read and print elements of array.
   - 8.2 Write a C program to find sum of all array elements.
   - 8.3 Write a C program to find reverse of an array.
   - 8.4 Write a C program to search an element in an array.
   - 8.5 Write a C program to sort array elements in ascending or descending order.

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4CS4-25: Java Lab

Credit: 1 **Max. Marks: 50(IA:30, ETE:20)**

0L+0T+2P

List of Experiment:

1. Develop an in depth understanding of programming in Java: data types, variables, operators, operator precedence, Decision and control statements, arrays, switch statement, Iteration Statements, Jump Statements, Using break, Using continue, return.
2. Write Object Oriented programs in Java: Objects, Classes constructors, returning and passing objects as parameter, Inheritance, Access Control, Using super, final with inheritance Overloading and overriding methods, Abstract classes, Extended classes.
3. Develop understanding to developing packages & Interfaces in Java: Package, concept of CLASSPATH, access modifiers, importing package, Defining and implementing interfaces.
4. Develop understanding to developing Strings and exception handling: String constructors, special string operations, character extraction, searching and comparing strings, string Buffer class. Exception handling fundamentals, Exception types, uncaught exceptions, try, catch and multiple catch statements. Usage of throw, throws and finally.
5. Develop applications involving file handling: I/O streams, File I/O.
6. Develop applications involving concurrency: Processes and Threads, Thread Objects, Defining and Starting a Thread, Pausing Execution with Sleep, Interrupts, Joins, and Synchronization.

Indicative List of exercises:

7. Programs to demonstrate basic concepts e.g. operators, classes, constructors, control & iteration statements, recursion etc. such as complex arithmetic, matrix arithmetic, tower of Hanoi problem etc.
8. Development of programs/projects to demonstrate concepts like inheritance, exception handling, packages, interfaces etc. such as application for electricity department, library management, ticket reservation system, payroll system etc.
9. Development of a project to demonstrate various file handling concepts.
10. Develop applications involving Applet: Applet Fundamentals, using paint method and drawing polygons. It is expected that each laboratory assignments to given to the students with an aim to In order to achieve the above objectives.

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5CS1A COMPUTER ARCHITECTURE (Common to CS & IT)

Class: V Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. Schedule per Week Lectures: 3	**Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Introduction to Computer Architecture and Organization. Von Neuman Architecture, Flynn Classification. Register Transfer and Micro operations: Register transfer language, Arithmetic Micro-operations, Logic Micro-operations, Shift Micro-operations, Bus and memory transfers. Computer Organization and Design: Instruction cycle, computer registers, common bus system, computer instructions, addressing modes, design of a basic computer
II	Central Processing Unit: General register organization, stack organization, Instruction formats, Data transfer and manipulation, program control. RISC, CISC characteristics. Pipeline and Vector processing: Pipeline structure, speedup, efficiency, throughput and bottlenecks. Arithmetic pipeline and Instruction pipeline.
III	Computer Arithmetic: Adder, Ripple carry Adder, carry look Ahead Adder, Multiplication: Add and Shift, Array multiplier and Booth Multiplier, Division: restoring and Non-restoring Techniques. Floating Point Arithmetic: Floating point representation, Add, Subtract, Multiplication, Division.
IV	Memory Organization: RAM, ROM, Memory Hierarchy, Organization, Associative memory, Cache memory, and Virtual memory: Paging and Segmentation.
V	Input-Output Organization: Input-Output Interface, Modes of Transfer, Priority Interrupt, DMA, IOP processor.

Text/References:

1. Computer Organization and Architecture - William Stallings (Pearson Education Asia)

2. Computer Organization and Architecture -John P. Hayes (McGraw -Hill)

3. Computer Organization -V. Carl. Hamacher (McGraw-Hill)

5CS2A Digital Logic Design

Class: V Sem. B.Tech.	Evaluation
Branch: Computer Engg. Schedule per Week Lectures: 3	**Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Hardware Description Languages and their use in digital logic design. VHDL: Modelling Concepts, Lexical Elements & Syntax Descriptions, Scalar Data types & Operations, Sequential Statements, Composite Data Types & Operations, Basic Modelling Constructs. Case Study: VHDL Simulation of Ripple Carry, & Look Ahead carry Adders.
II	VHDL: Subprograms, Packages & Use Clauses, Aliases, Resolved Signals, Components & Configurations, Generate Statements, Concurrent Statements. Use of VHDL in simulation and synthesis.
III	Clocked Sequential circuits. Design steps for synchronous sequential circuits. Design of a sequence detector. Moore and Mealy Machines. Design using JK flip-flops and D flip-flops. State reduction, State assignment, Algorithmic State Charts, converting ASM charts to hardware, one-hot state assignment. Considerations of clock skew, set-up time, hold-time and other flip-flop parameters, timing constraints. Programmable Logic Devices. Read-only memory. Boolean function implementation through ROM. PLD, PGA, PLA, PAL, FPGA.
IV	Event-driven Circuits. Design procedure for asynchronous circuits, stable and unstable states, races, race-free assignments. State reduction of incompletely specified machines. Compatibility and state reduction procedure. Hazards in combinational networks. Dynamic hazards, Function Hazards, and Essential Hazards. Eliminating hazards.
V	Field Programmable Gate Arrays: Introduction, Logic Elements & programmability, Interconnect structures & programmability, Extended Logic Elements, SRAM, Flash Memory & Antifuse Configuration, Case Studies of Altera Stratix & Xilinx Virtex-II pro. Technology Mapping for FPGAs: Logic Synthesis, Lookup Table Technology Mapping.

Text Book:

1. Brian Holdsworth and Clive Woods. Digital Logic Design. Newnes (Elsevier). [Available in Indian Edition].
2. Ashenden, The Designer's Guide to VHDL, Elsevier.
3. Stephen D. Brown, et.al., Field Programmable Gate Arrays, Kluwer Academic Publishers.
4. Scott Hauck, André DeHon , Reconfigurable computing: the theory and practice of FPGA based computation, Morgan Kauffman
5. Zvi Kohavi: Switching and Finite Automata Theory. TMH.
6. Parag K. Lala, Practical Digital Logic Design and Testing. PHI
7. Stephen H. Unger, The essence of logic circuits. Wiatrowski & House.

5CS3A TELECOMMUNICATION FUNDAMENTALS (Common to CS & IT)

Class: V Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. Schedule per Week Lectures: 3	**Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Data Transmission: Terminology, Frequency, spectrum, bandwidth, analog and digital transmission, Transmission impairments, channel capacity, Transmission Media. Wireless Transmission: Antenna and antenna gain. Network Reference Models (OSI/ISO and TCP/IP) Physical Layer: Line Encoding Schemes. Concept of bit period, effect of clock skew, Synchronous and Asynchronous communication. Data Link Layer: Functions of data link layer and design issues Flow Control: Flow control in loss less and lossy channels using stop-and-wait, sliding window protocols. Performance of protocols used for flow control.
II	Error Control Coding: Error Detection, Two Dimensional Parity Checks, and Internet Checksum. Polynomial Codes, Standardized polynomial codes, error detecting capability of a polynomial codes. Linear codes, performance of linear codes, error detection & correction using linear codes. Data Link Control: HDLC & PPP including frame structures. MAC sublayer: Channel Allocation Problem, Pure and slotted Aloha, CSMA, CSMA/CD, collision free multiple access. Throughput analysis of pure and slotted Aloha. Ethernet Performance.
III	Wireless LAN: Hidden node and Exposed node Problems, RTS/CTS based protocol, 802.11 Architecture, protocol stack, Physical layer, MAC Sublayer. Bluetooth Architecture and Protocol Stack Data Link Layer Switching: Bridges (Transparent, Learning and Spanning Tree), Virtual LANs
IV	Multiplexing: Frequency division, time division (Synchronous and statistical) multiplexing. ADSL, DS1 and DS3 carriers. Multiple Accesses: TDMA frame structure, TDMA Burst Structure, TDMA Frame efficiency, TDMA Superframe structure, Frame acquisition and synchronization, Slip rate in digital terrestrial networks. Switching: Qualitative description of Space division, time division and space-time-space division switching.
V	Spread Spectrum Techniques: Direct sequence(DSSS) & frequency hopping(FHSS); Performance consideration in DSSS & FHSS; Code division Multiple access (CDMA): frequency & channel specifications, forward & reverse CDMA channel, pseudo noise(PN) sequences, m-sequence, gold sequence, orthogonal code, gold sequences, Walsh codes, synchronization, power control, handoff, capacity of CDMA system, IMT-2000, WCDM

Text/References:

1. Stallings, Data and computer communication, 8th ed. Pearson
2. Tri.T.Ha, Digital Satellite Communications, 2/e, Tata McGraw Hill
3. Alberto Leon-Garcia, Indra Widjaja, COMMUNICATION NETWORKS, 2nd ed., TMH
4. Wireless Communications, 2/e, Rappaport, PHI

5. Analysis of Computer and Communication Networks, ISBN: 0387744363, Fayez Gebali, 2008, Springer-verlag, 1st Ed.

5CS4A DATABASE MANAGEMENT SYSTEMS (Common to CS & IT)

Class: V Sem. B.Tech	**Evaluation**
Branch: Computer Engg. Schedule per Week **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	INTRODUCTION TO DATABASE SYSTEMS: Overview and History of DBMS. File System v/s DBMS .Advantage of DBMS Describing and Storing Data in a DBMS. Queries in DBMS. Structure of a DBMS.
II	ENTITY RELATIONSHIP MODEL: Overview of Data Design Entities, Attributes and Entity Sets, Relationship and Relationship Sets. Features of the ER Model- Key Constraints, Participation Constraints, Weak Entities, Class Hierarchies, Aggregation, Conceptual Data Base, Design with ER Model-Entity v/s Attribute, Entity vs Relationship Binary vs Ternary Relationship and Aggregation v/s ternary Relationship Conceptual Design for a Large Enterprise.
III	RELATIONSHIP ALGEBRA AND CALCULUS**:** Relationship Algebra Selection and Projection, Set Operations, Renaming, Joints, Division, Relation Calculus, Expressive Power of Algebra and Calculus.
IV	SQL QUERIES PROGRAMMING AND TRIGGERS: The Forms of a Basic SQL Query, Union, Intersection and Except, Nested Queries ,Correlated Nested Queries, Set-Comparison Operations, Aggregate Operators, Null Values and Embedded SQL, Dynamic SQL, ODBC and JDBC, Triggers and Active Databases.
V	SCHEMA REFINEMENT AND NORMAL FORMS: Introductions to Schema Refinement, Functional Dependencies, Boyce-Codd Normal Forms, Third Normal Form, Normalization-Decomposition into BCNF Decomposition into 3-NF.

References:

1. H.f. Korth and Silberschatz: Database Systems Concepts, McGraw Hill
2. Almasri and S.B. Navathe: Fundamentals of Database Systems,
3. C.J. Date: Data Base Design, Addison Wesley
4. Hansen and Hansen : DBM and Design, PHI

5CS5A OPERATING SYSTEMS (Common to CS & IT)

Class: V Sem. B.Tech.	Evaluation
Branch: Computer Engg. Schedule per Week Lectures: 3	**Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Introduction and need of operating system, layered architecture/logical structure of operating system, Type of OS, operating system as resource manager and virtual machine, OS services, BIOS, System Calls/Monitor Calls, Firmware- BIOS, Boot Strap Loader. Process management- Process model, creation, termination, states & transitions, hierarchy, context switching, process implementation, process control block, Basic System calls- Linux & Windows. Threads- processes versus threads, threading, concepts, models, kernel & user level threads, thread usage, benefits, multithreading models.
II	Interprocess communication- Introduction to message passing, Race condition, critical section problem, mutual exclusion with busy waiting- disabling interrupts, lock variables, strict alteration, Peterson's solution, TSL instructions, busy waiting, sleep and wakeup calls, semaphore, monitors, classical IPC problems. Process scheduling- Basic concepts, classification, CPU and I/O bound, CPU scheduler- short, medium, long-term, dispatcher, scheduling:- preemptive and non-preemptive, Static and Dynamic Priority, Co-operative & Non-cooperative, Criteria/Goals/Performance Metrics, scheduling algorithms- FCFS, SJFS, shortest remaining time, Round robin, Priority scheduling, multilevel queue scheduling, multilevel feedback queue scheduling, Fair share scheduling.
III	Deadlock- System model, resource types, deadlock problem, deadlock characterization, methods for deadlock handling, deadlock prevention, deadlock avoidance, deadlock detection, recovery from deadlock. Memory management- concepts, functions, logical and physical address space, address binding, degree of multiprogramming, swapping, static & dynamic loading- creating a load module, loading, static & dynamic linking, shared libraries, memory allocation schemes- first fit, next fit, best fit, worst fit, quick fit. Free space management- bitmap, link list/free list, buddy's system, memory protection and sharing, relocation and address translation.
IV	Virtual Memory- concept, virtual address space, paging scheme, pure segmentation and segmentation with paging scheme hardware support and implementation details, memory fragmentation, demand paging, pre-paging, working set model, page fault frequency, thrashing, page replacement algorithms- optimal, NRU, FIFO, second chance, LRU, LRU-approximation clock, WS clock; Belady's anomaly, distance string; design issues for paging system- local versus global allocation policies, load control, page size, separate instruction and data spaces, shared pages, cleaning policy, TLB (translation look aside buffer) reach, inverted page table, I/O interlock, program structure, page fault handling, Basic idea of MM in Linux & windows.
V	File System- concepts, naming, attributes, operations, types, structure, file organization & access(Sequential, Direct ,Index Sequential) methods, memory mapped files, directory structures- one level, two level, hierarchical/tree, acyclic graph, general graph, file system mounting, file sharing, path name, directory operations, overview of file system in Linux & windows. Input/Output subsystems- concepts, functions/goals, input/output devices- block and character, spooling, disk structure & operation, disk attachment, disk storage capacity, disk scheduling algorithm- FCFS, SSTF, scan scheduling, C-scan schedule.

Text/Reference Books:

1. A. Silberschatz and Peter B Galvin: Operating System Principals, Wiley India Pvt. Ltd.
2. Achyut S Godbole: Operating Systems, Tata McGraw Hill
3. Tanenbaum: Modern Operating System, Prentice Hall.
4. DM Dhamdhere: Operating Systems – A Concepts Based Approach, Tata McGraw Hill
5. Charles Crowly: Operating System A Design – Oriented Approach, Tata McGraw Hill.

5CS6.1A ADVANCED DATA STRUCTURE (Common to CS & IT)

Class: V Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. Schedule per Week Lectures: 3	**Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	ADVANCED TREES: Definitions, Operations on Weight Balanced Trees (Huffman Trees), 2-3 Trees and Red- Black Trees. Dynamic Order Statistics, Interval Tree; Dictionaries.
II	MERGEABLE HEAPS: Mergeable Heap Operations, Binomial Trees, Implementing Binomial Heaps and its Operations, 2-3-4. Trees and 2-3-4 Heaps. Amortization analysis and Potential Function of Fibonacci Heap, Implementing Fibonacci Heap.
III	GRAPH THEORY DEFINITIONS: Definitions of Isomorphic Components. Circuits, Fundamental Circuits, Cut-sets. Cut- Vertices Planer and Dual graphs, Spanning Trees, Kuratovski's two Graphs. GRAPH THEORY ALGORITHMS: Algorithms for Connectedness, Finding all Spanning Trees in a Weighted Graph, Breadth First and Depth First Search, Topological Sort, Strongly Connected Components and Articulation Point. Single Min-Cut Max-Flow theorem of Network Flows. Ford-Fulkerson Max Flow Algorithms.
IV	SORTING NETWORK: Comparison network, zero-one principle, bitonic sorting and merging network sorter. Priority Queues and Concatenable Queues using 2-3 Trees. Operations on Disjoint sets and its union-find problem, Implementing Sets.
V	NUMBER THEORITIC ALGORITHM: Number theoretic notions, Division theorem, GCD, recursion, Modular arithmetic, Solving Modular Linear equation, Chinese Remainder Theorem, power of an element, Computation of Discrete Logarithms, primality Testing and Integer Factorization.

Text/References:

1. Cormen, Leiserson, Rivest: Introduction to Algorithms, Prentice Hall of India.

2. Horowitz and Sahani: Fundamental of Computer algorithms.

3. Aho A.V , J.D Ulman: Design and analysis of Algorithms, Addison Wesley

4. Brassard : Fundamental of Algorithmics, PHI.

5CS6.2A DIGITAL SIGNAL PROCESSING

Class: V Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. Schedule per Week Lectures: 3	**Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	INTRODUCTION : Discrete time signals and systems, properties of discrete time systems, Linear time invariant systems - discrete time. Properties of LTI systems and their block diagrams. Convolution, Discrete time systems described by difference equations.
II	Fourier Transform: Discrete time Fourier transform for periodic and aperiodic signals. Properties of DTFT. Z-transform: The region of convergence for the Z-transform. The Inverse Z-transform. Properties of Z transform.
III	SAMPLING: Mathematical theory of sampling. Sampling theorem. Ideal & Practical sampling. Interpolation technique for the reconstruction of a signal from its samples. Aliasing. Sampling in freq. domain. Sampling of discrete time signals.
IV	THE DISCRETE FOURIER TRANSFORMS (DFT): Properties of the DFT, Linear Convolution using DFT. Efficient computation of the DFT: Decimation–in-Time and Decimation-in frequency FFT Algorithms.
V	FILTER DESIGN TECHNIQUES: Structures for discrete-time systems- Block diagram and signal flow graph representation of LCCD (LCCD – Linear Constant Coefficient Difference) equations, Basic structures for IIR and FIR systems, Transposed forms. Introduction to filter Design: Butterworth & Chebyshev.IIR filter design by impulse invariance & Bilinear transformation. Design of FIR filters by Windowing: Rectangular, Hamming & Kaiser.

Text/References:

1. Oppenheim, Discrete-Time Signal Processing, 2/e, Pearson Education
2. Proakis, Digital Signal Processing, 4/e, Pearson Education
3. S.K.Mitra, Digital Signal Processing, 2/e, Tata McGraw Hill

5CS6.3A INFORMATION THEORY & CODING

Class: V Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. Schedule per Week Lectures: 3	**Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Introduction to information theory. Uncertainty, Information and Entropy, Information measures for continuous random variables, source coding theorem. Discrete Memory less channels, Mutual information, Conditional entropy.
II	Source coding schemes for data compaction: Prefix code, Huffman code, Shanon-Fane code & Hempel-Ziv coding channel capacity. Channel coding theorem. Shannon limit.
III	Linear Block Code: Introduction to error connecting codes, coding & decoding of linear block code, minimum distance consideration, conversion of non systematic form of matrices into systematic form.
IV	Cyclic Code: Code Algebra, Basic properties of Galois fields (GF) polynomial operations over Galois fields, generating cyclic code by generating polynomial, parity check polynomial. Encoder & decoder for cyclic codes.
V	Convolutional Code: Convolutional encoders of different rates. Code Tree, Trllis and state diagram. Maximum likelihood decoding of convolutional code: The viterbi Algorithm fee distance of a convolutional code.

Text/References

1. Digital Communication, Simon Haykin, Wiley.

5CS7A DATABASE LAB (Common to CS & IT)

Class: V Sem. B.Tech.	Evaluation
Branch: Computer Engg. Schedule per Week Practical Hrs.: 3	Examination Time = Four (4) Hours Maximum Marks = 100 [Sessional/Mid-term (60) & End-term (40)]

Objectives: At the end of the semester, the students should have clearly understood and implemented the following:

1. Stating a database design & application problem.
2. Preparing ER diagram
3. Finding the data fields to be used in the database.
4. Selecting fields for keys.
5. Normalizing the database including analysis of functional dependencies.
6. Installing and configuring the database server and the front end tools.
7. Designing database and writing applications for manipulation of data for a stand alone and shared data base including concepts like concurrency control, transaction roll back, logging, report generation etc.
8. Get acquainted with SQL.

In order to achieve the above objectives, it is expected that each students will chose one problem. The implementation shall being with the statement of the objectives to be achieved, preparing ER diagram, designing of database, normalization and finally manipulation of the database including generation of reports, views etc. The problem may first be implemented for a standalone system to be used by a single user.

All the above steps may then be followed for development of a database application to be used by multiple users in a client server environment with access control. The application shall NOT use web techniques.

One exercise may be assigned on creation of table, manipulation of data and report generation using SQL.

Suggested Tool:

For standalone environment, Visual FoxPro or any similar database having both the database and manipulation language may be used.

For multi-user application, MYSql is suggested. However, any other database may also be used. For front end, VB.Net, Java, VB Script or any other convenient but currently used by industry may be chosen.

Indicative List of exercise:

1. Student information system for your college.

2. Student grievance registration and redressal system.

3. A video library management system for a shop.
4. Inventory management system for a hardware/ sanitary item shop.
5. Inventory management system for your college.
6. Guarantee management system for the equipments in your college.

5CS8A SYSTEM DESIGNS in UML LAB

Class: V Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. Schedule per Week Practical Hrs : 3	**Examination Time = Four (4) Hours Maximum Marks = 75 [Sessional/Mid-term (45) & End-term (30)]**

Objectives:

1. The students shall be able to use following modules of UML for system description, implementation and finally for product development.

   - Capture a business process model.
   - The User Interaction or Use Case Model - describes the boundary and interaction between the system and users. Corresponds in some respects to a requirements model.
   - The Interaction or Communication Model - describes how objects in the system will interact with each other to get work done.
   - The State or Dynamic Model - State charts describe the states or conditions that classes assume over time. Activity graphs describe the workflows the system will implement.
   - The Logical or Class Model - describes the classes and objects that will make up the system.
   - The Physical Component Model - describes the software (and sometimes hardware components) that make up the system.
   - The Physical Deployment Model - describes the physical architecture and the deployment of components on that hardware architecture.

The students are expected to use the UML models, prepare necessary documents using UML and implement a system. Some hardware products like digital clock, digital camera, washing machine controller, air conditioner controller, an elctronic fan regulator, an elementary mobile phone etc. may also be chosen.

The students shall be assigned one problem on software based systems and another involving software as well as hardware.

5CS9A OPERATING SYSTEMS SIMULATION LAB (Common to CS & IT)

Class: V Sem. B.Tech.	Evaluation
Branch: Computer Engg. Schedule per Week Practical Hrs : 3	Examination Time = Four (4) Hours Maximum Marks = 100 [Sessional/Mid-term (60) & End-term (40)]

Objectives:

Understand the basic functions of operating systems.

In depth knowledge of the algorithms used for implementing the tasks performed by the operating systems.

Understand & simulate strategies used in Linux & Windows operating systems.

Develop aptitude for carrying out research in the area of operating system.

Suggested Tools:

Operating system simulator- MOSS preferably on Linux platform (Available for free download from http://www.ontko.com/moss/).

Recommended Exercises:

A. Exercises shall be given on simulation of algorithms used for the tasks performed by the operating systems. Following modules of the simulator may be used:

Scheduling

Deadlock

Memory Management Systems

File system simulator

Algorithms described in the text may be assigned. The simulation results such as average latency, hit & Miss Ratios or other performance parameters may be computed.

B. One exercise shall be on simulation of algorithms reported in the recent conferences/ journals and reproducing the results reported therein.

5CS10A DIGITAL HARDWARE DESIGN LAB (Common to CS & IT)

Class: V Sem. B.Tech.	Evaluation
Branch: Computer Engg. Schedule per Week Practical Hrs : 3	Examination Time = Four (4) Hours Maximum Marks = 75 [Sessional/Mid-term (45) & End-term

Objectives: At the end of course, the students shall be able to

- Should be able to design datapath for digital systems
- Create a digital system using discrete digital ICs
- Design a hard wired / micro-programmed control circuit
- Simulate a digital datapath in Hardware Description Language
- Understand IC descriptions and select proper IC in a given circuit based on its timing characteristics

Suggested Methodology and tools: Hardware description language like Verilog /VHDL can be used for simulation.

The exercise shall involve design of datapath, its simulation and finally realization on breadboard. Library of digital ICs have to be built. Similarly, manuals of Digital IC families have to be placed in the laboratories for reference by students.

Suggested Exercises

- Create a microprocessor from ALU 74181. For this, the students may design a small instruction set and attach necessary registers and suitable control unit to realize a microprocessor.
- Simulate and realize a Cordic calculator.
- Simulate & realize a Four bit Adder
    - Design and simulation of a 4-bit Adder
    - VHDL/Verilog HDL (Hardware description language)
    - Interfacing 7-segment decoder
- Combinational Multiplier
    - 4x4-bit multiplier
    - Binary-to-BCD conversion
    - Timing Constraints
- CRC checksum generator & verifier
- Realizing a carry look ahead adder

6CS1A COMPUTER NETWORKS (Common to CS & IT)

Class: VI Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. Schedule per Week Lectures: 3	**Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]**

NOTE: The first 2 lectures shall be devoted to review of the basis architectures and responsibilities of different layers.

Units	Contents of the subject
I	Network layer-design issue, routing algorithms: Distance vector, link state, hierarchical, Broadcast routing. Congestion control: congestion prevention policies, congestion control in Datagram subnets, load shedding, jitter control, Leaky bucket and token bucket algorithms.
II	Internetworking: Differences in networks, Tunneling, Internetwork routing, Fragmentation Network layer in the Internet: IPv4 classful and classless addressing, subnetting Network layer protocols(only working and purpose; packet headers etc. not included), Differences in IPV6 over IPV4. Routing to Mobile Hosts and Mobile IP
III	Elements of transport protocols: addressing, connection establishment and release, flow control and buffering, multiplexing and demultiplexing, crash recovery, introduction to UDP protocol. Principles of Reliable Data Transfer: Reliable data transfer over a perfectly reliable channel, Channel with bit errors and Lossy Channel with bit errors.
IV	Transport Layer in the Internet: Introduction to TCP, TCP service Model, TCP Header and segment structure, TCP connection establishment and release, transmission policy, timer management, Transactional TCP. Mobile TCP TCP Congestion Control: Fairness, TCP delay modeling.
V	Application Layer: World Wide Web (WWW), Domain Name System (DNS), E-mail, File Transfer Protocol (FTP), Introduction to Network security. P2P File Sharing: Centralized Directory, Query flooding, exploiting heterogeneity.

Text/References:

1. Tanenbaum; Computer Network, 4th Ed., Pearson.
2. Kurose; Computer Networking, 3rd Ed., Pearson.
3. Peterson, Davie; Computer Networks, 4rd Ed., ELSEVIER

6CS2A DESIGN AND ANALYSIS OF ALGORITHMS(Common to CS & IT)

Class: VI Sem. B.Tech.	Evaluation
Branch: **Computer Engg.** **Schedule per** **Week Lectures:** **3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	BACKGROUND: Review of Algorithm Complexity, Order Notations: definitions and calculating complexity. DIVIDE AND CONQUER METHOD: Binary Search, Merge Sort, Quick sort and Strassen's matrix multiplication algorithms. GREEDY METHOD: Knapsack Problem, Job Sequencing, Optimal Merge Patterns and Minimal Spanning Trees.
II	DYNAMIC PROGRAMMING: Matrix Chain Multiplication. Longest Common Subsequence and 0/1 Knapsack Problem. BRANCH AND BOUND: Traveling Salesman Problem and Lower Bound Theory. Backtracking Algorithms and queens problem.
III	PATTERN MATCHING ALGORITHMS: Naïve and Rabin Karp string matching algorithms, KMP Matcher and Boyer Moore Algorithms. ASSIGNMENT PROBLEMS: Formulation of Assignment and Quadratic Assignment Problem.
IV	RANDOMIZED ALGORITHMS. Las Vegas algorithms, Monte Carlo algorithms, randomized algorithm for Min-Cut, randomized algorithm for 2- SAT. Problem definition of Multicommodity flow, Flow shop scheduling and Network capacity assignment problems.
V	PROBLEM CLASSES NP, NP-HARD AND NP-COMPLETE: Definitions of P, NP-Hard and NP-Complete Problems. Decision Problems. Cook's Theorem. Proving NP-Complete Problems - Satisfiability problem and Vertex Cover Problem. Approximation Algorithms for Vertex Cover and Set Cover Problem.

Text/References:

1. Cormen, Leiserson, Rivest: Introduction to Algorithms, Prentice Hall of India.

2. Horowitz and Sahani: Fundamental of Computer algorithms.

3. Aho A.V , J.D Ulman: Design and analysis of Algorithms, Addison Wesley

6CS3A THEORY OF COMPUTATION (Common to CS & IT)

Class: VI Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. Schedule per Week Lectures: 3,	**Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Finite Automata & Regular Expression: Basic Concepts of finite state system, Deterministic and non-deterministic finite automation and designing regular expressions, relationship between regular expression & Finite automata minimization of finite automation mealy & Moore Machines.
II	Regular Sets of Regular Grammars: Basic Definition of Formal Language and Grammars. Regular Sets and Regular Grammars, closure proportion of regular sets, Pumping lemma for regular sets, decision Algorithms for regular sets, Myhell_Nerod Theory & Organization of Finite Automata.
III	Context Free Languages& Pushdown Automata: Context Free Grammars – Derivations and Languages – Relationship between derivation and derivation trees – ambiguity – simplification of CEG – Greiback Normal form – Chomsky normal forms – Problems related to CNF and GNF Pushdown Automata: Definitions – Moves – Instantaneous descriptions – Deterministic pushdown automata – Pushdown automata and CFL - pumping lemma for CFL - Applications of pumping Lemma.
IV	Turing Machines: Turing machines – Computable Languages and functions – Turing Machine constructions – Storage in finite control – multiple tracks – checking of symbols – subroutines – two way infinite tape. Undecidability: Properties of recursive and Recursively enumerable languages – Universal Turing Machines as an undecidable problem – Universal Languages – Rice's Theorems.
V	Linear bounded Automata Context Sensitive Language: Chomsky Hierarchy of Languages and automata, Basic Definition & descriptions of Theory & Organization of Linear bounded Automata Properties of context-sensitive languages

Text/References

1. Aho, Hopcroft and Ullman, Introduction to Automata Theory, Formal Languages and Computation, Narosa
2. Cohen, Introduction to Computer Theory, Addison Wesley.
3. Papadimitriou, Introduction to Theory of Computing, Prentice Hall.

6CS4A COMPUTER GRAPHICS & MULTIMEDIA TECHNIQUES.

Class: VII Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Introduction to Raster scan displays, Storage tube displays, refreshing, flicking, interlacing, color monitors, display processors, resolution, Introduction to Interactive. Computer Graphics: Picture analysis, Overview of programmer's model of interactive graphics, Fundamental problems in geometry. Scan Conversion: point, line, circle, ellipse polygon, Aliasing, and introduction to Anti Aliasing (No anti aliasing algorithm).
II	2D & 3D Co-ordinate system**:** Homogeneous Co-ordinates, Translation, Rotation, Scaling, Reflection, Inverse transformation, Composite transformation. Polygon Representation, Flood Filling, Boundary filling. Point Clipping, Cohen-Sutherland Line Clipping Algorithm, Polygon Clipping algorithms.
III	Hidden Lines & Surfaces: Image and Object space, Depth Buffer Methods, Hidden Facets removal, Scan line algorithm, Area based algorithms. Curves and Splines: Parametric and Non parametric Representations, Bezier curve, B-Spline Curves.
IV	Rendering: Basic illumination model, diffuse reflection, specular reflection, phong shading, Gourand shading, ray tracing, color models like RGB, YIQ, CMY, HSV
V	Multimedia components, Multimedia Input/Output Technologies: Storage and retrieval technologies, Architectural and telecommunication considerations. Animation: Introduction, Rules, problems and Animation techniques.

Text/References:

1. J. Foley, A. Van Dam, S. Feiner, J. Hughes: Computer Graphics- Principles and Practice, Pearson
2. Hearn and Baker: Computer Graphics, PHI
3. Multimedia Systems Design, Prabhat Andleigh and Thakkar, PHI.

4. Multimedia Information Networking, N.K.Sharda, PHI..

6CS5A EMBEDDED SYSTEM DESIGN

Class: VI Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Introduction to embedded systems hardware needs; typical and advanced, timing diagrams, memories (RAM, ROM, EPROM). Tristate devices, Buses, DMA, UART and PLD's. Built-ins on the microprocessor.
II	Interrupts basics, ISR;Context saving, shared data problem. Atomic and critical section, Interrupt latency. Survey of software architectures, Round Robin, Function queue scheduling architecture, Use of real time operating system.
III	RTOS, Tasks, Scheduler, Shared data reentrancy, priority inversion, mutex binary semaphore and counting semaphore. Inter task communication, message queue, mailboxes and pipes, timer functions, events. Interrupt routines in an RTOS environment.
IV	Embedded system software design using an RTOS. Hard real-time and soft real time system principles, Task division, need of interrupt routines, shared data.
V	Embedded Software development tools. Host and target systems, cross compilers, linkers, locators for embedded systems. Getting embedded software in to the target system. Debugging techniques. Testing on host machine, Instruction set emulators, logic analysers. In-circuit emulators and monitors. Regional

Text Books:

1. John Davies, MSP430 Microcontroller Basics, Elsevier, 2008.
2. Andrew N. Sloss et.al. ARM System Developers Guide, ELSEVIER
3. Muhammad Ali Mazidi et.al., The 8051 Microcontroller & Embedded Systems, Pearson
4. Embedded System Design, A Unified Hardware/Software Introduction, Frank Vahid / Tony Givargis, 2006 reprint, John Wiley Student Edition.

6CS6.1A ADVANCE TOPICS IN OPERATING SYSTEMS (Common to CS & IT)

Class: VI Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. Schedule per Week Lectures: 3	**Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Operating system structures – policies & mechanism, Structures- monolithic, layered, virtual machines, micro kernel, exokernels, client- server model. Examples from Linux & Windows. Threads Advance Concepts– Libraries- Pthreads, win32 threads, Java threads, Introduction to threading issues, system calls, cancellation, signal handling, thread pool, thread specific data, window threads, Linux threads, Solaris Threads. Message Passing System – Need of Message Passing Systems, design issues, naming, synchronization, Implementation–buffering and delivery; mailboxes; RPC & RMI. Examples Systems – Linux, Windows.
II	File System- file system layouts, file system implementation, contagious allocation, link list allocation, indexed allocation, file allocation table, virtual file system, directory implementation- linear list and hash table. File System reliability and integrity. I/O system: device drivers/ controllers, busses and interfaces- USB, IDE, SCSI, IEEE1394, RAID system, disk caching and buffering, disk management-disk formatting, RAID Structure, boot block, bad block, swap-space management. System Security: Security Problems, Program Threats, System Network Threats, Cryptography as a Security Tool, User Authentication, Implementing Security Defenses, Firewalling to Protect Systems and Network, Computer Security Classifications. Overview of security in Windows. [4]
III	The Linux OS: Unix Vs Linux, Design Principles, Kernel Structure, components Kernel Modules, Shell- usage, types; An overview of- Process Management, Thread Management and Scheduling, Memory Management, Process Scheduling in Linux, File System structure & implementation, I/O Management, Network File System, Inter-process Communications, Booting and login process, security.[3]
IV	The Window OS: Design Principles, System Components- Hardware Abstraction layer, Kernel, Executives; Environmental Subsystems- MS-DOS Environment, 16-bit Windows Environment, Win32 API, POSIX subsystem; Exception and Interrupts; An overview of-memory management, process management and thread; Process Scheduling in Windows; File Systems: Internal Layout, recovery, Volume Management and Fault Tolerance, FAT and NTFS, Security features, window registry, OS organizations.[3]

V	Multiprocessor Operating Systems: Architecture of Multiprocessor Systems, Overview of Multiprocessor OS, Kernal Structure and Multiprocessing support in Linux & Windows, Process Synchronization- Queued Lock, Spin Lock, Sleep Lock; Process Scheduling. Multimedia Operating System- Introduction to Multimedia & Data Compression- concepts, common graphics file formats, common audio file formats; Video server, Process management- real time scheduling; Multimedia file systems, Multimedia file storage mechanisms, Video sever organization.[2] Mobile Operating System- Windows CE, Palm OS, Symbian OS, JAVA card, Multos.

Text/Reference Books:

1. DM Dhamdhere: Operating Systems – A Concepts Based Approach, Tata McGraw Hill
2. Achyut S Godbole: Operating Systems, Tata McGraw Hill
3. Tanenbaum: Modern Operating System, Prentice Hall
4. A. Silberschatz and Peter B Galvin: Operating System Principals, Wiley India Pvt. Ltd.
5. Charles Crowly: Operating System A Design – Oriented Approach, Tata McGraw Hill.
6. Bach, Design of Unix Operating Systems.

6CS6.2A ARTIFICIAL INTELLIGENCE

Class: VII Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. Schedule per Week Lectures: 3	**Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Meaning and definition of artificial intelligence, Various types of production systems, Characteristics of production systems, Study and comparison of breadth first search and depth first search. Techniques, other Search Techniques like hill Climbing, Best first Search. A* algorithm, AO* algorithms etc, and various types of control strategies.
II	Knowledge Representation, Problems in representing knowledge, knowledge representation using propositional and predicate logic, comparison of propositional and predicate logic, Resolution, refutation, deduction, theorem proving, inferencing, monotonic and nonmonotonic reasoning.
III	Probabilistic reasoning, Baye's theorem, semantic networks scripts schemas, frames, conceptual dependency and fuzzy logic, forward and backward reasoning.
IV	Game playing techniques like minimax procedure, alpha-beta cut-offs etc, planning, Study of the block world problem in robotics, Introduction to understanding and natural languages processing.
V	Introduction to learning, Various techniques used in learning, introduction to neural networks, applications of neural networks, common sense, reasoning, some example of expert systems.

Text Books & References:

1. Artificial Intelligence: Elaine Rich, Kevin Knight, Mc-Graw Hill.
2. Introduction to AI & Expert System: Dan W. Patterson, PHI.
3. Artificial Intelligence by Luger (Pearson Education)
4. Russel & Norvig, Artificial Intelligence: A Modern Approach, Prentice-Hall

6CS6.3A HUMANCOMPUTER INTERFACE (Common to CS & IT)

Class: VI Sem. B.Tech.	**Evaluation**
Branch: Comp. **Engg. Schedule per** **Week Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	The Human: input-output channels, Human memory, thinking, emotions, individual differences, psychology and the design of interactive systems. The Computer: Text entry devices with focus on the design of key boards, positioning, pointing and drawing, display devices. The Interaction: Models of interaction, ergonomics, interaction styles, elements of WIMP interfaces, interactivity, experience, engagement and fun. Paradigms for Interaction.
II	Design Process: The process of design, user focus, scenarios, navigation design screen design and layout, iteration & prototyping. Usability Engineering Design rules: Principles to support usability, standards, guidelines, rules and heuristics, HCI patterns.
III	Evaluation Techniques: Definition and goals of evaluation, evaluation through expert analysis and user participation, choosing an evaluation method. User support, requirement, approaches, adaptive help systems, designing user support systems
IV	Cognitive methods: Goals and task hierarchies, linguistic models, challenges of display based systems, physical and device models, cognitive architectures.
V	Communications and collaborations models: Face to Face communication, conversations, Text based communication, group working. Task Analysis: Differences between task analysis and other techniques, task decomposition, knowledge based analysis, ER based analysis, sources of information and data collection, use of task analysis.

Text/References:

1. Human Computer Interaction; Alan Dix et.al, 3rd ed., Pearson

6CS7A JAVA PROGRAMMING LAB (Common to CS & IT)

Class: VI Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. Schedule per Week Practical Hrs.: 3	**Examination Time = Four (4) Hours Maximum Marks = 75 [Sessional/Mid-term (60) & End-term**

Objectives: At the end of the semester, the students should have clearly understood and implemented the following:

1. Develop an in depth understanding of programming in Java: data types, variables, operators, operator precedence, Decision and control statements, arrays, switch statement, Iteration Statements, Jump Statements, Using break, Using continue, return.
2. Write Object Oriented programs in Java: Objects, Classes constructors, returning and passing objects as parameter, Inheritance, Access Control, Using super, final with inheritance Overloading and overriding methods, Abstract classes, Extended classes.
3. Develop understanding to developing packages & Interfaces in Java: Package, concept of CLASSPATH, access modifiers, importing package, Defining and implementing interfaces.
4. Develop understanding to developing Strings and exception handling: String constructors, special string operations, character extraction, searching and comparing strings, string Buffer class. Exception handling fundamentals, Exception types, uncaught exceptions, try, catch and multiple catch statements. Usage of throw, throws and finally.
5. Develop applications involving file handling: I/O streams, File I/O.
6. Develop applications involving concurrency: Processes and Threads, Thread Objects, Defining and Starting a Thread, Pausing Execution with Sleep, Interrupts, Joins, and Synchronization.
7. Develop applications involving Applet: Applet Fundamentals, using paint method and drawing polygons.

It is expected that each laboratory assignments to given to the students with an aim to In order to achieve the above objectives

Indicative List of exercises:

7. Programs to demonstrate basic concepts e.g. operators, classes, constructors, control & iteration statements, recursion etc. such as complex arithmetic, matrix arithmetic, tower of Hanoi problem etc.
8. Development of programs/projects to demonstrate concepts like inheritance, exception handling, packages, interfaces etc. such as application for electricity department, library management, ticket reservation system, payroll system etc.
9. Development of a project to demonstrate various file handling concepts.
10. Development of a project to demonstrate various applet concepts.

6CS8A Computer graphics & multimedia lab

Class: VI Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Practical Hrs:2**	**Examination Time = Three (3) Hours** **Maximum Marks = 50** **[Mid-term (60) & End-term (40)]**

S. No.	List of Experiment
1	Implementation of Line, Circle and ellipse attributes
2	Two Dimensional transformations - Translation, Rotation, Scaling, Reflection, Shear
3	Composite 2D Transformations
4	Cohen Sutherland 2D line clipping and Windowing
5	Sutherland – Hodgeman Polygon clipping Algorithm
6	Three dimensional transformations - Translation, Rotation, Scaling
7	Composite 3D transformations
8	Drawing three dimensional objects and Scenes
9	Generating Fractal images
10	To plot a point (pixel) on the screen
11	To draw a straight line using DDA Algorithm
12	Implementation of mid-point circle generating Algorithm
13	Implementation of ellipse generating Algorithm
14	To translate an object with translation parameters in X and Y directions
15	To scale an object with scaling factors along X and Y directions
16	To rotate an object with a certain angle about origin
17	Perform the rotation of an object with certain angle about an arbitrary point

6CS9A DESIGN AND ANALYSIS OF ALGORITHMS Lab(Common to CS & IT)

Class: VI Sem. B.Tech.	**Evaluation**
Branch: Comp. Engg. Schedule per Week Practical Hrs : 3	**Examination Time = Four (4) Hours Maximum Marks = 100 [Sessional/Mid-term (60) & End-term (40)]**

Objectives: Upon successful completion of this course, students should be able to:

- Prove the correctness and analyze the running time of the basic algorithms for those classic problems in various domains;
- Apply the algorithms and design techniques to solve problems;
- Analyze the complexities of various problems in different domains.

Suggested Tools: For implementation and estimation of running time on various sizes of input(s) or output(s) as the case may be, Linux platform is suggested.

Suggested Exercises:

A. It is expected that teachers will assign algorithms to the students for estimation of time & space complexity. Algorithms reported in various research journals may be chosen by the teachers.

B. Problem on designing algorithms to meet complexity constraints may be assigned. For example, a problem on design, analysis and implementation for transposing a sparse matrix requiring not more than one pass from the original matrix may be assigned.

C. A guide to such problems is given below:

1. Exploring a Binary Heap: Consider a binary heap containing n numbers (the root stores the greatest number). You are given a positive integer k < n and a number x. You have to determine whether the k^{th} largest element of the heap is greater than x or not. Your algorithm must take O(k) time. You may use O(k) extra storage.

2. Merging two search trees: You are given two height balanced binary search trees T and T', storing m and n elements respectively. Every element of tree T is smaller than every element of tree T'. Every node u also stores height of the subtree rooted at it. Using this extra information how can you merge the two trees in time O(log m + log n) (preserving both the height balance and the order)?

3. Complete binary tree as an efficient data-structure:
   You are given an array of size n (n being a power of two). All the entries of the array are initialized to zero. You have to perform a sequence of the following online operations :
   1. (i) Add(i,x) which adds x to the entry A[i].
   2. (ii) Report sum(i,j) = sum of the entries in the array from indices i to j for any 0 < i < j <= n.

It can be seen easily that we can perform the first operation in O(1) time whereas the second operation may cost O(n) in worst case. Your objective is to perform these operations efficiently. Give a data-structure which will guarantee O(log n) time per operation.

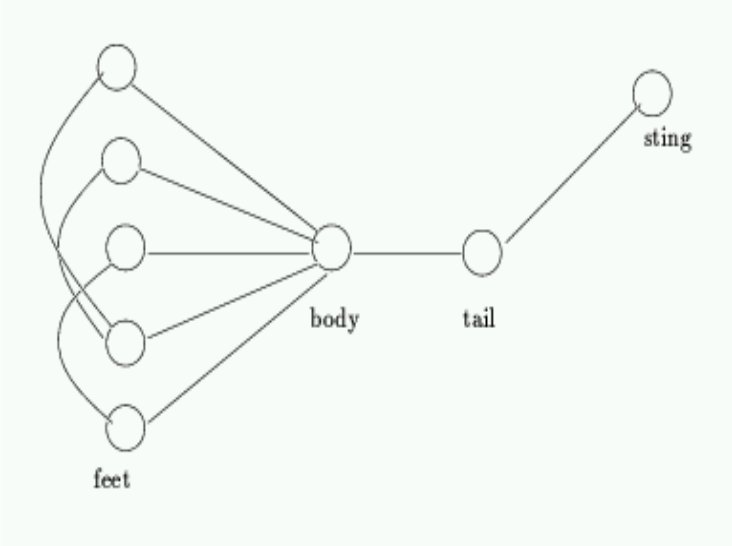
4. Problems on Amortized Analysis
    a. Delete-min in constant time!!! Consider a binary heap of size n, the root storing the smallest element. We know that the cost of insertion of an element in the heap is O(log n) and the cost of deleting the smallest element is also O(log n). Suggest a valid potential function so that the amortized cost of insertion is O(log n) whereas amortized cost of deleting the smallest element is O(1).
    b. Implementing a queue by two stack
    c. Show how to implement a queue with two ordinary stacks so that the amortized cost of each Enqueue and each Dequeue operation is O(1).

5. Computing a spanning tree having smallest value of largest edge weight: Describe an efficient algorithm that, given an undirected graph G, determines a spanning tree of G whose largest edge weight is minimum over all spanning trees of G.

6. Shortest Path Problems:
    i. From a subset of vertices to another subset of vertices
        a. Given a directed graph G(V,E), where edges have nonnegative weights. S and D are two disjoint subsets of the set of vertices. Give an O(|V| log |V| + |E|) time algorithm to find the shortest path among the set of paths possible from any node in S to any node in D.
    ii. Paths in Directed Acyclic Graph
        a. Counting the number of paths
        Given two nodes u,v in a directed acyclic graph G(V,E). Give an O(|E|) time algorithm to count all the paths from u to v.
        b. Path passing through a subset of nodes
        Given two nodes u,v and a set of vertices w_1, w_2,...,w_k in a directed acyclic graph G(V,E). Give an O(|E|) time algorithm to output a path(if exists) from u to v which passes through each of the nodes w_1,...,w_k. If there is no such path then your algorithm must report that "no such path exists".

7. Searching for a friend:
    You are standing at a crossing from where there emerge four roads extending to infinity. Your friend is somewhere on one of the four roads. Youdonotknowonwhichroadheisandhowfarheisfromyou. You have to walk to your friend and the total distance traveled by you must be at most a constant times the actual distance of your friend from you. In terminology of algorithms, you should traverse O(d) distance, where d is the distance of your friend from you.

8. A simple problem on sorted array: Design an O(n)-time algorithm that, given a real number x and a sorted array S of n numbers, determines whether or not there exist two elements in S whose sum is exactly x .
9. Finding the decimal dominant in linear time: You are given n real numbers in an array. A number in the array is called a decimal dominant if it occurs more than n/10 times in the array. Give an O(n) time algorithm to determine if the given array has a decimal dominant.
10. Finding the first one: You are given an array of infinite length containing zeros followed by ones. How fast can you locate the first one in the array?
11. Searching for the Celebrity: Celebrity is a person whom everybody knows but he knows nobody. You have gone to a party. There are total n persons in the party. Your job is to find the celebrity in the party. You can ask questions of the form Does Mr. X know Mr. Y ?. You will get a binary answer for each such question asked. Find the celebrity by asking only O(n) questions.
12. Checking the Scorpion: An n-vertex graph is a scorpion if it has a vertex of degree 1(the sting) connected to a vertex of degree two (the tail) connected to a vertex of degree n-2 (the body) connected to the other n-3 (the feet). Some of the feet may be connected to other feet. Design an algorithm that decides whether a given adjacency matrix represents a scorpion by examining only O(n) entries.



13. Endless list: You are having a pointer to the head of singly linked list. The list either terminates at null pointer or it loops back to some previous location(not necessarily to the head of the list). You have to determine whether the list loops back or ends at a null location in time proportional to the length of the list. You can use at most a constant amount of extra storage.
14. Nearest Common Ancestor:
Given a rooted tree of size n. You receive a series of online queries: "Give nearest common ancestor of u, v ". Your objective is to preprocess the tree in O(n) time to get a data structure of size O(n) so that you can answer any such query in O(log n) time.

6CS10A Embedded System Design Lab.

Class: VI Sem. B.Tech.	**Evaluation**
Branch: Comp. Engg. Schedule per Week Practical Hrs :2	**Examination Time = Four (4) Hours Maximum Marks = 75 [Sessional/Mid-term (60) & End-term (40)]**

Course Objectives

Upon successful completion of the course, students will be able to design simple embedded systems and develop related software. Students also learn to work in a team environment and communicate the results as written reports and oral presentations.

Suggested Microcontroller Platform: Texas Instruments MSP430, ARM 9, 68HC12, 8051.

It is assumed that there are 14 weeks in the semester and about 5 to 6 experiments will be carried out. More experiments are provided to bring in variation.

Experiment #0

Get familiar with the microcontroller kit and the development software. Try the sample programs that are supplied to get familiar with the Microcontroller.

Experiment #1

a) Blink an LED which is connected to your microcontroller using the built-in timer in the microcontroller. Assume that the LED should be on for x milliseconds and off for y milliseconds; assume that these values are stored in memory locations X and Y. We should be able to change the value of x and y and rerun the program.

b) Consider an alternate way to program this application. Here, the microcontroller turns the LED on and waits in a busy loop to implement a delay of x milliseconds. Then it turns the LED off and waits in a busy loop to implement a delay of y milliseconds. How do you compare these two solutions?

Experiment #2

Assume that in Experiment #1, the values of x and y have been chosen to be 200 and 500 respectively. When the LED blinking program runs, pressing a key on the keyboard should generate an interrupt to the microcontroller. If the key that has been pressed is a numeric key, the value of x and y must be interchanged by the interrupt service routine. If the key that has been pressed is not a numeric key, then the LED must be turned off for 2 seconds before resuming the blinking.

Experiment #3

If your microcontroller kit has an LCD interface, write a program to display a character string on the LCD. Assume that the string is stored at a location

STRING and consists of alphanumeric characters. The string is null-terminated. Modify your program to scroll the displayed string from left to right.

Experiment #4

Modern microcontrollers usually have an in-built Digital-to-Analog and Analog-to-Digital converter. Use the built-in DAC to generate voltage waveforms such as (a) pulse train (b) triangular waveform (c) sinusoidal waveform. Observe these waveforms on an oscilloscope.

Experiment #5

Your microcontroller may have a built-in temperature sensor. If not, interface an external temperature sensor to the microcontroller. Write a program to take several measurements of temperature at regular intervals and display the average temperature on the LCD display. Test if the readings change when the ambient temperature changes.

Experiment #6

Your microcontroller may have a built-in ADC. Build a voltmeter that can measure stable voltages in a certain range. The measured value must be displayed on the LCD display. Measure the same voltage using a multimeter and record the error in measurement. Tabulate the error for several values of the voltage.

Experiment #7

Build a simple security device based on the microcontroller kit. Interface an external motion sensor to the microcontroller. An alarm must be generated if motion is sensed in a specified region. There must be a provision to record the time at which the intrusion was detected. Similarly, there must be a provision to turn the alarm off by pressing a key.

Experiment #8

A voltage waveform v(t) is available as an input to the microcontroller. We must continuously check the waveform and record the maximum value of the waveform and display the maximum value on the LCD display. Test the program by using a DC supply to generate v(t) and varying the DC value.

6CS11A Humanities and Social Sciences (Common to CS & IT)

Class: VI Sem. B.Tech.	Evaluation
Branch: Computer Engg. Schedule per Week Practical Hrs :2	Examination Time = Three (3) Hours Maximum Marks = 50 [Sessional/Mid-term (30) & End-term (20)]

Units	Contents of the subject
I	India-brief history of Indian constitution ,framing-features fundamental rights,duties,directive principles of states,History of Indian National movement,Socio economic growth after independence.
II	Society-Social groups-concepts and types,socialization-concept theory,social control:concept,social problem in contempory India,status and role.
III	The fundamental of Economics-meaning,definition animportance of economics,Logic of choice,central economic problems,positive and normative approaches,economic systems-socialism and capitalism.
IV	Microeconomics-Law of demand and supply,utility approach,indifferencecurves,elasticity of demand & supply and applications,consumer surplus,Law of returns to factors and returns to scale.
V	Macroeconomics- concept relating to National product-National income and its measurement,simple Keynesian theory,simple multiplier, money and banking.Meaning,concept of international trade,determination of exchange rate,Balance of payments.

References:

1. Economics-Lipsey & Chrystal, Oxford Univ.Press,2010
Nordhaus, William, Samuelson,Paul-2009-10

7CS1A Cloud Computing

Class: VII Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Introduction Cloud Computing: Nutshell of cloud computing, Enabling Technology, Historical development, Vision, feature Characteristics and components of Cloud Computing. Challenges, Risks and Approaches of Migration into Cloud. Ethical Issue in Cloud Computing, Evaluating the Cloud's Business Impact and economics, Future of the cloud. Networking Support for Cloud Computing. Ubiquitous Cloud and the Internet of Things
II	Cloud Computing Architecture: Cloud Reference Model, Layer and Types of Clouds, Services models, Data center Design and interconnection Network, Architectural design of Compute and Storage Clouds. Cloud Programming and Software: Fractures of cloud programming, Parallel and distributed programming paradigms-MapReduce, Hadoop , High level Language for Cloud. Programming of Google App engine,
III	Virtualization Technology: Definition, Understanding and Benefits of Virtualization. Implementation Level of Virtualization, Virtualization Structure/Tools and Mechanisms , Hypervisor VMware, KVM, Xen. Virtualization: of CPU, Memory, I/O Devices, Virtual Cluster and Resources Management, Virtualization of Server , Desktop, Network, and Virtualization of data-center
IV	Securing the Cloud : Cloud Information security fundamentals, Cloud security services, Design principles, Policy Implementation, Cloud Computing Security Challenges, Cloud Computing Security Architecture . Legal issues in cloud Computing. Data Security in Cloud: Business Continuity and Disaster Recovery , Risk Mitigation , Understanding and Identification of Threats in Cloud, SLA-Service Level Agreements, Trust Management
V	*Cloud Platforms in Industry:* Amazon web services , Google AppEngine, Microsoft Azure Design, Aneka: Cloud Application Platform -Integration of Private and Public Clouds *Cloud applications:* Protein structure prediction, Data Analysis, Satellite Image Processing, CRM and ERP ,Social networking . Cloud Application- Scientific Application, Business Application. *Advance Topic in Cloud Computing:* Federated Cloud/InterCloud, Third Party Cloud Services

Recommended Text:

1. " Distributed and Cloud Computing " By Kai Hawang , Geofrey C.Fox, Jack J. Dongarra Pub: Elservier
2. Cloud Computing ,Principal and Paradigms, Edited By Rajkumar Buyya, Jemes Broberg, A. Goscinski, Pub.- Wiley
3. Kumar Saurabh, "Cloud Computing" , Wiley Pub
4. Krutz , Vines, "Cloud Security " , Wiley Pub
5. Velte, "Cloud Computing- A Practical Approach" ,TMH Pub

7CS2A Information System Security (Common to CS & IT)

Class: VII Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Introduction to security attacks, services and mechanism, classical encryption techniques- substitution ciphers and transposition ciphers, cryptanalysis, stream and block ciphers. Modern Block Ciphers: Block ciphers principals, Shannon's theory of confusion and diffusion, fiestal structure, data encryption standard(DES), differential and linear cryptanalysis of DES, block cipher modes of operations, triple DES.
II	AES, RC6, random number generation. S-box theory: Boolean Function, S-box design criteria, Bent functions, Propagation and nonlinearity, construction of balanced functions, S-box design.
III	Public Key Cryptosystems: Principles of Public Key Cryptosystems, RSA Algorithm, security analysis of RSA, Exponentiation in Modular Arithmetic. Key Management in Public Key Cryptosystems: Distribution of Public Keys, Distribution of Secret keys using Public Key Cryptosystems. X.509 Discrete Logarithms, Diffie-Hellman Key Exchange.
IV	Message Authentication and Hash Function: Authentication requirements, authentication functions, message authentication code, hash functions, birthday attacks, security of hash functions and MAC, MD5 message digest algorithm, Secure hash algorithm(SHA). Digital Signatures: Digital Signatures, authentication protocols, digital signature standards (DSS), proof of digital signature algorithm. Remote user Authentication using symmetric and Asymmetric Authentication
V	Pretty Good Privacy. IP Security: Overview, IP Security Architecture, Authentication Header, Encapsulation Security Payload in Transport and Tunnel mode with multiple security associations (Key Management not Included). Strong Password Protocols: Lamport's Hash, Encrypted Key Exchange.

Text/References:

1. Stalling Williams: Cryptography and Network Security: Principles and Practices, 4th Edition, Pearson Education, 2006.
2. Kaufman Charlie et.al; Network Security: Private Communication in a Public World, 2nd Ed., PHI/Pearson.
3. Pieprzyk Josef and et.al; Fundamentals of Computer Security, Springer-Verlag, 2008.
4. Trappe & Washington, Introduction to Cryptography, 2nd Ed. Pearson.

7CS3A Data Mining & Ware Housing (Common to CS & IT)

Class: VII Sem. B.Tech.	Evaluation
Branch: Computer Engg. Schedule per Week Lectures: 3	Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]

Units	Contents of the subject
I	Overview, Motivation(for Data Mining),Data Mining-Definition & Functionalities, Data Processing, Form of Data Preprocessing, Data Cleaning: Missing Values, Noisy Data, (Binning, Clustering, Regression, Computer and Human inspection), Inconsistent Data, Data Integration and Transformation. Data Reduction:-Data Cube Aggregation, Dimensionality reduction, Data Compression, Numerosity Reduction, Clustering, Discretization and Concept hierarchy generation.
II	Concept Description: Definition, Data Generalization, Analytical Characterization, Analysis of attribute relevance, Mining Class comparisons, Statistical measures in large Databases. Measuring Central Tendency, Measuring Dispersion of Data, Graph Displays of Basic Statistical class Description, Mining Association Rules in Large Databases, Association rule mining, mining Single-Dimensional Boolean Association rules from Transactional Databases– Apriori Algorithm, Mining Multilevel Association rules from Transaction Databases and Mining Multi- Dimensional Association rules from Relational Databases.
III	What is Classification & Prediction, Issues regarding Classification and prediction, Decision tree, Bayesian Classification, Classification by Back propagation, Multilayer feed-forward Neural Network, Back propagation Algorithm, Classification methods K-nearest neighbour classifiers, Genetic Algorithm. Cluster Analysis: Data types in cluster analysis, Categories of clustering methods, Partitioning methods. Hierarchical Clustering- CURE and Chameleon. Density Based Methods-DBSCAN, OPTICS. Grid Based Methods- STING, CLIQUE. Model Based Method –Statistical Approach, Neural Network approach, Outlier Analysis
IV	Data Warehousing: Overview, Definition, Delivery Process, Difference between Database System and Data Warehouse, Multi Dimensional Data Model, Data Cubes, Stars, Snow Flakes, Fact Constellations, Concept hierarchy, Process Architecture, 3 Tier Architecture, Data Mining.
V	Aggregation, Historical information, Query Facility, OLAP function and Tools. OLAP Servers, ROLAP, MOLAP, HOLAP, Data Mining interface, Security, Backup and Recovery, Tuning Data Warehouse, Testing Data Warehouse.

Text Books & References:

1. Data Warehousing in the Real World – Anahory and Murray, Pearson Education.
2. Data Mining – Concepts and Techniques – Jiawai Han and Micheline Kamber.
3. Building the Data Warehouse – WH Inmon, Wiley.

7CS4A COMPUTER AIDED DESIGN FOR VLSI

Class: VII Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Complexity in microelectronic circuit design and Moore's Law, design styles -Full-custom design, standard-cell design, Programmable Logic Devices, Field Programmable Gate Arrays, Design Stages, Computer-Aided Synthesis and Optimizations, design flow and related problems.
II	Boolean functions and its representations – co-factor, unite, derivatives, consensus and smoothing; tabular representations and Binary Decision Diagram (BDD), OBDD, ROBDD and Bryant's reduction algorithm and ITE algorithm. Hardware abstract models – structures and logic networks, State diagram, data-flow and sequencing graphs, hierarchical sequencing graphs. Compilation and behavioral optimizations.
III	Architectural Synthesis – Circuit description and problem definition, temporal and spatial domain scheduling, synchronization problem. Scheduling algorithms - ASAP and ALAP scheduling algorithms, scheduling under constraints, relative scheduling, list scheduling heuristic. Scheduling in pipelined circuits.
IV	Resource Sharing & Binding in sequencing graphs for resource dominated circuits, sharing of registers and busses; binding variables to registers. Two-level logic optimization principles – definitions and exact logic minimizations. Positional cube notations, functions with multi-valued logic. List-oriented manipulations.
V	Physical Design. Floor planning – goals and objectives. Channel definition, I/O and power planning. Clock Planning. Placement – goals and objectives. Placement algorithms. Iterative improvement algorithms. Simulated Annealing. Timing-driven Placement. Global routing – goals and objectives. Global routing methods. Timing-driven global routing. Detailed Routing – goals and objectives. Left-edge algorithm. Constraints and routing graphs. Channel routing algorithms. Via minimization. Clock routing, power routing, circuit extraction and Design Rule Checking.

Text Books:

1. S.H. Gerez. Algorithms VLSI Design Automation. Wiley India. (Indian edition available.)
2. Michael John Sebastian Smith. Application-Specific Integrated Circuits. Addison-Wesley. (Low-priced edition is available.)
3. G.D. Micheli, Synthesis and optimization of digital circuits, TMH.

References:

1. http://www.fie-conference.org/fie98/papers/1002.pdf
2. S. Sait and H. Youssef. VLSI Physical Design Automation: Theory and Practice.

7CS5A COMPILER CONSTRUCTION

Class: VII Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Compiler, Translator, Interpreter definition, Phase of compiler introduction to one pass & Multipass compilers, Bootstrapping, Review of Finite automata lexical analyzer, Input, buffering, Recognition of tokens, Idea about LEX: A lexical analyzer generator, Error handling.
II	Review of CFG Ambiguity of grammars, Introduction to parsing. Bottom up parsing Top down parsing techniques, Shift reduce parsing, Operator precedence parsing, Recursive descent parsing predictive parsers. LL grammars & passers error handling of LL parser. LR parsers, Construction of SLR, Conical LR & LALR parsing tables, parsing with ambiguous grammar. Introduction of automatic parser generator: YACC error handling in LR parsers.
III	Syntax directed definitions; Construction of syntax trees, L-attributed definitions, Top down translation. Specification of a type checker, Intermediate code forms using postfix notation and three address code, Representing TAC using triples and quadruples, Translation of assignment statement. Boolean e xpression and control structures.
IV	Storage organization, Storage allocation, Strategies, Activation records, Accessing local and non local names in a block structured language, Parameters passing, Symbol table organization, Data structures used in symbol tables.
V	Definition of basic block control flow graphs, DAG representation of basic block, Advantages of DAG, Sources of optimization, Loop optimization, Idea about global data flow analysis, Loop invariant computation, Peephole optimization, Issues in design of code generator, A simple code generator, Code generation from DAG.

Text/References:

1. Aho, Ullman and Sethi: Compilers, Addison Wesley.

2. Holub, Compiler Design in C, PHI.

7CS6.1A ADVANCE DATABASE MANGEMENT SYSTEMS

Class: VII Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Query Processing and Optimization: Overview of Relational Query Optimization, System Catalog in a Relational DBMS, Alternative Plans, Translating SQL, Queries into Algebra, Estimating the Cost of a Plan, Relational Algebra Equivalences, Enumeration of Alternative Plans. [2]
II	Object Database Systems: Motivating Examples, Structured Data Types, Operations On Structured Data, Encapsulation and ADT's, Inheritance, Objects, OIDs and Reference Types, Database Design for an ORDBMS, ORDBMS Implementation Challenges, ORDBMS, Comparing RDBMS, OODBMS, and ORDBMS.
III	Parallel and Distributed Databases: Architectures for Parallel, Databases, Parallel Query Evaluation, Parallelizing Individual Operations, Parallel Query Optimization, Distributed DBMS Architectures, Storing Data in a Distributed DBMS, Distributed Catalog Management, Distributed Query Processing, Updating Distributed Data, Introduction to Distributed Transactions, Distributed Concurrency Control, Distributed Recovery. [2]
IV	Database Security and Authorization: Introduction to Database Security, Access Control, Discretionary Access Control- Grant and Revoke on Views and Integrity Constraints, Mandatory Access Control- Multilevel Relations and Polyinstantiation, Covert Channels, DoD Security Levels, Additional Issues Related to Security- Role of the Database Administrator, Security in Statistical Databases, Encryption. [2]
V	POSTGES: POSTGRES user interfaces, sql variations and extensions, Transaction Management, Storage and Indexing, Query processing and optimizations, System Architectures. XML: Motivation, Structure of XML data, XML Document Schema, Querying and Transformation, Application Program Interface to XML, Storage of XML Data, XML applications. [2]

Text/References

1. Elmasri R and Navathe SB, Fundamentals of Database Systems, 3rd Edition, Addison Wesley,2000.
2. Connolly T, Begg C and Strachan A, Database Systems, 2nd Edition, Addison Wesley, 1999
3. Ceri Pelagatti , Distributed Database: Principles and System - (McGraw Hill)
4. Simon AR, Strategic Database Technology: Management for the Year 2000, Morgan Kaufmann, 1995
5. A. Silversatz, H. Korth and S. Sudarsan: Database Cocepts 5th edition, Mc-Graw Hills 2005.

7CS6.2A Robotics

Class: Vll Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Unit	Contents
I	Introduction -- brief history, types, classification and usage, Science and Technology of robots, Some useful websites, textbooks and research journals.
II	Elements of robots -- joints, links, actuators, and sensors Position and orientation of a rigid body, Homogeneous transformations, Representation of joints, link representation using D-H parameters, Examples of D-H parameters and link transforms, different kinds of actuators – stepper, DC servo and brushless motors, model of a DC servo motor, Types of transmissions, Purpose of sensors, internal and external sensors, common sensors – encoders, tachometers, strain gauge based force-torque sensors
III	Introduction, Direct and inverse kinematics problems, Examples of kinematics of common serial manipulators, workspace of a serial robot, Inverse kinematics of constrained and redundant robots, Tractrix based approach for fixed and free robots and multi-body systems, simulations and experiments, Solution procedures using theory of elimination, Inverse kinematics solution for the general 6R serial manipulator.
IV	Degrees-of-freedom of parallel mechanisms and manipulators, Active and passive joints, Constraint and loop-closure equations, Direct kinematics problem, Mobility of parallel manipulators, Closed-from and numerical solution, Inverse kinematics of parallel manipulators and mechanisms, Direct kinematics of Gough-Stewart platform.
V	Linear and angular velocity of links, Velocity propagation, Manipulator Jacobians for serial and parallel manipulators, Velocity ellipse and ellipsoids, Singularity analysis for serial and parallel manipulators, Loss and gain of degree of freedom, Statics of serial and parallel manipulators, Statics and force transformation matrix of a Gough-Stewart platform, Singularity analysis and statics.

Text Books :

1. Mittal and Nagrath, Robotics and Control, Tata McGraw-Hill Education, 2003.

2. Fred G. Martin, Robotic Explorations: A Hands On Introduction to Engineering, Pearson Education, 2001.

7CS6.3A Data Compression Techniques

Class: VII Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Compression Techniques: Lossless, lossy, measure of performance, modeling & coding. Lossless compression: Derivation of average information, data models, uniquely decodable codes with tests, prefix codes, Kraft-Mc Millan inequality. Huffman coding: Algorithms, minimum variance Huffman codes, optimality, length extended codes, adaptive coding, Rice codes, using Huffman codes for lossless image compression.
II	Arithmetic coding with application to lossless compression. Dictionary Techniques: LZ77, LZ78, LZW Predictive coding: Burrows-Wheeler Transform and move-to-front coding, JPEG-LS Facsimile Encoding: Run length, T.4 and T.6
III	Lossy coding- Mathematical preliminaries: Distortion criteria, conditional entropy, average mutual information, differential entropy, rate distortion theory, probability and linear system models. Scalar quantization: The quantization problem, uniform quantizer, Forward adaptive quantization, non-uniform quantization-Formal adopting quantization, companded Quantization Vector quantization: Introduction, advantages, The Linde-Ruzo-Grey algorithm, lattice vector quantization.
IV	Differential encoding – Introduction, Basic algorithm, Adaptive DPCM, Delta modulation, speech and image coding using delta modulation. Sampling in frequency and time domain, z-transform, DCT, DST, DWHT, quantization and coding of transform coefficient.
V	Sub band coding: Introduction, Filters, Basic algorithm, Design of Filter banks, G.722, MPEG. Wavelet based compression: Introduction, wavelets multi-resolution analysis and the scaling function implementation using filters.

Text Books & References:

1. Sayood K: Introduction to Data Compression: ELSEVIER 2005.

7CS7A Web Development Lab

Class: VII Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Practical Hrs: 2**	**Examination Time = Three (3) Hours** **Maximum Marks = 50** **[Mid-term (60) & End-term (40)]**

S. No.	List of Experiment
1	. Creation of HTML Files
2	Working with Client Side Scripting : VBScript, JavaScript
3	Configuration of web servers: Apache Web Server, Internet Information Server (IIS)
4	Working with ActiveX Controls in web documents
5	Experiments in Java Server Pages: Implementing MVC Architecture using Servlets, Data Access Programming (using ADO), Session and Application objects, File System Management
6	Working with other Server Side Scripting: Active Server Pages, Java Servlets, PHP
7	Experiments in Ajax Programming
8	Developing Web Services
9	Developing any E-commerce application (Mini Project)
10	Application Development in cloud computing Environment
11	Experiment Using Open Source Tool e.g. ANEKA

7CS8A VLSI PHYSICAL DESIGN LAB

Class: VII Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. **Schedule per Week** **Practical Hrs : 3**	**Examination Time = Four (4) Hours** **Maximum Marks = 75** **[Sessional/Mid-term (45) & End-term (30)]**

VLSI Physical Design Automation is essentially the research, development and productization of algorithms and data structures related to the physical design process. The objective is to investigate optimal arrangements of devices on a plane (or in three dimensions) and efficient interconnection schemes between these devices to obtain the desired functionality and performance. Since space on a wafer is very expensive real estate, algorithms must use the space very efficiently to lower costs and improve yield. In addition, the arrangement of devices plays a key role in determining the performance of a chip. Algorithms for physical design must also ensure that the layout generated abides by all the rules required by the fabrication process. Fabrication rules establish the tolerance limits of the fabrication process. Finally, algorithms must be efficient and should be able to handle very large designs. Efficient algorithms not only lead to fast turn-around time, but also permit designers to make iterative improvements to the layouts. The VLSI physical design process manipulates very simple geometric objects, such as polygons and lines. As a result, physical design algorithms tend to be very intuitive in nature, and have significant overlap with graph algorithms and combinatorial optimization algorithms. In view of this observation, many consider physical design automation the study of graph theoretic and combinatorial algorithms for manipulation of geometric objects in two and three dimensions. However, a pure geometric point of view ignores the electrical (both digital and analog) aspect of the physical design problem. In a VLSI circuit, polygons and lines have inter-related electrical properties, which exhibit a very complex behavior and depend on a host of variables. Therefore, it is necessary to keep the electrical aspects of the geometric objects in perspective while developing algorithms for VLSI physical design automation. With the introduction of Very Deep Sub-Micron (VDSM), which provides very small features and allows dramatic increases in the clock frequency, the effect of electrical parameters on physical design will play a more dominant role in the design and development of new algorithms.

(Source: Algorithms For VLSI Physical Design Automation, by Naveed A. Sherwani).

The exercise should be such that the above objectives are met.

Automation tools such as Synopsis/ Cadence are available in the area. However, to begin, the students shall be assigned exercises on route optimization, placement & floor planning. Small circuits may be taken & algorithms implemented. At a later stage, the students may use tools and design more complex circuits.

7CS9A COMPILER DESIGN LAB

Class: VII Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. **Schedule per Week** **Practical Hrs : 3**	**Examination Time = Four (4) Hours** **Maximum Marks = 75** **[Sessional/Mid-term (60) & End-term (40)]**

Objectives: At the end of the semester, the students should have clearly understood and implemented the following:

1. Develop an in depth understanding of system programming concept. Lexical analysis, syntax analysis, semantics analysis, code optimization, code generation. Language specification and processing

2. Develop an Understanding of Scanning by using concept of Finite state automaton. Parse tree and syntax tree, Top down parsing (recursive decent parsing, LL (1) parser) Bottom up parsing (operator precedence parsing) .Managing symbol table, opcode table, literal table, pool table

3. Develop an Understanding of Intermediate code form: Three address code, Polish notation (Postfix strings)

4. Develop an Understanding of Allocation data structure. Heaps

5. Develop an Understanding about Language processor development tools: LEX, YACC. Language processing activities (Program generation and execution)

It is expected that each laboratory assignments to given to the students with an aim to In order to achieve the above objectives

Indicative List of exercises:

1. Write grammar for a fictitious language and create a lexical analyzer for the same.
2. Develop a lexical analyzer to recognize a few patterns in PASCAL and C (ex: identifiers, constants, comments, operators etc.)
3. Write a program to parse using Brute force technique of Top down parsing
4. Develop on LL (1) parser (Construct parse table also).
5. Develop an operator precedence parser (Construct parse table also)
6. Develop a recursive descent parser
7. Write a program for generating for various intermediate code forms

   i) Three address code ii) Polish notation
8. Write a program to simulate Heap storage allocation strategy
9. Generate Lexical analyzer using LEX
10. Generate YACC specification for a few syntactic categories
11. Given any intermediate code form implement code optimization techniques

Reference

V.V Das, Compiler Design using FLEX and YACC, PHI

8CS1A MOBILE COMPUTING (Common to CS & IT)

Class: VIII Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Mobile computing: Definitions, adaptability issues (transparency, Environmental Constraints, application aware adaptation), mechanisms for adaptation and incorporating adaptations. Mobility management: mobility management, location management principle and techniques, PCS location management Scheme.
II	Data dissemination and management: challenges, Data dissemination, bandwidth allocation for publishing, broadcast disk scheduling, mobile cache maintenance schemes, Mobile Web Caching. Introduction to mobile middleware.
III	Middleware for application development: adaptation, Mobile agents. Service Discovery Middleware: Service Discovery & standardization Methods (universally Unique Identifiers, Textual Description & using interfaces), unicast Discovery, Multicast Discovery & advertisement, service catalogs, Garbage Collection, Eventing.
IV	Mobile IP, Mobile TCP, Database systems in mobile environments, World Wide Web and mobility
V	Ad Hoc networks, localization, MAC issues, Routing protocols, global state routing (GSR), Destination sequenced distance vector routing (DSDV), Dynamic source routing (DSR), Ad Hoc on demand distance vector routing (AODV), Temporary ordered routing algorithm (TORA), QoS in Ad Hoc Networks, applications.

Text/References:

1. Frank Adelstein, Sandeep Gupta, Golden Richard III, Loren Schwiebert, Fundamentals of Mobile and Pervasive Computing, TMH.
2. Principles of mobile computing Hansmann & Merk., Springer
3. Mobile communications Jochen Schiller , Pearson
4. 802.11 wireless networks Matthew S.Gast, O'REILLY.
5. Wireless LANs: Davis & McGuffin, McGraw Hill
6. Mobile Communications Handbook by Jerry D. Gybson
7. Mobile Communications Handbook by R

8CS2A Digital Image Processing (Common to CS & IT)

Class: VIII Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	**Introduction to Image Processing:** Digital Image representation, Sampling & Quantization, Steps in image Processing, Image acquisition, color image representation
II	**Image Transformation & Filtering:** Intensity transform functions, histogram processing, Spatial filtering, Fourier transforms and its properties, frequency domain filters, colour models, Pseudo colouring, colour transforms, Basics of Wavelet Transforms
III	**Image Restoration:** Image degradation and restoration process, Noise Models, Noise Filters, degradation function, Inverse Filtering, Homomorphism Filtering
IV	**Image Compression:** Coding redundancy, Interpixel redundancy, Psychovisual redundancy, Huffman Coding, Arithmetic coding, Lossy compression techniques, JPEG Compression
V	**Image Segmentation & Representation:** Point, Line and Edge Detection, Thresholding, Edge and Boundary linking, Hough transforms, Region Based Segmentation, Boundary representation, Boundary Descriptors, Regional

References:

1. Gonzalez and Woods: Digital Image Processing ISDN 0-201-600- 781, Addison Wesley 1992.

Boyle and Thomas: Computer Vision - A First Gurse 2nd Edition, ISBN 0-632-028-67X, Blackwell

Science 1995.

2. Gonzalez and Woods: Digital Image Processing ISDN 0-201-600- 781, Addison Wesley 1992.

3. Pakhera Malay K: Digital Image Processing and Pattern Recogination, PHI.

4. Trucco&Verri: Introductory Techniques for 3-D Computer Vision, Prentice Hall, Latest Edition

5. Low: Introductory Computer Vision and Image Processing, McGraw-Hill 1991, ISBN 0-07-707403-3.

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8CS3A DISTRIBUTED SYSTEMS

Class: VIII Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Distributed Systems: Features of distributed systems, nodes of a distributed system, Distributed computation paradigms, Model of distributed systems, Types of Operating systems: Centralized Operating System, Network Operating Systems, Distributed Operating Systems and Cooperative Autonomous Systems, design issues in distributed operating systems. Systems Concepts and Architectures: Goals, Transparency, Services, Architecture Models, Distributed Computing Environment (DCE).[1.2] Theoretical issues in distributed systems: Notions of time and state, states and events in a distributed system, time, clocks and event precedence, recording the state of distributed systems.[2]
II	Concurrent Processes and Programming: Processes and Threads, Graph Models for Process Representation, Client/Server Model, Time Services, Language Mechanisms for Synchronization, Object Model Resource Servers, Characteristics of Concurrent Programming Languages (Language not included).[1] Inter-process Communication and Coordination: Message Passing, Request/Reply and Transaction Communication, Name and Directory services, RPC and RMI case studies.[1]
III	Distributed Process Scheduling: A System Performance Model, Static Process Scheduling with Communication, Dynamic Load Sharing and Balancing, Distributed Process Implementation.[1] Distributed File Systems: Transparencies and Characteristics of DFS, DFS Design and implementation, Transaction Service and Concurrency Control, Data and File Replication.[1,2] Case studies: Sun network file systems, General Parallel file System and Window's file systems. Andrew and Coda File Systems [2,3]
IV	Distributed Shared Memory: Non-Uniform Memory Access Architectures, Memory Consistency Models, Multiprocessor Cache Systems, Distributed Shared Memory, Implementation of DSM systems.[1] Models of Distributed Computation: Preliminaries, Causality, Distributed Snapshots, Modeling a Distributed Computation, Failures in a Distributed System, Distributed Mutual Exclusion, Election, Distributed Deadlock handling, Distributed termination detection. [1]
V	Distributed Agreement: Concept of Faults, failure and recovery, Byzantine Faults, Adversaries, Byzantine Agreement, Impossibility of Consensus and Randomized Distributed Agreement.[1] Replicated Data Management: concepts and issues, Database Techniques, Atomic Multicast, and Update Propagation.[1] CORBA case study: Introduction, Architecture, CORBA RMI, CORBA Services.[3]

Text Books:

1. Distributed operating systems and algorithm analysis by Randy Chow and T. Johnson, Pearson
2. Operating Systems A concept based approach by DM Dhamdhere, TMH
3. Distributed Systems- concepts and Design, Coulouris G., Dollimore J, and Kindberg T., Pearson

8CS4.1A Hardware Testing and Fault Tolerance

Class: VIII Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Overview of hardware testing. Reliability and Testing, Difference between Verification and Testing, Concepts of fault models, test pattern generation and fault coverage. Types of tests – exhaustive testing, pseudo-exhaustive testing, pseudo-random testing, and deterministic testing. Test Application. Design for Test. Testing Economics. Defects, Failures and Faults. How are physical defects modeled as faults. Stuck-at faults, Single stuck-at-faults multiple stuck-at faults, bridging faults, delay faults, transient faults.
II	Relation between VLSI Design and Testing. a) Design Representation for the purpose of testing – Representation in the form of mathematical equations, tabular format, graphs, Binary Decision Diagrams, Netlists, or HDL descriptions. b) Recap of VLSI Design Flow and where testing fits in the flow. Importance of Simulation and Fault Simulation. Compiled and event-driven simulation. Parallel and deductive fault simulation. Using fault simulation to estimate fault coverage and building a fault dictionary
III	Combinational Test Pattern Generation. D-algorithm. Critical Path Tracking. PODEM algorithm for test generation. Testing sequential circuits. Functional and deterministic ATPG for sequential circuits and the associated challenges. Motivation for Design for Testability. Test Points, Partitioning for Testability. Scan Testing. Scan Architectures. Cost of Scan Testing. Boundary Scan Testing. Board-level testing. Boundary-scan Architecture and various modes of operation.
IV	a) Built-in Self Test. Pseudo-random test generation. Response Compaction. Random pattern-resistant faults. BIST architectures – Circular BIST, BILBO, STUMPS. b) Testing of Memories – Fault models, Functional tests for memories, Memory BIST. c) Testing of microprocessors.
V	Hardware fault tolerance. Failure Rate, Reliability, Mean Time to Failure. Different kinds of redundancy schemes for fault-tolerance (Space, Time, and Information Redundancy). N-modular Redundancy. Watch Dog Processors, Byzantine Failures. Information Redundancy – parity codes, checksums, m-of-n codes. RAID architectures for disk storage systems. Fault tolerance in interconnection networks. Fault-tolerant routing techniques.

Text Book:

1. Samiha Mourad and Yervant Zorian. Principles of Electronic Systems. Wiley Student Editon. [Available in Indian Edition].
2. Koren and C. Mani Krishna. Fault-Tolerant Systems. Elsevier. (Indian Edition Available.)

Text/References:

1. Abramovici, M., Breuer, M. A. and Friedman, A. D. Digital systems testing and testable design. IEEE press (Indian edition available through Jayco Publishing house), 2001.2. Essentials of Electronic Testing for Digital, Memory and Mixed-Signal VLSI Circuits by Bushnell and Agrawal, Springer, 2000.

8CS4.2A REAL TIME SYSTEMS

Class: VIII Sem. B.Tech.	Evaluation
Branch: Computer Engg. Schedule per Week Lectures: 3	Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]

Units	Contents of the subject
I	Introduction: Definition, Typical Real Time Applications, concept of tasks, types of tasks and real time systems, block diagram of RTS, and tasks parameters -Release Times, execution time, period, Deadlines, and Timing Constraints etc. RTS requirements.
II	Reference Models for Real Time Systems: processors and Resources, Temporal Parameters of Real-Time Workload, Periodic and Aperiodic Task Model, Precedence Constrains and Data Dependency, Other Types of Dependencies, Functional Parameters, Resource Parameters. Real Time Scheduling: classification of Real Time Scheduling, scheduling criteria, performance metrics, schedulability analysis, Introduction to Clock Driven scheduling, Weighted Round Robin Approach and Priority Driven Approach. Dynamic Versus Static systems, Offline Versus Online Scheduling.
III	Periodic tasks scheduling: Clock Driven Scheduling – definition, notations and assumption, scheduler concepts, general scheduling structure, cyclic executives. Priority Driven Scheduling; notations and assumption, fixed priority verses dynamic priority, fixed priority scheduling algorithms (RM and DM) and their schedulability analysis, concept of schedulability tests – Inexact and exact schedulability tests for RM and DM, Optimality of the RM and DM algorithms, practical factors.
IV	Aperiodic task scheduling; assumption and approaches, server based and non-server based fixed priority scheduling algorithms – polling server, deferrable server , simple sporadic server, priority exchange, extended priority exchange, slack stealing. Introduction to scheduling of flexible computations –flexible applications, imprecise computation model and firm deadline model.
V	Resources Access Control: Assumptions on Resources and their usage, Effect of Resource Contention and Resource Access Control (RAC), Non-preemptive Critical Sections, priority inversion problem, need of new resource synchronization primitives/protocols for RTS, Basic Priority-Inheritance and Priority-Ceiling Protocols, Stack Based Priority-Ceiling Protocol, Use of Priority- Ceiling Protocol in Dynamic Priority Systems, Preemption Ceiling Protocol, Access Control in Multiple-Unit Resources, Controlling Concurrent Accesses to Data Objects.

Text & References:

1. J.W.S.Liu: Real-Time Systems, Pearson Education Asia
2. P.D.Laurence, K.Mauch: Real-time Microcomputer System Design, An Introduction, McGraw Hill
3. C.M. Krisna & K. G. Shim- Real time systems- TMH

8CS4.3A AInformation Retrieval (Common to CS & IT)

Class: VIII Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Knowledge Representation: Knowledge representation, Basics of Prepositional logic, Predicate logic, reasoning using first order logic, unification, forward chaining, backward chaining, resolution Production rules, frames, semantic networks scripts.
II	Ontology Development: Description logic-taxonomies, Topic maps Ontology, Definition expressing ontology, logically ontology representations, – XML, RDF, RDFS, OWL, OIL, ontology development for specific domain, ontology engineering, Semantic web services.
III	Information Retrieval Modeling: Information retrieval, taxonomy, formal characterization, classic information retrieval, set theoretic model, algebraic model, probabilistic model, structured text, retrieval models, models for browsing, retrieval performance evaluation, keyword based querying, pattern matching, structural queries, query operations.
IV	Text and Multimedia Languages and Properties: Introduction, metadata, markup languages, multimedia. Text operations: document preprocessing, document clustering text Compressionbasic concepts - statistical methods. Indexing and searching: inverted files, suffix trees, signature file, Boolean queries, sequential searching, pattern matching.
V	Recent Trends in IR: Parallel and distributed IR, multimedia IR, data modeling, query languages, A generic Multimedia indexing Approach, one dimensional time series, two dimensional color images, Automatic feature extraction. Web Searching, Characterizing the Web, Search Engines, Browsing, Meta searchers, Searching using hyperlinks

TEXT BOOKS :

1. Stuart Russell and Peter Norvig, “Artificial Intelligence – A Modern Approach”, Pearson Education, Second edition, 2003. (UNIT I)

2. Michael C. Daconta, Leo J. Obart and Kevin J. Smith,”Semantic Web – A Guide to the Future of XML, Web Services and Knowledge Management”, Wiley Publishers, 2003 (UNIT II)

3. Ricardo Baeza-Yates, BerthierRibeiro-Neto, “Modern Information Retrieval”, Addison Wesley, 1999. (UNITs III, IV & V)

REFERENCES

1. Elain Rich and Kevin Knight, "Artificial Intelligence", Tata McGraw-Hill, Third edition, 2003

2. Christopher D. Manning, PrabhakarRaghavan and HinrichSchutze, “Introduction to Information Retrieval”, Cambridge University Press, 2008.

8CS5A UNIX NETWORK PROGRAMMING & SIMULATION LAB

Class: VIII Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. **Schedule per Week** **Practical Hrs.: 3**	**Examination Time = Four (4) Hours** **Maximum Marks = 100** **[Sessional/Mid-term (60) & End-term (40)]**

Objectives:

At the end of course, the students should be able to

- Understand various distributions of Unix viz. BSD, POSIX etc.
- Write client/server applications involving unix sockets involving TCP or UDP involving iterative or concurrent server.
- Understand IPV4 & IPV6 interoperability issues
- Use fork( ) system call.
- Understand the network simulator NS2 and Simulate routing algorithm on NS2 (Available on http://www.isi.edu/nsnam/ns/).

Suggested Platform: For Socket Programming- Linux, For NS2 Any of Microsoft Windows or Linux (In case of Microsoft, Virtual environment cygwin will also be required).

Suggested Exercises

S.No.	**List of Experiments**
1.	Write two programs in C: hello_client and hello_server • The server listens for, and accepts, a single TCP connection; it reads all the data it can from that connection, and prints it to the screen; then it closes the connection • The client connects to the server, sends the string "Hello, world!", then closes the connection
2.	Write an Echo_Client and Echo_server using TCP to estimate the round trip time from client to the server. The server should be such that it can accept multiple connections at any given time.
3.	Repeat Exercises 1 & 2 for UDP.
4.	Repeat Exercise 2 with multiplexed I/O operations
5.	Simulate Bellman-Ford Routing algorithm in NS2

References:

- **Stevens, Unix Network Programming, Vol-I**

8CS6A FPGA LAB.

Class: VIII Sem. B.Tech.	**Evaluation**
Branch: Computer Engg. **Schedule per Week** **Practical Hrs : 3**	**Examination Time = Four (4) Hours** **Maximum Marks = 100** **[Sessional/Mid-term (60) & End-term (40)]**

S. No.	**List of Experiments**
1.	Fundamental Theory Introduction to DSP architectures and programming Sampling Theory, Analog-to-Digital Converter (ADC), Digital-to-Analog Converter (DAC), and Quantization; Decimation, Interpolation, Convolution, Simple Moving Average; Periodic Signals and harmonics; Fourier Transform (DFT/FFT), Spectral Analysis, and time/spectrum representations; FIR and IIR Filters;
2.	Design (Simulation) using MATLAB/ Simulink Simulate the lab exercises using MATLAB/Simulink
3.	Implementation using pure DSP, pure FPGA and Hybrid DSP/FPGA platforms Digital Communications: On-Off- Keying (OOK), BPSK modulation, and a simple transceiver design Adaptive Filtering: Echo/Noise Cancellation, Least Mean Square (LMS) algorithm (2 weeks) Wireless Communications: Channel coding/decoding, Equalization, Simple Detection Algorithm, OFDM Speech Processing: Prediction Algorithms, Speech Classification and Synthesis

8CS7A Digital Image Processing lab(Common to Comp. Engg. & Info. Tech)

Class: VIII Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Practical Hrs: 2**	**Examination Time = Three (3) Hours** **Maximum Marks = 50** **[Mid-term (60) & End-term (40)]**

S. No.	List of Experiment
1	Color image segmentation algorithm development
2	Wavelet/vector quantization compression
3	Deformable templates applied to skin tumor border finding
4	Helicopter image enhancement
5	High-speed film image enhancement
6	Computer vision for skin tumor image evaluation
7	New Border Images

Scheme and Syllabus

Rajasthan Technical University, Kota

B. Tech. in Electrical Engineering

(Session 2018-19)

Scheme of

UNDERGRADUATE DEGREE COURSE

Electrical Engineering

Rajasthan Technical University, Kota

Effective from session: 2018 – 2019

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Teaching & Examination Scheme
B.Tech. : Electrical Engineering
2nd Year - III Semester

SN	Category	Course		Contact hrs/week			Marks				Cr
		Code	Title	L	T	P	Exm Hrs	IA	ETE	Total	
THEORY											
1	BSC	3EE2-01	Advance Mathematics	3	0	0	3	30	120	**150**	**3**
2	HSMC	3EE1-02/ 3EE1-03	Technical Communication / Managerial Economics and Financial Accounting	2	0	0	2	20	80	**100**	**2**
3	ESC	3EE3-04	Power generation Process	2	0	0	2	20	80	**100**	**2**
4	PCC	3EE4-05	Electrical Circuit Analysis	3	0	0	3	30	120	**150**	**3**
5		3EE4-06	Analog Electronics	3	0	0	3	30	120	**150**	**3**
6		3EE4-07	Electrical Machine - I	3	0	0	3	30	120	**150**	**3**
7		3EE4-08	Electromagnetic Field	2	0	0	2	20	80	**100**	**2**
			Sub Total	18	0	0		180	720	**900**	**18**
PRACTICAL & SESSIONAL											
8	PCC	3EE4-21	Analog Electronics Lab	0	0	2		30	20	**50**	**1**
9		3EE4-22	Electrical Machine-I Lab	0	0	4		60	40	**100**	**2**
10		3EE4-23	Electrical circuit design Lab	0	0	4		60	40	**100**	**2**
13	PSIT	3EE7-30	Industrial Training	0	0	2				**50**	**1**
14	SODECA	3EE8-00	Social Outreach, Discipline & Extra Curricular Activities							**25**	**0.5**
			Sub- Total	0	0	12		150	100	**325**	**6.5**
			TOTAL OF III SEMESTER	18	0	12		330	820	**1225**	**24.5**

L: *Lecture,* ***T:*** *Tutorial,* ***P:*** *Practical,* ***Cr:*** *Credits*

ETE: *End Term Exam,* ***IA:*** *Internal Assessment*

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Teaching & Examination Scheme
B.Tech. : Electrical Engineering
2nd Year - IV Semester

SN	Category	Course		Contact hrs/week			Marks				Cr
		Code	Title	L	T	P	Exm Hrs	IA	ETE	Total	
THEORY											
1	BSC	4EE2-01	Biology	2	0	0	2	20	80	**100**	**2**
2	HSMC	4EE1-02/ 4EE1-03	Technical Communication / Managerial Economics and Financial Accounting	2	0	0	2	20	80	**100**	**2**
3	ESC	4EE3-04	Electronic Measurement & Instrumentation	2	0	0	2	20	80	**100**	**2**
4	PCC	4EE4-05	Electrical Machine - II	3	0	0	3	30	120	**150**	**3**
5		4EE4-06	Power Electronics	3	0	0	3	30	120	**150**	**3**
6		4EE4-07	Signals & Systems	3	0	0	3	30	120	**150**	**3**
7		4EE4-08	Digital Electronics	2	0	0	2	20	80	**100**	**2**
			Sub Total	17	0	0		170	680	**850**	**17**
PRACTICAL & SESSIONAL											
8	PCC	4EE4-21	Electrical Machine - II Lab	0	0	4		60	40	**100**	**2**
9		4EE4-22	Power Electronics Lab	0	0	4		60	40	**100**	**2**
10		4EE4-23	Digital Electronics Lab	0	0	2		30	20	**50**	**1**
11		4EE3-24	Measurement Lab	0	0	2		30	20	**50**	**1**
13	SODECA	4EE8-00	Social Outreach, Discipline & Extra Curricular Activities							**25**	**0.5**
			Sub- Total	0	0	12		180	120	**325**	**6.5**
			TOTAL OF IV SEMEESTER	17	0	12		350	800	**1175**	**23.5**

L: *Lecture,* ***T:*** *Tutorial,* ***P:*** *Practical,* ***Cr:*** *Credits*

ETE: *End Term Exam,* ***IA:*** *Internal Assessment*

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

Course	Electrical Engineering	Code	EE

SEMESTER V Subject Code	Title	Hrs. / Week L	T	P	*IA*	*Exam*	Total
5EE1A	Power Electronics	3	1	Theory Subjects	20	80	100
5EE2A	Microprocessors & Computer Architecture	3			20	80	100
5EE3A	Control Systems	3	1		20	80	100
5EE4A	Data Base Management System	3			20	80	100
5EE5A	Transmission & Distribution of Electrical Power	3	1		20	80	100
5EE6.1A	Optimisation Techniques	3	1		20	80	100
5EE6.2A	Principle of Communication Systems						
5EE6.3A	Introduction to VLSI						
5EE7A	Power Electronics Lab	Practical laboratory courses		2	45	30	75
5EE8A	Microprocessor Lab			2	45	30	75
5EE9A	System Programming Lab			2	45	30	75
5EE10A	DBMS Lab			2	45	30	75
5EE11A	Professional Ethics and Disaster Manage			2	30	20	50
5EEDC	Discipline & Extra Curricular Activity						50
	Total	18	3	10			1000

SEMESTER VI Subject Code	Title	Hrs. / Week L	T	P	*IA*	*Exam*	Total
6EE1A	Modern Control Theory	3	1	Theory Subjects	20	80	100
6EE2A	High Voltage Engineering	3			20	80	100
6EE3A	Switchgear & Protection	3	1		20	80	100
6EE4A	Advanced Power Electronics	3	1		20	80	100
6EE5A	Smart Grid Technology	3			20	80	100
6EE6.1A	Advanced Microprocessors	3	1		20	80	100
6EE6.2A	Power System Instrumentation						
6EE6.3A	Digital Communication and Information Theory						
6EE7A	Control System Lab	Practical laboratory courses		2	45	30	75
6EE8A	Power System Lab			2	45	30	75
6EE9A	Advanced Power Electronics Lab			2	45	30	75
6EE10A	Smart Grid Lab			2	45	30	75
6EE11A	Entrepreneurship Development			2	30	20	50
6EEDC	Discipline & Extra Curricular Activity						50
	Total	18	3	10			1000

Course	Electrical Engineering	Code	EE

SEMESTER VII Subject Code	Title	Hrs. / Week L	T	P	*IA*	*Exam*	Total
7EE1A	Power System Planning	3		Theory Subjects	20	80	100
7EE2A	Power System Analysis	3	1		20	80	100
7EE3A	Artificial Intelligence Techniques	3			20	80	100
7EE4A	Non Conventional Energy Sources	3			20	80	100
7EE5A	Power System Engineering	3	1		20	80	100
7EE6.1A	Electromagnetic Field Theory	3	1		20	80	100
7EE6.2A	Computer Aided Design of Electrical Machines						
7EE6.3A	Economic Operation of Power Systems						
7EE7A	Power System Planning Lab	Practical laboratory courses		2	45	30	75
7EE8A	Power System Modelling & Simulation Lab			2	45	30	75
7EE9A	Industrial Economics & Management			2	30	20	50
7EETR	Practical Training & Industrial visit			2		100	100
7EEPR	Project-1			2	50		50
7EEDC	Discipline & Extra Curricular Activity						50
	Total	18	2	10			1000

SEMESTER VIII Subject Code	Title	Hrs. / Week L	T	P	*IA*	*Exam*	Total
8EE1A	EHV AC/DC Transmission	3	1	Theory Subjects	20	80	100
8EE2A	Electric Drives and Their Control	3	1		20	80	100
8EE3A	Protection of Power System	3			20	80	100
8EE4.1A	Utilization of Electrical Power	3			20	80	100
8EE4.2A	FACTS Devices & Their Applications						
8EE4.3A	Power System Transients						
8EE5A	Computer Based Power System Lab	Practical laboratory courses		3	60	40	100
8EE6A	Electrical Drives and Control Lab			3	60	40	100
8EE7A	High Voltage Engineering Lab			2			50
8EEPR	Project-2			2	120	80	200
8EESM	Seminar			2	60	40	100
8EEDC	Discipline & Extra Curricular Activity						50
	Total	12	2	12			1000

Syllabus of

UNDERGRADUATE DEGREE COURSE

Electrical Engineering

Rajasthan Technical University, Kota

Effective from session: 2018 – 2019

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

2nd Year - III Semester: B.Tech. (Electrical Engineering)

3EE2-01: Advance Mathematics

Credit: 3 **Max. Marks: 150 (IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	**Numerical Methods:** Finite differences, Relation between operators, Interpolation using Newton's forward and backward difference formulae. Gauss's forward and backward interpolation formulae. Stirling's Formulae. Interpolation with unequal intervals: Newton's divided difference and Lagrange'sformulae. Numerical Differentiation, Numerical integration: Trapezoidal rule and Simpson's 1/3rd and 3/8 rules. Solution of polynomial and transcendental equations-Bisection method, Newton-Raphson method and Regula-Falsi method.	**14**
2	**Transform Calculus:** Laplace Transform: Definition and existence of Laplace transform, Properties of Laplace Transform and formulae, Unit Step function, Dirac Delta function, Heaviside function, Laplace transform of periodic functions. Finding inverse Laplace transform by different methods, convolution theorem. Fourier Transform: Fourier Complex, Sine and Cosine transform, properties and formulae, inverse Fourier transforms, Convolution theorem. Z-Transform: Definition, properties and formulae, Convolution theorem, inverse Z-transform, application of Z-transform to difference equation.	**20**
3	**Complex Variable:** Differentiation, Cauchy-Riemann equations, analytic functions, harmonic functions, finding harmonic conjugate; elementary analytic functions (exponential, trigonometric, logarithm) and their properties; Conformal mappings, Mobius transformations and their properties.	**06**
	TOTAL	**40**

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RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

2nd Year - III Semester: B.Tech. (Electrical Engineering)

3EE1-02/4EE1-02: Technical Communication

Credit: 2 **Max. Marks: 100 (IA:20, ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**Introduction to Technical Communication-** Definition of technical communication, Aspects of technical communication, forms of technical communication, importance of technical communication, technical communication skills (Listening, speaking, writing, reading writing), linguistic ability, style in technical communication.	4
2	**Comprehension of Technical Materials/Texts and Information Design & development-** Reading of technical texts, Readingand comprehending instructions and technical manuals, Interpreting and summarizing technical texts, Note-making. Introduction of different kinds of technical documents, Information collection, factors affecting information and document design, Strategies for organization, Information design and writing for print and online media.	6
3	**Technical Writing, Grammar and Editing**- Technical writing process, forms of technical discourse, Writing, drafts and revising, Basics of grammar, common error in writing and speaking, Study of advanced grammar, Editing strategies to achieve appropriate technical style, Introduction to advanced technical communication. Planning, drafting and writing Official Notes, Letters, E-mail, Resume, Job Application, Minutes of Meetings.	8
4	**Advanced Technical Writing**- Technical Reports, types of technical reports, Characteristics and formats and structure of technical reports. Technical Project Proposals, types of technical proposals, Characteristics and formats and structure of technical proposals. Technical Articles, types of technical articles, Writing strategies, structure and formats of technical articles.	8
	TOTAL	26

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RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

2nd Year - III Semester: B.Tech. (Electrical Engineering)

3EE1-03/4EE1-03: Managerial Economics and Financial Accounting

Credit: 2 **Max. Marks: 100 (IA:20, ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1.	**Basic economic concepts** Meaning, nature and scope of economics, deductive vs inductive methods, static and dynamics, Economic problems: scarcity and choice, circular flow of economic activity, national income-concepts and measurement.	4
2.	**Demand and Supply analysis** Demand-types of demand, determinants of demand, demand function, elasticity of demand, demand forecasting –purpose, determinants and methods, Supply-determinants of supply, supply function, elasticity of supply.	5
3.	**Production and Cost analysis** Theory of production- production function, law of variable proportions, laws of returns to scale, production optimization, least cost combination of inputs, isoquants. Cost concepts-explicit and implicit cost, fixed and variable cost, opportunity cost, sunk costs, cost function, cost curves, cost and output decisions, cost estimation.	5
4.	**Market structure and pricing theory** Perfect competition, Monopoly, Monopolistic competition, Oligopoly.	4
5.	**Financial statement analysis** Balance sheet and related concepts, profit and loss statement and related concepts, financial ratio analysis, cash-flow analysis, funds-flow analysis, comparative financial statement, analysis and interpretation of financial statements, capital budgeting techniques.	8
	TOTAL	**26**

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RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

2nd Year - III Semester: B.Tech. (Electrical Engineering)

3EE3-04: Power Generation Processes

Credit: 2 **Max. Marks: 100 (IA:20, ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1.	**Conventional Energy Generation Methods** Thermal Power plants: Basic schemes and working principle. (ii) Gas Power Plants: open cycle and closed cycle gas turbine plants, combined gas & steam plants-basic schemes. Hydro Power Plants: Classification of hydroelectric plants. Basic schemes of hydroelectric and pumped storage plants. (iv) Nuclear Power Plants: Nuclear fission and nuclear fusion. Fissile and fertile materials. Basic plant schemes with boiling water reactor, heavy water reactor and fast breeder reactor. Efficiencies of various power plants.	6
3.	**New Energy Sources** Impact of thermal, gas, hydro and nuclear power stations on environment. Green House Effect (Global Warming).Renewable and nonrenewable energy sources. Conservation of natural resources and sustainable energy systems. Indian energy scene. Introduction to electric energy generation by wind, solar and tidal.	6
4.	**Loads and Load Curves** Types of load, chronological load curve, load duration curve, energy load curve and mass curve. Maximum demand, demand factor, load factor, diversity factor, capacity factor and utilization.	2
5.	**Power Factor Improvement** Causes and effects of low power factor and advantages of power factor improvement. Power factor improvement using shunt capacitors and synchronous condensers.	3
6.	**Power Plant Economics** Capital cost of plants, annual fixed and operating costs of plants, generation cost and depreciation. Effect of load factor on unit energy cost. Role of load diversity in power system economics. Calculation of most economic power factor when (a) kW demand is constant and (b) kVA demand is constant. (iii) Energy cost reduction: off peak energy utilization, co-generation, and energy conservation.	5
7.	**Tariff** Objectives of tariffs. General tariff form. Flat demand rate, straight meter rate, block meter rate. Two part tariff, power factor dependent tariffs, three part tariff. Spot (time differentiated) pricing.	3

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8.	**Selection of Power Plants** Comparative study of thermal, hydro, nuclear and gas power plants. Base load and peak load plants. Size and types of generating units, types of reserve and size of plant. Selection and location of power plants.	**4**
	Total	**28**

Office of Dean Academic Affairs
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3EE4-05 Electrical Circuit Analysis

Credit: 3 **Max. Marks: 150 (IA:30, ETE:120)**
3L+0T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1.	**Network Theorems** Superposition theorem, Thevenin theorem, Norton theorem, Maximum power transfer theorem, Reciprocity theorem, Compensation theorem. Analysis with dependent current and voltage sources. Node and Mesh Analysis. Concept of duality and dual networks.	10
2.	**Solution of First and Second order networks** Solution of first and second order differential equations for Series and parallel R-L, R-C, RL- C circuits, initial and final conditions in network elements, forced and free response, time constants, steady state and transient state response.	8
3.	**Sinusoidal steady state analysis** Representation of sine function as rotating phasor, phasor diagrams, impedances and admittances, AC circuit analysis, effective or RMS values, average power and complex power. Three-phase circuits. Mutual coupled circuits, Dot Convention in coupled circuits, Ideal Transformer.	8
4.	**Electrical Circuit Analysis Using Laplace Transforms** Review of Laplace Transform, Analysis of electrical circuits using Laplace Transform for standard inputs, convolution integral, inverse Laplace transform, transformed network with initial conditions. Transfer function representation. Poles and Zeros. Frequency response (magnitude and phase plots), series and parallel resonances	8
5.	**Two Port Network and Network Functions** Two Port Networks, terminal pairs, relationship of two port variables, impedance parameters, admittance parameters, transmission parameters and hybrid parameters, interconnections of two port networks.	6
	TOTAL	40

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RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

2nd Year - III Semester: B.Tech. (Electrical Engineering)

3EE4-06: Analog Electronics

Credit: 3 **Max. Marks: 150 (IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN		Hours
1.	**Diode circuits** P-N junction diode, I-V characteristics of a diode; review of half-wave and full-wave rectifiers, Zener diodes, clamping and clipping circuits.	4
2.	**BJT circuits** Structure and I-V characteristics of a BJT; BJT as a switch. BJT as an amplifier: small-signal model, biasing circuits, current mirror; common-emitter, common-base and common collector amplifiers; Small signal equivalent circuits, high-frequency equivalent circuits.	8
3.	**MOSFET circuits** MOSFET structure and I-V characteristics. MOSFET as a switch. MOSFET as an amplifier: small-signal model and biasing circuits, common-source, common-gate and common-drain amplifiers; small signal equivalent circuits - gain, input and output impedances, transconductance, high frequency equivalent circuit.	8
4.	**Differential, multi-stage and operational amplifiers** Differential amplifier; power amplifier; direct coupled multi-stage amplifier; internal structure of an operational amplifier, ideal op-amp, non-idealities in an op-amp (Output offset voltage, input bias current, input offset current, slew rate, gain bandwidth product)	8
5.	**Linear applications of op-amp** Idealized analysis of op-amp circuits. Inverting and non-inverting amplifier, differential amplifier, instrumentation amplifier, integrator, active filter, P, PI and PID controllers and lead/lag compensator using an op-amp, voltage regulator, oscillators (Wein bridge and phase shift). Analog to Digital Conversion.	8
6.	**Nonlinear applications of op-amp** Hysteretic Comparator, Zero Crossing Detector, Square-wave and triangular-wave generators, Precision rectifier, peak detector. Monoshot	6
	TOTAL	42

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RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

2nd Year - III Semester: B.Tech. (Electrical Engineering)

3EE4-07: Electrical Machine-I

Credit: 3 **Max. Marks: 150 (IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1.	**Magnetic fields and magnetic circuits** Review of magnetic circuits - MMF, flux, reluctance, inductance; review of Ampere Law and Biot Savart Law; Visualization of magnetic fields produced by a bar magnet and a current carrying coil - through air and through a combination of iron and air; influence of highly permeable materials on the magnetic flux lines.	6
2.	**Electromagnetic force and torque** B-H curve of magnetic materials; flux-linkage v/s current characteristic of magnetic circuits; linear and nonlinear magnetic circuits; energy stored in the magnetic circuit; force as a partial derivative of stored energy with respect to position of a moving element; torque as a partial derivative of stored energy with respect to angular position of a rotating element. Examples - galvanometer coil, relay contact, lifting magnet, rotating element with eccentricity or saliency	9
3.	**DC machines** Basic construction of a DC machine, magnetic structure - stator yoke, stator poles, pole-faces or shoes, air gap and armature core, visualization of magnetic field produced by the field winding excitation with armature winding open, air gap flux density distribution, flux per pole, induced EMF in an armature coil. Armature winding and commutation – Elementary armature coil and commutator, lap and wave windings, construction of commutator, linear commutation Derivation of back EMF equation, armature MMF wave, derivation of torque equation, armature reaction, air gap flux density distribution with armature reaction.	8
4.	**DC machine - motoring and generation** Armature circuit equation for motoring and generation, Types of field excitations – separately excited, shunt and series. Open circuit characteristic of separately excited DC generator, back EMF with armature reaction, voltage build-up in a shunt generator, critical field resistance and critical speed. V-I characteristics and torque-speed characteristics of separately excited, shunt and series motors. Speed control through armature voltage. Losses, load testing and back-to-back testing of DC machines.	7

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5.	**Transformers** Principle, construction and operation of single-phase transformers, equivalent circuit, phasor diagram, voltage regulation, losses and efficiency Testing - open circuit and short circuit tests, polarity test, back-to-back test, separation of hysteresis and eddy current losses Three-phase. transformer - construction, types of connection and their comparative features, Parallel operation of single-phase and three-phase transformers, Autotransformers - construction, principle, applications and comparison with two winding transformer, Magnetizing current, effect of nonlinear B-H curve of magnetic core material, harmonics in magnetization current,Phase conversion - Scott connection, three-phase to six-phase conversion, Tap-changing transformers - No-load and on-load tap-changing of transformers, Three-winding transformers. Cooling of transformers.	**12**
	TOTAL	**42**

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RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

2nd Year - III Semester: B.Tech. (Electrical Engineering)

3EE4-08: Electromagnetic Fields

Credit: 2 **Max. Marks: 100 (IA:20, ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1.	**Review of Vector Calculus** Vector algebra- addition, subtraction, components of vectors, scalar and vector multiplications, triple products, three orthogonal coordinate systems (rectangular, cylindrical and spherical). Vector calculus differentiation, partial differentiation, integration, vector operatordel, gradient, divergence and curl; integral theorems of vectors. Conversion of a vector from one coordinate system to another.	4
2.	**Static Electric Field** Coulomb's law, Electric field intensity, Electrical field due to point charges. Line, Surface and Volume charge distributions. Gauss law and its applications. Absolute Electric potential, Potential difference, Calculation of potential differences for different configurations. Electric dipole, Electrostatic Energy and Energy density.	4
3.	**Conductors, Dielectrics and Capacitance** Current and current density, Ohms Law in Point form, Continuity of current, Boundary conditions of perfect dielectric materials. Permittivity of dielectric materials, Capacitance, Capacitance of a two wire line, Poisson's equation, Laplace's equation, Solution of Laplace and Poisson's equation, Application of Laplace's and Poisson's equations.	4
4.	**Static Magnetic Fields** Biot-Savart Law, Ampere Law, Magnetic flux and magnetic flux density, Scalar and Vector Magnetic potentials. Steady magnetic fields produced by current carrying conductors.	4
5.	**Magnetic Forces, Materials and Inductance** Force on a moving charge, Force on a differential current element, Force between differential current elements, Nature of magnetic materials, Magnetization and permeability, Magnetic boundary conditions, Magnetic circuits, inductances and mutual inductances.	4
6.	**Time Varying Fields and Maxwell's Equations** Faraday's law for Electromagnetic induction, Displacement current, Point form of Maxwell's equation, Integral form of Maxwell's equations, Motional Electromotive forces. Boundary Conditions.	4
7.	**Electromagnetic Waves** Derivation of Wave Equation, Uniform Plane Waves, Maxwell's equation in Phasor form, Wave equation in Phasor form, Plane waves in free space and in a homogenous material. Wave equation for a conducting medium, Plane waves in lossy dielectrics, Propagation in good conductors, Skin effect. Poynting theorem.	4
	TOTAL	**28**

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3EE4-21: Analog Electronics Lab

Credit: 1 **Max. Marks: 50 (IA:30, ETE:20)**
0L+0T+2P

1) Plot gain-frequency characteristics of BJT amplifier with and without negative feedback in the emitter circuit and determine bandwidths, gain bandwidth products and gains at 1 kHz with and without negative feedback.
2) Study of series and shunt voltage regulators and measurement of line and load regulation and ripple factor.
3) Plot and study the characteristics of small signal amplifier using FET.
4) Study of push pull amplifier. Measure variation of output power & distortion with load.
5) Study Wein bridge oscillator and observe the effect of variation in R & C on oscillator frequency.
6) Study transistor phase shift oscillator and observe the effect of variation in R & C on oscillator frequency and compare with theoretical value.
7) Study the following oscillators and observe the effect of variation of C on oscillator frequency:
   (a) Hartley (b) Colpitts.
8) To plot the characteristics of UJT and UJT as relaxation.

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3EE4-22: Electrical Machines-I Lab

Credit: 2 **Max. Marks: 100 (IA:60, ETE:40)**
0L+0T+4P

1) To perform O.C. and S.C. test on a 1-phase transformer and to determine the parameters of its equivalent circuit its voltage regulation and efficiency.
2) To perform sumpner's test on two identical 1-phase transformers and find their efficiency & parameters of the equivalent circuit.
3) To determine the efficiency and voltage regulation of a single-phase transformer by direct loading.
4) To perform the heat run test on a delta/delta connected 3-phase transformer and determine the parameters for its equivalent circuit.
5) To perform the parallel operation of the transformer to obtain data to study the load sharing.
6) Separation of no load losses in single phase transformer.
7) To study conversion of three-phase supply to two-phase supply using Scott-Connection.
8) Speed control of D.C. shunt motor by field current control method & plot the curve for speed verses field current.
9) Speed control of D.C. shunt motor by armature voltage control method & plot the curve for speed verses armature voltage.
10) To determine the efficiency at full load of a D.C shunt machine considering it as a motor by performing Swinburne's test.
11) To perform Hopkinson's test on two similar DC shunt machines and hence obtain their efficiencies at various loads.

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3EE4-23: Electrical Circuit Design Lab

Credit: 2 **Max. Marks: 100 (IA:60, ETE:40)**
0L+0T+4P

1) Introduction to Datasheet Reading.
2) Introduction to Soldering - Desoldering process and tools.
3) Simulate characteristic of BJT and UJT. Validate on Bread Board or PCB.
4) Simulate Bridge Rectifier Circuit and validate on Bread Board or PCB.
   a) Half Bridge.
   b) Full Bridge.
5) Simulate Regulated Power Supply and validate on Bread Board or PCB.
   a) Positive Regulation (03 Volt to 15 Volt).
   b) Negative Regulation (03 Volt to 15 Volt).
   c) 25 Volt, 1–10 A Power Supply.
6) Simulate Multivibrator circuit using IC 555 and BJT separately. Validate on Bread Board or PCB.
   a) Astable Mode.
   b) Bistable Mode.
   c) Monostable Mode.
7) Introduction to Sensors to measure real time quantities and their implementation in different processes.
   (Proximity, Accelerometer, Pressure, Photo-detector, Ultrasonic Transducer, Smoke, Temperature, IR, Color, Humidity, etc.).
8) Hardware implementation of temperature control circuit using Thermistor.
9) Simulate Frequency divider circuit and validate it on Bread Board or PCB.
10) Hardware implementation of 6/12 V DC Motor Speed Control (Bidirectional)
11) Simulate Buck, Boost, Buck-Boost circuit and validate on Bread Board or PCB.
12) Simulate Battery Voltage Level Indicator Circuit and validate on Bread Board or PCB.

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Syllabus of

UNDERGRADUATE DEGREE COURSE

Electrical Engineering

Rajasthan Technical University, Kota

Effective from session: 2018 – 2019

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

2nd Year - IV Semester: B.Tech. (Electrical Engineering)

4EE2-01: Biology

Credit: 2 **Max. Marks: 100(IA:20, ETE:80)**

2L+0T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Introduction:** Purpose: To convey that Biology is as important a scientific discipline as Mathematics, Physics and Chemistry. Bring out the fundamental differences between science and engineering by drawing a comparison between eye and camera, Bird flying and aircraft. Mention the most exciting aspect of biology as an independent scientific discipline. Why we need to study biology? Discuss how biological observations of 18th Century that lead to major discoveries. Examples from Brownian motion and the origin of thermodynamics by referring to the original observation of Robert Brown and Julius Mayor. These examples will highlight the fundamental importance of observations in any scientific inquiry.	1
3	**Classification:** Purpose: To convey that classification *per se* is not what biology is all about. The underlying criterion, such as morphological, biochemical or ecological be highlighted. Hierarchy of life forms at phenomenological level. A common thread weaves this hierarchy Classification. Discuss classification based on (a) cellularity- Unicellular or multicellular (b) ultrastructureprokaryotes or eucaryotes. (c) energy and Carbon utilization -Autotrophs, heterotrophs, lithotropes (d) Ammonia excretion- aminotelic, uricotelic, ureotelic (e) Habitata- acquatic or terrestrial (e) Molecular taxonomy- three major kingdoms of life. A given organism can come under different category based on classification. Model organisms for the study of biology come from different groups. E.coli, S.cerevisiae, D. Melanogaster, C. elegance, A. Thaliana, M. musculus	3
4	**Genetics:** Purpose: To convey that "Genetics is to biology what Newton's laws are to Physical Sciences". Mendel's laws, Concept of segregation and independent assortment. Concept of allele. Gene mapping, Gene interaction, Epistasis. Meiosis and Mitosis be taught as a part of genetics. Emphasis to be give not to the mechanics of cell division nor the phases but how genetic material passes from parent to offspring. Concepts of recessiveness and dominance. Concept of mapping of phenotype to genes. Discuss about the single gene disorders in humans. Discuss the concept of complementation using human genetics.	3
5	**Biomolecules:** Purpose: To convey that all forms of life has the same building blocks and yet the manifestations are as diverse as one can imagine. Molecules of life. In this context discuss monomeric units and polymeric structures. Discuss about sugars, starch and cellulose. Amino acids and proteins. Nucleotides and DNA/RNA. Two carbon units and lipids.	3

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6	**Enzymes:** Purpose: To convey that without catalysis life would not have existed on earth. Enzymology: How to monitor enzyme catalysed reactions. How does an enzyme catalyse reactions? Enzyme classification. Mechanism of enzyme action. Discuss at least two examples. Enzyme kinetics and kinetic	**3**
7	**Information Transfer:** Purpose: The molecular basis of coding and decoding genetic information is universal. Molecular basis of information transfer. DNA as a genetic material. Hierarchy of DNA structure- from single stranded to double helix to nucleosomes. Concept of genetic code. Universality and degeneracy of genetic code. Define gene in terms of complementation and recombination.	**3**
8	**Macromolecular analysis:** Purpose: To analyse biological processes at the reductionistic level. Proteins- structure and function. Hierarch in protein structure. Primary secondary, tertiary and quaternary structure. Proteins as enzymes, transporters, receptors and structural elements.	**4**
9	**Metabolism:** Purpose: The fundamental principles of energy transactions are the same in physical and biological world. Thermodynamics as applied to biological systems. Exothermic and endothermic versus endergonic and exergonic reactions. Concept of Keq and its relation to standard free energy. Spontaneity. ATP as an energy currency. This should include the breakdown of glucose to CO2 + H2O (Glycolysis and Krebs cycle) and synthesis of glucose from CO2 and H2O (Photosynthesis). Energy yielding and energy consuming reactions. Concept of Energy charge.	**4**
10	**Microbiology:** Concept of single celled organisms. Concept of species and strains. Identification and classification of microorganisms. Microscopy. Ecological aspects of single celled organisms. Sterilization and media compositions. Growth kinetics.	**3**
	Total	**28**

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RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

2nd Year - IV Semester: B.Tech. (Electrical Engineering)

4EE1-03/3EE1-03: Managerial Economics and Financial Accounting

Credit: 2 **Max. Marks: 100 (IA:20, ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Basic economic concepts** Meaning, nature and scope of economics, deductive vs inductive methods, static and dynamics, Economic problems: scarcity and choice, circular flow of economic activity, national income-concepts and measurement.	3
3	**Demand and Supply analysis** Demand-types of demand, determinants of demand, demand function, elasticity of demand, demand forecasting –purpose, determinants and methods, Supply-determinants of supply, supply function, elasticity of supply.	5
4	**Production and Cost analysis** Theory of production- production function, law of variable proportions, laws of returns to scale, production optimization, least cost combination of inputs, isoquants. Cost concepts-explicit and implicit cost, fixed and variable cost, opportunity cost, sunk costs, cost function, cost curves, cost and output decisions, cost estimation.	5
5	**Market structure and pricing theory** Perfect competition, Monopoly, Monopolistic competition, Oligopoly.	4
6	**Financial statement analysis** Balance sheet and related concepts, profit and loss statement and related concepts, financial ratio analysis, cash-flow analysis, funds-flow analysis, comparative financial statement, analysis and interpretation of financial statements, capital budgeting techniques.	8
	TOTAL	26

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RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

2nd Year - IV Semester: B.Tech. (Electrical Engineering)

4EE1-02/3EE1-02: Technical Communication

Credit: 2 **Max. Marks: 100 (IA:20, ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Introduction to Technical Communication-** Definition of technical communication, Aspects of technical communication, forms of technical communication, importance of technical communication, technical communication skills (Listening, speaking, writing, reading writing), linguistic ability, style in technical communication.	3
3	**Comprehension of Technical Materials/Texts and Information Design & development-** Reading of technical texts, Readingand comprehending instructions and technical manuals, Interpreting and summarizing technical texts, Note-making. Introduction of different kinds of technical documents, Information collection, factors affecting information and document design, Strategies for organization, Information design and writing for print and online media.	6
4	**Technical Writing, Grammar and Editing**- Technical writing process, forms of technical discourse, Writing, drafts and revising, Basics of grammar, common error in writing and speaking, Study of advanced grammar, Editing strategies to achieve appropriate technical style, Introduction to advanced technical communication. Planning, drafting and writing Official Notes, Letters, E-mail, Resume, Job Application, Minutes of Meetings.	8
5	**Advanced Technical Writing**- Technical Reports, types of technical reports, Characteristics and formats and structure of technical reports. Technical Project Proposals, types of technical proposals, Characteristics and formats and structure of technical proposals. Technical Articles, types of technical articles, Writing strategies, structure and formats of technical articles.	8
	TOTAL	26

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RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

2nd Year - IV Semester: B.Tech. (Electrical Engineering)

4EE3-04: Electronic Measurement and Instrumentation

Credit: 2 **Max. Marks: 100(IA:20, ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Measuring Instruments:** Moving coil, moving iron, electrodynamic and induction instruments-construction, operation, torque equation and errors. Applications of instruments for measurement of current, voltage, single-phase power and single-phase energy. Errors in wattmeter and energy meter and their compensation and adjustment. Testing and calibration of single-phase energy meter by phantom loading.	4
3	**Polyphase Metering:** Blondel's Theorem for n-phase, p-wire system. Measurement of power and reactive kVA in 3-phase balanced and unbalanced systems: One-wattmeter, two- wattmeter and three-wattmeter methods. 3-phase induction type energy meter. Instrument Transformers: Construction and operation of current and potential transformers. Ratio and phase angle errors and their minimization. Effect of variation of power factor, secondary burden and frequency on errors. Testing of CTs and PTs. Applications of CTs and PTs for the measurement of current, voltage, power and energy.	6
5	**Potentiometers:** Construction, operation and standardization of DC potentiometers– slide wire and Crompton potentiometers. Use of potentiometer for measurement of resistance and voltmeter and ammeter calibrations. Volt ratio boxes. Construction, operation and standardization of AC potentiometer in-phase and quadrature potentiometers. Applications of AC potentiometers.	5
6	**Measurement of Resistances:** Classification of resistance. Measurement of medium resistances – ammeter and voltmeter method, substitution method, Wheatstone bridge method. Measurement of low resistances – Potentiometer method and Kelvin's double bridge method. Measurement of high resistance: Price's Guard-wire method. Measurement of earth resistance.	6
7	**AC Bridges:** Generalized treatment of four-arm AC bridges. Sources and detectors. Maxwell's bridge, Hay's bridge and Anderson bridge for self-inductance measurement. Heaviside's bridge for mutual inductance measurement. De Sauty Bridge for capacitance measurement. Wien's bridge for capacitance and frequency measurements. Sources of error in bridge measurements and precautions. Screening of bridge components. Wagner earth device.	6
	Total	28

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SYLLABUS

2nd Year - IV Semester: B.Tech. (Electrical Engineering)

4EE4-05: Electrical Machines – II

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Fundamentals of AC machine windings** Physical arrangement of windings in stator and cylindrical rotor; slots for windings; single turn coil - active portion and overhang; full-pitch coils, concentrated winding, distributed winding, winding axis, 3D visualization of the above winding types, Air-gap MMF distribution with fixed current through winding - concentrated and distributed, Sinusoidally distributed winding, winding distribution factor.	7
3	**Pulsating and revolving magnetic fields** Constant magnetic field, pulsating magnetic field - alternating current in windings with spatial displacement, Magnetic field produced by a single winding - fixed current and alternating current Pulsating fields produced by spatially displaced windings, Windings spatially shifted by 90 degrees, Addition of pulsating magnetic fields, Three windings spatially shifted by 120 degrees (carrying three-phase balanced currents), revolving magnetic field.	4
4	**Induction Machines** Construction, Types (squirrel cage and slip-ring), Torque Slip Characteristics, Starting and Maximum Torque. Equivalent circuit. Phasor Diagram, Losses and Efficiency. Effect of parameter variation on torque speed characteristics (variation of rotor and stator resistances, stator voltage, frequency). Methods of starting, braking and speed control for induction motors. Generator operation. Self-excitation. Doubly-Fed Induction Machines.	12
5	**Single-phase induction motors** Constructional features, double revolving field theory, equivalent circuit, determination of parameters. Split-phase starting methods and applications.	6
6	**Synchronous machines** Constructional features, cylindrical rotor synchronous machine - generated EMF, equivalent circuit and phasor diagram, armature reaction, synchronous impedance, voltage regulation. Operating characteristics of synchronous machines, V-curves. Salient pole machine – two reaction theory, analysis of phasor diagram, power angle characteristics. Parallel operation of alternators - synchronization and load division.	10
	Total	40

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4EE4-06: Power Electronics

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**
3L+0T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Power switching devices** Diode, Thyristor, MOSFET, IGBT: I-V Characteristics; Firing circuit for thyristor; Voltage and current commutation of a thyristor; Gate drive circuits for MOSFET and IGBT.	5
3	**Thyristor rectifiers** Single-phase half-wave and full-wave rectifiers, Single-phase full-bridge thyristor rectifier with R-load and highly inductive load; Three-phase full-bridge thyristor rectifier with R-load and highly inductive load; Input current wave shape and power factor.	6
4	**DC-DC buck converter** Elementary chopper with an active switch and diode, concepts of duty ratio and average voltage, power circuit of a buck converter, analysis and waveforms at steady state, duty ratio control of output voltage.	5
5	**DC-DC boost converter** Power circuit of a boost converter, analysis and waveforms at steady state, relation between duty ratio and average output voltage.	5
6	**Single-phase voltage source inverter** Power circuit of single-phase voltage source inverter, switch states and instantaneous output voltage, square wave operation of the inverter, concept of average voltage over a switching cycle, bipolar sinusoidal modulation and unipolar sinusoidal modulation, modulation index and output voltage.	10
7	**Three-phase voltage source inverter** Power circuit of a three-phase voltage source inverter, switch states, instantaneous output voltages, average output voltages over a sub-cycle, three-phase sinusoidal modulation.	8
	Total	40

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RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

2nd Year - IV Semester: B.Tech. (Electrical Engineering)

4EE4-07: Signals and Systems

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Introduction to Signals and Systems:** Signals and systems as seen in everyday life, and in various branches of engineering and science. Signal properties: periodicity, absolute integrability, determinism and stochastic character. Some special signals of importance: the unit step, the unit impulse, the sinusoid, the complex exponential, some special time-limited signals; continuous and discrete time signals, continuous and discrete amplitude signals. System properties: linearity: additivity and homogeneity, shift-invariance, causality, stability, realizability. Examples.	6
3	**Behavior of continuous and discrete-time LTI systems:** Impulse response and step response, convolution, input-output behavior with aperiodic convergent inputs, cascade interconnections. Characterization of causality and stability of LTI systems. System representation through differential equations and difference equations. State-space Representation of systems. State-Space Analysis, Multi-input, multi-output representation. State Transition Matrix and its Role. Periodic inputs to an LTI system, the notion of a frequency response and its relation to the impulse response.	14
4	**Fourier, Laplace and z- Transforms:** Fourier series representation of periodic signals, Waveform Symmetries, Calculation of Fourier Coefficients. Fourier Transform, convolution/multiplication and their effect in the frequency domain, magnitude and phase response, Fourier domain duality. The Discrete- Time Fourier Transform (DTFT) and the Discrete Fourier Transform (DFT). Parseval's Theorem. Review of the Laplace Transform for continuous time signals and systems, system functions, poles and zeros of system functions and signals, Laplace domain analysis, solution to differential equations and system behavior. The z-Transform for discrete time signals and systems, system functions, poles and zeros of systems and sequences, z-domain analysis.	12
5	**Sampling and Reconstruction:** The Sampling Theorem and its implications. Spectra of sampled signals. Reconstruction: ideal interpolator, zero-order hold, first-order hold. Aliasing and its effects. Relation between continuous and discrete time systems. Introduction to the applications of signal and system theory: modulation for communication, filtering, feedback control systems.	8
	Total	41

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RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

2nd Year - IV Semester: B.Tech. (Electrical Engineering)

4EE4-08: Digital Electronics

Credit: 2 **Max. Marks: 100(IA:20, ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Fundamentals of Digital Systems and logicfamilies:** Digital signals, digital circuits, AND, OR, NOT, NAND, NOR and Exclusive-OR operations, Boolean algebra, examples of IC gates, number systems-binary, signed binary, octal hexadecimal number, binary arithmetic, one's and two's complements arithmetic, codes, error detecting and correcting codes, characteristics of digital lCs, digital logic families, TTL, Schottky TTL and CMOS logic, interfacing CMOS and TTL, Tri-state logic.	4
3	**Combinational DigitalCircuits:** Standard representation for logic functions, K-map representation, simplification of logic functions using K-map, minimization of logical functions. Don't care conditions, Multiplexer, De-Multiplexer/Decoders, Adders, Subtractors, BCD arithmetic, carry look ahead adder, serial adder, ALU, elementary ALU design, popular MSI chips, digital comparator, parity checker/generator, code converters, priority encoders, decoders/drivers for display devices, Q-M method of function realization.	6
4	**Sequential circuits and systems:** A 1-bit memory, the circuit properties of Bistable latch, the clocked SR flip flop, J- K-T and D-types flip flops, applications of flip flops, shift registers, applications of shift registers, serial to parallel converter, parallel to serial converter, ring counter, sequence generator, ripple (Asynchronous) counters, synchronous counters, counters design using flip flops, special counter IC's, asynchronous sequential counters, applications of counters.	6
5	**A/D and D/A Converters:** Digital to analog converters: weighted resistor/converter, R-2R Ladder D/A converter, specifications for D/A converters, examples of D/A converter lCs, sample and hold circuit, analog to digital converters: quantization and encoding, parallel comparator A/D converter, successive approximation A/D converter, counting A/D converter, dual slope A/D converter, A/D converter using voltage to frequency and voltage to time conversion, specifications of A/D converters, example of A/D converter ICs	4
6	**Semiconductor memories and Programmable logic devices** Memory organization and operation, expanding memory size, classification and characteristics of memories, sequential memory, read only memory (ROM), read and write memory(RAM), content addressable memory (CAM), charge de coupled device memory (CCD), commonly used memory chips, ROM as a PLD, Programmable logic array, Programmable array logic, complex Programmable logic devices (CPLDS), Field Programmable Gate Array (FPGA).	7
	Total	28

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4EE4-21: Electrical Machines - II Lab

Credit: 2 **Max. Marks: 100(IA:60, ETE:40)**

0L+0T+4P

1) To study various types of starters used for 3 phase induction motor.
2) To connect two 3-phase induction motor in cascade and study their speed control.
3) To perform load test on 3-phase induction motor and calculate torque, output power, input power, efficiency, input power factor and slip for various load settings.
4) To perform no load and blocked rotor test on a 3-phase induction motor and determine the parameters of its equivalent circuits.
5) Draw the circle diagram and compute the following (i) Max. Torque (ii) Current (iii) slips (iv) p. f. (v) Efficiency.
6) Speed control of 3- Φ Induction Motor
7) To plot the O.C.C. & S.C.C. of an alternator.
8) To determine Zs , Xd and Xq by slip test, Zero power factor (ZPF)/ Potier reactance method.
9) To determine the voltage regulation of a 3-phase alternator by direct loading.
10) To determine the voltage regulation of a 3-phase alternator by synchronous impedance method.
11) To study effect of variation of field current upon the stator current and power factor of synchronous motor andPlot V-Curve and inverted V-Curve of synchronous motor for different values of loads.
12) To synchronize an alternator across the infinite bus and control load sharing.

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4EE4-22: Power Electronics Lab

Credit: 2 **Max. Marks: 100(IA:60, ETE:40)**
0L+0T+4P

1) Study the comparison of following power electronics devices regarding ratings, performance characteristics and applications: Power Diode, Power Transistor, Thyristor, Diac, Triac, GTO, MOSFET, MCT and SIT.
2) Determine V-I characteristics of SCR and measure forward breakdown voltage, latching and holding currents.
3) Find V-I characteristics of TRIAC and DIAC.
4) Find output characteristics of MOSFET and IGBT.
5) Find transfer characteristics of MOSFET and IGBT.
6) Find UJT static emitter characteristics and study the variation in peak point and valley point.
7) Study and test firing circuits for SCR-R, RC and UJT firing circuits.
8) Study and test 3-phase diode bridge rectifier with R and RL loads. Study the effect of filters.
9) Study and obtain waveforms of single-phase half wave controlled rectifier with and without filters. Study the variation of output voltage with respect to firing angle.
10) Study and obtain waveforms of single-phase half controlled bridge rectifier with R and RL loads. Study and show the effect of freewheeling diode.
11) Study and obtain waveforms of single-phase full controlled bridge converter with R and RL loads. Study and show rectification and inversion operations with and without freewheeling diode.
12) Control the speed of a dc motor using single-phase half controlled bridge rectifier and full controlled bridge rectifier. Plot armature voltage versus speed characteristics.

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4EE4-23: Digital Electronics Lab

Credit: 1 **Max. Marks: 50(IA:30, ETE:20)**
0L+0T+2P

1) To verify the truth tables of basic logic gates: AND, OR, NOR, NAND, NOR. Also to verify the truth table of Ex-OR, Ex-NOR (For 2, 3, & 4 inputs using gates with 2, 3, & 4 inputs).
2) To verify the truth table of OR, AND, NOR, Ex-OR, Ex-NOR realized using NAND & NOR gates.
3) To realize an SOP and POS expression.
4) To realize Half adder/ Subtractor & Full Adder/ Subtractor using NAND & NOR gates and to verify their truth tables.
5) To realize a 4-bit ripple adder/ Subtractor using basic half adder/ Subtractor & basic Full Adder/ Subtractor.
6) To verify the truth table of 4-to-1 multiplexer and 1-to-4 demultiplexer. Realize the multiplexer using basic gates only. Also to construct and 8-to-1 multiplexer and 1-to-8 demultiplexer using blocks of 4-to-1 multiplexer and 1-to-4 demultiplexer.
7) Design & Realize a combinational circuit that will accept a 2421 BCD code and drive a TIL -312 seven segment display.
8) Using basic logic gates, realize the R-S, J-K and D-flip flops with and without clock signal and verify their truth table.
9) Construct a divide by 2,4& 8 asynchronous counter. Construct a 4-bit binary counter and ring counter for a particular output pattern using D flip flop.
10) Perform input/output operations on parallel in/Parallel out and Serial in/Serial out registers using clock. Also exercise loading only one of multiple values into the register using multiplexer.

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4EE4-24: Measurement Lab

Credit: 1 **Max. Marks: 50(IA:30, ETE:20)**
0L+0T+2P

1) Study working and applications of (i) C.R.O. (ii) Digital Storage C.R.O. & (ii) C.R.O. Probes.
2) Study working and applications of Meggar, Tong-tester, P.F. Meter and Phase Shifter.
3) Measure power and power factor in 3-phase load by (i) Two-wattmeter method and (ii) One-wattmeter method.
4) Calibrate an ammeter using DC slide wire potentiometer.
5) Calibrate a voltmeter using Crompton potentiometer.
6) Measure low resistance by Crompton potentiometer.
7) Measure Low resistance by Kelvin's double bridge.
8) Measure earth resistance using fall of potential method.
9) Calibrate a single-phase energy meter by phantom loading at different power factors.
10) Measure self-inductance using Anderson's bridge.

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Syllabus B.Tech. (Electrical Engineering), 5th semester

5EE1A: POWER ELECTRONICS (Common for EE and EX)

B.Tech. (Electrical) 5th Semester **Max. Marks: 80**
3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**Power Semiconductor Devices:**Construction, Principle of operation, Characteristics and applications of Power Transistor& Thyristor.	3
	Characteristics of GTO, DIAC, MCT, TRIAC, Power MOSFET and IGBT; Two-Transistor Model of Thyristor, Thyristor Commutation methods.	5
2	**SCR:** Construction and characteristics, specification and ratings, pulse transformer, optical isolators, methods of turn on, triggering circuits for SCR: R, RC, UJT relaxation oscillator.	4
	Rating extension by series and parallel connections, string efficiency. Protection of SCR-Protection against over voltage, over current, dv/dt, di/dt, Gate protection.	4
3	**Converters-I:** Single Phase half & full wave converters with RL & RLE load, Single phase dual converters, Three phase half wave converters.	5
	Three phase full converters with RL load, Three phase dual converters.	3
4	**Converters-II:** Single and three-phase semi converters with RL & RLE load. Power factor improvement-Extinction angle control, symmetrical angle control, pulse width modulation control and sinusoidal pulse width modulation control.	6
	Inversion operation. Effect of load and source impedances.	2
5	**DC-DC Converters:** Step Up/Down Copper, Control strategies, Chopper Configurations, Analysis of type A Chopper	4
	Voltage, current and load commutated chopper. Multiphase Chopper	4

Text Books

S.No.	Name of authors'/books/publisher	Year of Pub.
1	M. D. Singh and K. B. Khanchandani: Power Electronics 2/e, MGH.	2008
2	M. H. Rashid: Power Electronics, Circuits Devices and Applications, Pearson.	2011

Reference Books

S. No.	Name of authors'/books/publisher	Year of Pub.
1	V. R. Moorthi: Power Electronics-Devices, Circuits and Industrial Applications, Oxford.	2005
2	Theodore Wildi: Electrical Machines, Drives and Power Systems, Pearson.	2007
3	Ned Mohan: Power Electronics, John Wiley.	2013
4	Krein P. T.: Elements of Power Electronics, Oxford.	1998
5	P. S. Bimbhra: Power Electronics, Khanna Publishers.	2012

5EE2A: MICROPROCESSOR ANDCOMPUTER ARCHITECTURE

B.Tech. (Electrical) 5th Semester **Max. Marks: 80**
3L **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**Introduction to 8085 Microprocessor Architecture:** CPU, address bus, data bus and control bus. Input/Output devices, buffers,encoders, latches and memories.	4
	Internal Data Operations and Registers, Pins and Signals, Peripheral Devices and Memory Organization, Interrupts.	4
2	**8085 Microprocessor Instructions:** Classification, Format and Timing.	4
	Instruction Set: 8 Bit and 16 Bit Instructions, Programming and Debugging, Subroutines.	4
3	**8085 Microprocessor Interfacing:** 8259, 8257, 8255, 8253, 8155 chips and their applications.	5
	A/D conversion, memory, keyboard and display interface (8279).	3
4	**8086 Microprocessor: Architecture:** Architecture of INTEL 8086 (Bus Interface Unit, Execution unit), register organization, memory addressing, memory segmentation, Operating Modes	6
	Instruction Set of 8086: Addressing Modes: Instruction format: Discussion on instruction Set: Groups: data transfer, arithmetic, logic string, branch control transfer, processor control. **Interrupts:** Hardware and software interrupts, responses and types.	2
5	**Basic Computer Architecture:** Central Processing Unit, memory and input/output interfacing. Memory Classification Volatile and non-volatile memory, Primary and secondary memory, Static and Dynamic memory, Logical, Virtual and Physical memory.	4
	Types Of Memory: Magnetic core memory, binary cell, Rom architecture and different types of ROM, RAM architecture, PROM, PAL, PLA, Flash and Cache memory, SDRAM, RDRAM and DDRAM. Memory latency, memory bandwidth, memory seek time.	4

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	Gaonkar, Ramesh S.: Microprocessor Architecture, programming and Applications with the 8085, Pen Ram International Publishing 5th Ed.	2002
2	K. Udaykumar and B. S. Umashankar: The 8085 Microprocessor: Architecture, Programming and Interfacing, Pearson Publisher.	2008

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1.	Douglas V. Hall: Microprocessors and Interfacing, Revised Second Edition (SIE), MGH.	2007
2.	Ray. A. K. & Burchandi, K. M.: Advanced Microprocessors and Peripherals, Architecture,Programming and Interfacing, MGH.	2006
3.	Lyla B. Das: The X 86 Microprocessors: Architecture, Programming and Interfacing (8086 to Pentium), Pearson Publisher.	2010
4.	Krishna Kant: Microprocessors and Microcontrollers, PHI Learning.	2007
5.	M. Rafiquzzaman: Microprocessors-Theory and applications, PHI.	1993
6.	B. Ram: Advanced Microprocessor & Interfacing. MGH.	2000

5EE3A: CONTROLSYSTEMS (Common for EE and EX)

B.Tech. (Electrical) 5th Semester **Max. Marks: 80**
3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**Introduction:** Elements of control systems, concept of open loop and closed loop systems, Examples and application of open loop and closed loop systems, brief idea of multivariable control systems.	5
	Mathematical Modeling of Physical Systems: Representation of physical system (Electro Mechanical) by differential equations, Determination of transfer function by block diagram reduction techniques and signal flow method, Laplace transformation function, inverse Laplace transformation.	3
2	**Time Response Analysis of First Order and Second Order System**:Characteristic equations, response to step, ramp and parabolic inputs.	4
	Transient response analysis, steady state errors and error constants, Transient & steady state analysis of LTI systems	4
3	**Control System Components:** Constructional and working concept of ac servomotor, synchronous and stepper motor	5
	Stability and Algebraic Criteria: concept of stability and necessary conditions, Routh-Hurwitz criteria and limitations.Root Locus Technique: The root locus concepts, construction of root loci.	3
4	**Frequency Response Analysis:** Frequency response, correlation between time and frequency responses,polar and inverse polar plots, Bode plots	5
	Stability in Frequency Domain: Nyquist stability criterion, assessment of relative stability: gain margin and phase margin, M and N Loci, Nichols chart.	3
5	The design problem and preliminary considerations lead, lag and lead-lag networks, design of closed loop systems using compensation techniques in time domain and frequency domain.	4
	Brief idea of proportional, derivative and integral controllers.	4

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	Smarjit Ghosh, Control Systems: Theory and Applications, 2/e, Pearson Publisher.	2004
2	Dhannesh N. Manik: Control System, Cengage Learning.	2012

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	I. J. Nagrath and M. Gopal: Control Systems Engineering, 3rd Ed, New Age Publication.	2008
2	K. R. Varmah: Control Systems, MGH	2010
3	Anandnatrajan et. al.: Control Systems Engineering, 4th ed., Scitech Pub.	2013
4	K. Ogata: Modern Control Engineering, Prentice Hall of India.	2010
5	Norman S. Nise: Control System Engineering, John Wiley & Sons.	2011
6	Richard C. Dorf, Robert H. Bishop: Modern Control Systems, Prentice-Hall	2000
7	Robert H. Bishop: Modern Control Systems, Boyd and Fraser pub	2000

5EE4A: DATA BASE MANGEMENT SYSTEM (Common for EE and EX)

B.Tech. (Electrical) 5th Semester **Max. Marks: 80**
3L **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	Introduction, need, purpose and goals of DBMS. DBMS Architecture, Concept of keys, Generalization and specialization,	4
	Introduction to relational data model, ER modeling, concept of ER diagram	4
2	**Database Design:** Conceptual Data Base design. Theory of normalization, Primitive and composite data types, concept of physical and logical databases,	3
	Data abstraction and data independence, relational algebra and relational calculus.	5
3	**SQL, DDL and DML**. Constraints assertions, views database security. Application Development using SQL: Host Language interface embedded SQL programming.	5
	GL's, Forms management and report writers. Stored procedures and triggers. Dynamic SQL, JDBC.	3
4	**Internal of RDBMS**: Physical data organization in sequential, indexed, random and hashed files. Inverted and multi-list structures	8
5	**(i) Transaction Management:** Transaction concept, transaction state, serializability, conflict serializability, views serializability. **(ii) Concurrency Control:** Lock based protocol.	4
	(iii) Deadlock Handling: Prevention detection, recovery. **(iv) Recovery System:** Log based recovery.	4

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	Silverschatz Korth and Sudarshan: Database System Concepts, 6th ed., MGH.	2011
2	Raghu Rama Krishnan: Database Management Systems, 2nd ed., MGH.	2003

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	S. K Singh: Database System Concepts, Designs and Applications, Pearson Education	2011
2	Elmasari: Fundamentals of Data Base Systems, Pearson Education.	2003
3	G. K. Gupta: Database Management Systems, MGH.	2011
4	Date C. J.: An Introduction To Database System, Addition Wesley.	2003
5	Alex Berson & Stephen J. Smith: Data Warehousing, Data Mining & OLAP, MGH.	2011
6	Mallach: Data Warehousing System, MGH.	2003
7	Majumdar & Bhattacharya: Database Management System, MGH.	2011

5EE5A: TRANSMISSION & DISTRIBUTION OF ELECTRICAL POWER (Common for EE and EX)

B.Tech. (Electrical) 5th Semester **Max. Marks: 80**

3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**Supply systems:**Basic network of power system. Transmission and distribution voltage, effect of system voltage on size of conductor and losses. Comparison of DC 2- wire, DC 3-wire, 1-phase AC and 3-phase AC (3-wire and 4-wire) systems.	5
	Distribution Systems: Primary and secondary distribution systems, feeder, distributor and service mains. Radial and ring- main distribution systems. Kelvin's law for conductor size.	3
2	**Mechanical Features of Overhead Lines:**Conductor material and types of conductor. Conductor arrangements and spacing.	4
	Calculation of sag and tension, supports at different levels, effect of wind and ice loading, stringing chart and sag template. Conductor vibrations and vibration dampers.	4
3	**Parameters of Transmission Lines:**Resistance inductance and capacitance of overheadlines, effect of earth, line transposition. Geometric mean radius and distance.	4
	Inductance and capacitance of line with symmetrical and unsymmetrical spacing Inductance and capacitance of double circuit lines. Skin and proximity effects.Equivalent circuits and performance of short and medium transmission lines.	4
4	**Generalized ABCD Line Constants:**equivalent circuit and performance of long transmission line. Ferranti effect. Interference with communication circuits. Power flow through atransmission line	6
	Corona: Electric stress between parallel conductors. Disruptive critical voltage and visual critical voltage, Factors affecting corona. Corona power loss. Effects of corona.	2
5	**Insulators:** Pin, shackle, suspension, post and strain insulators. Voltage distribution acrossan insulator string, grading and methods of improving string efficiency.	4
	Underground Cables: Conductor, insulator, sheathing and armoring materials. Types of cables. Insulator resistance and capacitance calculation. Electrostatic stresses and reduction of maximum stresses. Causes of breakdown. Thermal rating of cable. Introduction to oil filled and gas filled cables.	4

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	S. Sivanagaraju and S. Satyanarayana: Electric Power Transmission and Distribution, Pearson Publisher.	2008
2	A. S. Pabla: Electric Power Distribution, MGH.	2012

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	B. R. Gupta: Power System Analysis & Design, S. Chand Publishers.	2008
2	Soni, Gupta and Bhatnagar: A Course in Electrical Power, Dhanpat Rai.	1987
3	C. L. Wadhwa: Electrical Power Systems, New Age.	2009
4	Nagrath Kothari: Modern Power System Analysis, MGH.	2011
5	J. J. Grainger & W. D. Stevenson: Power System Analysis, MGH.	2003
6	Kamaraju: Electrical Power Distribution Systems, MGH.	2009

5EE6.1A: OPTIMIZATION TECHNIQUES

B.Tech. (Electrical) 5th Semester **Max. Marks: 80**
3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**Introduction:** Engineering application of Optimization, Formulation of design problems as mathematical programming problems, classification of optimization problems.	8
2	**Optimization Techniques:** Classical optimization, multivariable with no constraints, unconstrained minimization techniques,	4
	Penalty function techniques, Lagrange multipliers and feasibility techniques.	4
3	**Linear Programming:** Graphical method, Simplex method, Duality in linear programming (LP), Sensitivity analysis Applications in civil engineering.	5
4	**Non Linear Programming Techniques/Method:** Unconstrained optimization, one dimensional minimization, golden section, elimination, quadratic and cubic, Fibonacci, interpolation	6
	Direct search, Descent, Constrained optimization, Direct and indirect, Optimization with calculus, Khun-Tucker conditions.	2
5	**Constrained Optimization Techniques:** Direct, complex, cutting plane, exterior penalty function methods for structural engineering problems.	8

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	Rao S. S.: Engineering Optimization- Theory and Practice, New Age International.	2009
2	Hadley. G.: Linear programming, Narosa Publishing House, New Delhi.	2003

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	Deb. K.: Optimization for Engineering Design- Algorithms and Examples, PHI.	2012
2	Bhavikatti S. S.: Structural Optimization Using Sequential Linear Programming, Vikas Publishing House, New Delhi.	2003
3	Spunt: Optimum Structural Design, Prentice Hall.	1971
4	Uri Krisch: Optimum Structural Design, MGH.	1981

5EE6.2A: PRINCIPLE OF COMMUNICATION SYSTEMS (Common for EE and EX)

B.Tech. (Electrical) 5th Semester **Max. Marks: 80**

3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**Noise Effects in Communication Systems:** Resistor noise, Networks with reactive elements, Noise temperature, Noise bandwidth, effective input noise temperature,	2
	Noise figure. Noise figure & equivalent noise temperature in cascaded circuits.	6
2	**Amplitude Modulation:** Frequency translation, Recovery of base band signal, Spectrum & power relations in AM systems.	4
	Methods of generation & demodulation of AM-DSB, AMDSB/SC and AM-SSB signals. Modulation & detector circuits for AM systems. AM transmitters & receivers.	4
3	**Frequency Modulation:** Phase & freq. modulation & their relationship, Spectrum & bandwidth of a sinusoidally modulated FM signal, phasor diagram, Narrow band & wide band FM. Generation & demodulation of FM signals.	5
	FM transmitters & receivers, Comparison of AM, FM & PM. Pre emphasis & de-emphasis. Threshold in FM, PLL demodulator.	3
4	**Noise in AM and FM:** Calculation of signal-to-noise ratio in SSB-SC, DSB-SC, DSB with carrier, Noise calculation of square law demodulator & envelope detector.	6
	Calculation of S/N ratio in FM demodulators, Super-heterodyne receivers.	2
5	**Pulse Modulation Systems:** Sampling theorem, Generation and demodulation methods of PAM, PWM, PPM.	8

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	John G. Proakis, and Masoud Salehi: Fundamentals of Communication Systems, Pearson Publisher.	2007
2	P. Ramakrishna Rao: Communication Systems, MGH.	2013

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	V. Chandra Sekar: Communication Systems, Oxford.	2006
2	Taub and Schilling: Principles of Communication Systems 3/e, MGH.	2008
3	B. P. Lathi: Modern Analog & Digital Communication System, 4th ed., Oxford.	2009
4	Simon Hykin: Communication Systems, John Wiley and Sons.	2008
5	R. P. Singh and S. D. Sapre: Communication System Analog & Digital 2/e, MGH.	2008
6	G. Kennedy and B. Davis: Electronic Communication Systems, MGH.	1993
7	Roy Blake: Wireless Communication Technology, Thomson Asia Pvt. Ltd. Singapore.	2008

5EE6.3A INTRODUCTION TO VLSI (Common for EE and EX)

B.Tech. (Electrical) 5th semester **Max. Marks: 80**

3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**Introduction to MOS Technology:** Basic MOS transistors, Enhancement Mode transistoraction, Depletion Mode transistor action, NMOS and CMOS fabrication.	8
2	**Basic Electrical Properties of MOS Circuits:** I_{DS} versus V_{DS} relationship, Aspects of threshold voltage, Transistor Trans conductance gm.	4
	The NMOS inverter, Pull up to Pull-down ratio for a NMOS Inverter and CMOS Inverter(B_n/B_p), MOS transistor circuit Model, Noise Margin.	4
3	**CMOS Logic Circuits:** The inverter, Combinational Logic, NAND Gate NOR gate, Compound Gates, 2 input CMOS Multiplexer, Memory latches and registers	5
	Transmission Gate, Gate delays, CMOS-Gate Transistor sizing, Power dissipation	3
4	**Basic Physical Design of Simple Gates and Layout Issues:** Layout issues for inverter, Layout for NAND and NOR Gates,	6
	Complex Logic gates Layout, Layout optimization for performance.	2
5	Introduction to VHDL, Verilog & other design tools. VHDL Code for simple Logic gates, flip-flops, shift-registers, Counters, Multiplexers, adders and subtractors.	8

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	S. M. Sze: VLSI Technology, MGH.	2003
2	Debaprasad Das: VLSI Design, Oxford.	2011

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	Angsuman Sarkaret. al.: VLSI Design and EDA Tools, Scitech Pub.	2011
2	S. M. Kang: CMOS Digital Integrated Circuits, MGH.	2003
3	Stephen A. Campbell: The Science & Engineering of Microelectronic Fabrication, Oxford.	2001
4	James D. Plummer, Micheal Deal & Petter B. Griffin: Silicon VLSI Tech. Fundamental Practice & Modeling, Prentice Hall.	2000

5EE7A: POWER ELECTRONICS LAB (Common for EE and EX)

1. Study the comparison of following power electronics devices regarding ratings, performance characteristics and applications: Power Diode, Power Transistor, Thyristor, Diac, Triac, GTO, MOSFET, MCT and SIT.
2. Determine V-I characteristics of SCR and measure forward breakdown voltage, latching and holding currents.
3. Find V-I characteristics of TRIAC and DIAC.
4. Find output characteristics of MOSFET and IGBT.
5. Find transfer characteristics of MOSFET and IGBT.
6. Find UJT static emitter characteristics and study the variation in peak point and valley point.
7. Study and test firing circuits for SCR-R, RC and UJT firing circuits.
8. Study and test 3-phase diode bridge rectifier with R and RL loads. Study the effect of filters.
9. Study and obtain waveforms of single-phase half wave controlled rectifier with and without filters. Study the variation of output voltage with respect to firing angle.
10. Study and obtain waveforms of single-phase half controlled bridge rectifier with R and RL loads. Study and show the effect of freewheeling diode.
11. Study and obtain waveforms of single-phase full controlled bridge converter with R and RL loads. Study and show rectification and inversion operations with and without freewheeling diode.
12. Control the speed of a dc motor using single-phase half controlled bridge rectifier and full controlled bridge rectifier. Plot armature voltage versus speed characteristics.

Reference/Suggested Books

1. O. P. Arora: Power Electronics Laboratory-Experiments and Organization, Narosa Pub.
2. P. B. Zbar: Industrial Electronics- A Text-Lab Manual, MGH.

5EE8A: MICROPROCESSOR LAB

1. Study the hardware, functions, memory structure and operation of 8085-Microprocessor kit.
2. Program to perform integer division: (1) 8-bit by 8-bit (2) 16-bit by 8-bit.
3. Transfer of a block of data in memory to another place in memory
4. Transfer of black to another location in reverse order.
5. Searching a number in an array.
6. Sorting of array in: (1) Ascending order (2) Descending order.
7. Finding party of a 32-bit number.
8. Program to perform following conversion (1) BCD to ASCII (2) BCD to hexadecimal.
9. Program to multiply two 8–bit numbers
10. Program to generate and sum 15 Fibonacci numbers.
11. Program for rolling display of message “India”, “HELLO”.
12. To insert a number at correct place in a sorted array.
13. Reversing bits of an 8-bit number.
14. Fabrication of 8-bit LED interfaces for 8085 kit through 8155 and 8255.
15. Data transfer on output port 8155 & 8255 & implementation of disco light, running light, and sequential lights on the above mentioned hardware.
16. Parallel data transfer between two DYNA-85 kit using 8253 ports.
17. Generation of different waveform on 8253/8254 programmable timer.

5EE9A: SYSTEM PROGRAMMING LAB (Common for EE and EX)

Basics of MATLAB matrices and vectors, matrix and array operations, Saving and loading data, plotting simple graphs, scripts and functions, Script files, Function files, Global Variables, Loops, Branches, Control flow, Advanced data objects, Multi-dimensional matrices, Structures, Applications in linear algebra curve fitting and interpolation. Numerical integration, Ordinary differential equation. (All contents is to be covered with tutorial sheets)

Simulink: Idea about simulink, problems based on simulink. (All contents is to be covered with tutorial sheets)Write a program to generate Machine Op- code table using two pass Assembler.

Reference/Suggested Books

1. Almos Gilat: MATLAB: An Introduction with Applications, Wiley India Ltd., 2004.
2. Ram N. Patel et. al.: Programming in MATLAB, Pearson.

5EE10A: DBMS LAB (Common for EE and EX)

1 Designing database and constraints using DDL statements.
2 Experiments for practicing SQL query execution on designed database.
3 Database connectivity using JDBC/ODBC.
4 Features of embedded SQL.
5 Designing front end in HLL and accessing data from backend database.
6 Designing simple projects using front end-back end programming
7 Project for generating Electricity Bills
8 Project for managing student's attendance/marks details.

5EE11APROFESSIONAL ETHICS AND DISASTERS MANAGEMENT(Common for EE and EX)

1 **Objectives:** to help the students
- To appreciate the importance and values and ethics in implementing the technology and ensure sustainable development, happiness and prosperity.
- To understand the co-existence with nature and to be aware of potential natural and manmade disasters.

2 **Human Values:** Effect of Technological Growth and Sustainable Development.
Profession and Human Values: Values crisis in contemporary society. Nature of values. Psychological Values, Societal Values and Aesthetic Values. Moral and Ethical values.

3 **Professional Ethics:**
- Professional and Professionalism-Professional Accountability, Role of a professional, Ethic and image of profession.
- Engineering Profession and Ethics-Technology and society, Ethical obligations of Engineering professionals, Roles of Engineers in industry, society, nation and the world.
- Professional Responsibilities-Collegiality, Loyalty, Confidentially, Conflict of Interest, Whistle Blowing.

4 **Disaster Management:** Understanding Disasters and Hazards and related issues social and environmental. Risk and Vulnerability. Types of Disasters, their occurrence/ causes, impact and preventive measures:
Natural Disasters- Hydro-meteorological Based Disasters like Flood, Flash Flood, Cloud Burst, Drought, Cyclone, Forest Fires; Geological Based Disasters like Earthquake, Tsunami, Landslides, Volcanic Eruptions.

5 **Manmade Disasters**: Chemical Industrial Hazards, Major Power Break Downs, Traffic Accidents, Fire Hazards, Nuclear Accidents. Disaster profile of Indian continent. Case studies. Disaster Management Cycle and its components.

In order to fulfill objectives of course,

(A) The institute shall be required to organize at least 3 expert lectures by eminent social workers/professional leaders.

(B) Each student shall compulsorily be required to:

I. Visit a social institution/NGO for at least 7 days during the semester and submit a Summary report.

II. Perform a case study of a disaster that has occurred in last decade and submit a Summary report.

Reference/Suggested Books

1. R Subramanian: Professional Ethics, oxford publishers.
2. Engineering Ethics: Concepts and cases by Charles E. Harris, Jr., Michael S. Pritchard, MichaelJ. Rabins. Cengage Learning, Delhi
3. Stephen H. Unger: Controlling Technology- Ethics and Responsible Engineers, John Willey and Sons.
4. Deborah Johnson: Ethical Issues in Engineering, Prentice Hall.
5. A. N. Tripathi: Human Values in the engineering Profession, Moniograph, Published by IIM Calcutta.
6. D. K. Sinha: Towards Basics of Natural Disaster Reduction, Researchco Book Center, Delhi.
7. Amita Sinvhal: Understanding Earthquake Disasters,MGH, New Delhi.
8. Selected Resources available on www.nidmindia.nic.in

Syllabus B.Tech. (Electrical Engineering), 6th semester

6EE1A: MODERN CONTROL THEORY (Common for EE and EX)

B.Tech. (Electrical) 6th semester **Max. Marks: 80**
3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**Introduction:** Concept of Linear vector space Linear Independence, Bases & Representation, domain and range. Concept of Linearity, relaxedness, time invariance, causality.	5
	State Space Approach of Control System Analysis: Modern Vs conventional control theory, concept of state, state variable state vector, state space, state space equations, Writing statespace equations of mechanical, Electrical systems, Analogous systems.	3
2	State Space Representation using physical and phase variables, comparison form of system representation. Block diagram representation of state model. Signal flow graph representation.	5
	State space representation using canonical variables. Diagonal matrix. Jordan canonical form, Derivation of transfer functions from state-model.	3
3	**Solution of State Equations:**Eigenvalues and Eigen vectors. Matrix.Exponential, State transition matrix, Properties of state transition matrix.	4
	Computation of State transition matrix concepts of controllability &observability, Pole placement by state feedback.	4
4	**Digital Control Systems:** Introduction, sampled data control systems, signal reconstruction, difference equations.	4
	The z-transform, Z-Transfer Function. Block diagram analysis of sampled data systems, z and s domain relationship.	4
5	Modeling of sample-hold circuit, steady state accuracy, stability in z-plane and Jury stability criterion, bilinear transformation	4
	Routh-Hurwitz criterion on s-planes,digital PID controllers, Introductionto adaptive control.	4

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	I. J. Nagrath and M. Gopal: Control Systems Engineering, 3rd Ed, New Age Publication.	2008
2	S. K. Bhattacharya: Control Systems Engineering, 3e, Pearson Publishers.	2009

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	Dhannesh N. Manik: Control System, Cengage Learning.	2010
2	Richard C. Dorf, Robert H. Bishop: Modern Control Systems, Prentice-Hall.	2008
3	M. Gopal: Digital Control and State Variable Methods, MGH.	2012
4	B. C. Kuo: Digital Control System, Oxford.	1980
5	C. H. Houpis and G. B. Lamont, Digital Control Systems, MGH.	1992
6	Donald E. Kiv: Optimal Control Theory- An Introduction, Prentice Hall.	2009
7	D. Roy, Choudhary: Modern Control Engineering, Prentice Hall of India.	2005
8	C. T. Chen: System Theory &Design, Oxford University Press.	1999

6EE2A HIGH VOLTAGE ENGINEERING

B.Tech. (Electrical) 6th semester **Max. Marks: 80**
3L **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**(i) Breakdown in Gases:** Introduction to mechanism of breakdown in gases, Townsend's breakdown mechanism. Breakdown in electromagnetic gases, Application of gases in power system. **(ii) Breakdown in Liquids:** Introduction to mechanism of breakdown in liquids, suspended solid particle mechanism and cavity breakdown. Application of oil in power apparatus.	4
	(iii) Breakdown in solids: Introduction to mechanism of breakdown in solids, electromechanical breakdown, treeing & tracking breakdown and thermal breakdown	4
2	**(i) High DC Voltage Generation:** Generation of high dc voltage, basic voltage multiplier circuit. **(ii) High AC Voltage Generation:** Cascaded Transformers.	5
	(iii) Impulse Voltage generation: Impulse voltage, basic impulse circuit, Mark's multistage impulse generator. **(iv) Measurement of High Voltage:** Potential dividers - resistive, capacitive and mixed potential dividers. Sphere gap- Construction and operation. Klydonorgraph.	3
3	**Nondestructive Insulation Tests:** (i) Measurement of resistively, dielectric constant and loss factor. High Voltage Schering Bridge- measurement of capacitance and dielectric loss.	3
	(ii) Partial Discharges: Introduction to partial discharge, partial discharge equivalent circuit. Basic wide-band and narrow band PD detection circuits.	5
4	**(i) Over voltages:** Causes of over voltages, introduction to lightning phenomena, over voltages due to lighting.	2
	(ii) Travelling Waves: Travelling waves on transmission lines-open end line, short circuited line, line terminated through a resistance, line connected to a cable, reflection and refraction at a T-junction and line terminated through a capacitance. Attenuation of traveling waves.	6
5	**(i) Over Voltage Protection:** Basic construction and operation of ground wires-protection angle and protective zone, ground rods, counterpoise, surge absorber, rod gap and arcing horn, lighting arresters - expulsion type, non -linear gap type and metal oxide gapless type.	5
	(ii) Insulation Coordination: Volt-time curves, basic impulse insulation levels, coordination of insulation levels	3

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	Naidu: High Voltage Engineering 4/e, MGH.	2013
2	John Kuffel, E. Kuffel and W. S. Zaengl: High Voltage engineering, Elsevier.	2000

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	C. L.Wadhwa: High Voltage Engineering, Wiley Eastern Ltd.	2007
2	Subir Ray: An Introduction to High Voltage Engineering, Prentice Hall of India.	2013

6EE3A: SWITCHGEAR &PROTECTION (Common for EE and EX)

B.Tech. (Electrical) 6th semester **Max. Marks: 80**
3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**Static Relays:**Introduction to static relays, merits and demerits. **Comparators:**amplitude and phase comparators, duality between amplitude and phase comparators. Introduction to (a) amplitude comparators-circulating current type, phase splitting type and sampling type, (b) phase comparators-vector product type and coincidence type.	6
	Static Over Current Relays: Introduction to instantaneous, definite time, inverse time and directional overcurrent relays.	2
2	**Static Differential Relays:** Brief description of static differential relay schemes-single phase and three phase schemes. Introduction to static differential protection of generator and transformer.	5
	Static Distance Relays: Introduction to static impedance, reactance and mho relays.	3
3	**Carrier Current Protection:** Basic apparatus and scheme of power line carrier system. Principle of operation of directional comparison and phase comparison carrier protection and carrier assisted distance protection.	4
	Distance Protection: Effect of power swings on the performance of distance protection. Out of step tripping and blocking relays, mho relay with blinders. Introduction to quadrilateral and elliptical relays.	4
4	**Circuit Breakers-I:**Electric arc and its characteristics, arc interruption-high resistance interruption and current zero interruption. Arc interruption theories–recovery rate theory and energy balance theory.	4
	Restriking voltage and recovery voltage, develop expressions for restriking voltage and RRRV. Resistance switching, current chopping and interruption of capacitive current. Oil circuit breakers-bulk oil and minimum oil circuit breakers. Air circuit breakers. Miniature Circuit breaker (MCB).	4
5	**Circuit Breakers-II**: Air blast, SF6 and vacuum circuit breakers. Selection of circuit breakers, rating of circuit breakers.	4
	Digital Protection: Introduction to digital protection. Brief description of block diagram of digital relay. Introduction to digital overcurrent, transformer differential and transmission line distance protection.	4

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	Bhavesh Bhalja, R. P. Maheshari and Nilesh G. Chothani: Protection and Switchgear, Oxford.	2011
2	Bhuvanesh A. Oza and Nair: Power System Protection and Switchgear, MGH.	2010

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	B. Ravindranath and M. Chander: Power system Protection and Switchgear, Wiley.	1977
2	B. Ram and D. N. Vishwakarma: Power System Protection and Switchgear, MGH.	2001
3	Y. G. Paithankar and S. R. Bhide: Fundamentals of Power System Protection, PHI.	2010
4	T.S.M. Rao: Power System Protection- Static Relays with Microprocessor Applications, MGH.	1989
5	A. R. Van C. Warringtaon: Protective Relays-Their Theory and Practice, Vol. I & II, Jhon Willey & Sons.	1978
6	S. S. Rao: Switchgear and Protection, Khanna Publishers.	2008

6EE4A: ADVANCED POWER ELECTRONICS (Common for EE and EX)

B.Tech. (Electrical) 6th semester **Max. Marks: 80**
3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**AC Voltage Controllers**: Principle of On-Off Control, Principle of Phase control, SinglePhase Bi-directional Controllers with Resistive Loads, Single Phase Controllers with Inductive Loads, Three Phase full wave AC controllers, AC Voltage Controller with PWM Control.	5
2	**Cyclo-converters:** Basic principle of operation, single phase to single phase, three-phase to three-phase and three-phase to single phase cyclo-converters. Output equation, Control circuit.	8
3	**Inverters:**Principle of Operation, Single-phase bridge inverters. Three phase bridge Inverters: 180 and 120 degree of conduction. VSI and CSI.	6
	Voltage control of Single Phase and Three Phase Inverters, Harmonic analysis,harmonic reduction techniques,Pulse width modulation techniques.	5
4	**Resonant Pulse Inverter:**Series resonant inverter with unidirectional switches, parallel resonant inverter, class E resonant inverter, L-type and M-type ZCS resonant converter, ZVS resonant converter.	8
5	**Power Supplies:** Switched Mode DC Power Supplies, fly-back converter, forward converter, half and full bridge converter, resonant DC power supplies, bi-directional power supplies.	4
	Resonant AC power supplies, bidirectional AC power supplies. Multistage conversions, Control Circuits: Voltage Mode Control, Current Mode Control	4

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	M. H. Rashid: Power Electronics: Circuits, Devices & Applications, Pearson Publishers.	2004
2	Bimal Bose: Power Electronics & Motor Drives, Elsevier-2006.	2010

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	V. R. Moorthy: Power Electronics: Devices, Circuits and Industrial Applications, Oxford.	2005
2	P. C. Sen: Power Electronics, MGH.	1987
3	Ned Mohan, T. M. Undeland and W. P. Robbins: Power Electronics- Converters, Applications and Design, Wiley India Ltd, 2008.	2007
4	R. Krishnan: electric motor drives- modeling, analysis and control, Pearson Edu.	2001

6EE5A: SMART GRID TECHNOLOGY

B.Tech. (Electrical) 6th semester **Max. Marks: 80**
3L **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**Introduction to Smart Grid:** Evolution of Electric Grid, Concept, Definitions and Need for Smart Grid, Smart grid drivers, functions, opportunities, challenges and benefits	5
	Difference between conventional & Smart Grid, Concept of Resilient &Self-Healing Grid, Present development & International policies in Smart Grid, Diverse perspectives from experts and global Smart Grid initiatives.	3
2	**Smart Grid Technologies:** Technology Drivers, Smart energy resources, Smart substations, Substation Automation, Feeder Automation ,Transmission systems: EMS, FACTS and HVDC, Wide area monitoring,	5
	Protection and Control, Distribution Systems: DMS, Volt/Var control, Fault Detection, Isolation and service restoration, Outage management, High-Efficiency Distribution Transformers, Phase Shifting Transformers, Plug in Hybrid Electric Vehicles (PHEV).	3
3	**Smart Meters and Advanced Metering Infrastructure:** Introduction to Smart Meters, Advanced Metering infrastructure (AMI) drivers and benefits,AMIprotocols, standards and initiatives,	4
	AMI needs in the smart grid, Phasor Measurement, Unit (PMU), Intelligent Electronic Devices (IED) & their application for monitoring & protection.	4
4	**Power Quality Management in Smart Grid:** Power Quality & EMC in Smart Grid, Power Quality issues of Grid connected RenewableEnergy Sources,	5
	Power Quality Conditioners for Smart Grid, Web based Power Qualitymonitoring, Power Quality Audit.	3
5	**High Performance Computing for Smart Grid Applications:** Local Area Network (LAN), House Area Network (HAN), Wide Area Network (WAN), Broadbandover Power line (BPL),	4
	IP based Protocols, Basics of Web Service and CLOUD Computing tomake Smart Grids smarter, Cyber Security for Smart Grid	4

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	Vehbi C. Güngör, Dilan Sahin, Taskin Kocak, Salih Ergüt, Concettina Buccella, Carlo Cecati, and Gerhard P. Hancke: Smart Grid Technologies- Communication Technologies and Standards IEEE Transactions on Industrial Informatics, Vol. 7, No. 4, November 2011.	2011
2	Xi Fang, Satyajayant Misra, Guoliang Xue, and Dejun Yang: Smart Grid – The New and Improved Power Grid- A Survey, IEEE Transaction on Smart Grids,	2011

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	Stuart Borlase: Smart Grid-Infrastructure, Technology and Solutions, CRC Press	2012

6EE6.1A: ADVANCED MICROPROCESSORS

B.Tech. (Electrical) 6th semester **Max. Marks: 80**
3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**8086 Microprocessor:** Hardware specifications, architecture, address spaces, clock generator, bus controller and arbiter, Minimum and maximum mode, System Bus Timing.	8
2	**Software & Instruction Set:** Assembly language programming: addressing mode and instructions of 8086, linking and execution of programs, MACRO programming, assembler directives and operators.	8
3	**I/O Interfaces:** Programmable peripheral interfacing (8255, 8155), Programmable Timer interfacing (8253, 8254), Programmable interrupt controller (8259), Serial CommunicationInterfaces.	8
4	**Data & Memory Interfacing:** A/D, D/A converter interfacing, Memory interfacing and Decoding, DMA controller.	8
5	**Multiprocessor Configurations:** 8086 based Multiprocessor systems. 8087 Numeric data processor. Introduction to 8-bit and 16-bit microcontrollers.	8

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	A. Nagoor Kani: Microprocessors and Microcontrollers, 2nd ed., MGH.	2012
2	N. Senthil Kumar and Saravanan: Microprocessors and Interfacing, Oxford.	2012

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	John Freer: System design with Advance Microprocessors, A.H. Wheeler	1987
2	Ray & Bhurchandi: Advanced Microprocessors and Peripherals 2/e (MGH)	2006
3	Lyla B. Das: The X 86 Microprocessors- Architecture, Programming and Interfacing (8086 to Pentium), Pearson Publisher.	2010
4	Gibson: 16-Bit Microprocessor.	
5	Brey: 16-Bit Microprocessor	2009
6	Ray, A. K. & Burchandi, K. M.: Advanced Microprocessors and Peripherals-Architecture, Programming and Interfacing, MGH.	2006
7	Brey, Barry B.: The INTEL Microprocessors, Pearson Education.	2011
8	Ayala: The 8051 Micro Controller, Cengage Learning.	2004

6EE6.2A: POWER SYSTEM INSTRUMENTATION(Common for EE and EX)

B.Tech. (Electrical) 6th semester **Max. Marks: 80**

3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**Theory of Errors:** Accuracy and precision, systematic and random errors, limits of error, probable error and standard deviation. Gaussian error curves, combination of errors.	8
2	**Transducers:** Construction & Operating Characteristics of active and digital transducers, Measurement of temperature, pressure, displacement, acceleration, noise level.	4
	Instrumentation for strain, displacement, velocity, acceleration, force, torque and temperature.	4
3	**Signal Conditioning:** Instrumentation amplifiers, isolation amplifiers, analog multipliers, analog dividers, function generators, timers, sample and hold, optical and magnetic isolators.	5
	Frequency to voltage converters, temperature to current converters. Shielding and grounding.	3
4	**Power System Instrumentation-I:**Measurement of voltage, current, phase angle, frequency, active power and reactive power in power plants. Energy meters and multipart tariff meters. Basic idea of LT & HT panel's.	8
5	**Power System Instrumentation-II:**Capacitive voltage transformers and their transient behavior, Current Transformers for measurement and protection,composite errors and transient response.	8

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	R. H. Cerni and L. E. Foster: Instrumentation for Engineering Measurements, John Wiley and Sons.	1962
2	Curtis and D. Hohnson: Process Control Instrumentation Technology, John Wiley and sons.	2013

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	R. Morrison: Instrumentation Fundamentals and Applications, John Wiley and Sons.	1984
2	A. K. Sawhney: Advanced Measurements & Instrumentation, Dhanpat Rai & Sons.	1994
3	E.O. Decblin: Measurement System– Application & design, MGH.	1975
4	W.D. Cooper and A.P. Beltried: Electronics Instrumentation and Measurement Techniques, Prentice Hall International.	1987
5	A. S. Moris: Principles of Measurement & Instrumentation, Prentice Hall	1993

6EE6.3A: DIGITAL COMMUNICATION AND INFORMATION THEORY(Common for EE and EX)

B.Tech. (Electrical) 6th semester **Max. Marks: 80**

3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**PCM & Delta Modulation Systems:** PCM and delta modulation, quantization noise in PCM and delta modulation. Signal-to-noise ratio in PCM and delta modulation,	4
	T1 Carrier System, Comparison of PCM and DM. Adaptive delta Modulation. Bit, word and frame synchronization, Matched filter detection.	4
2	**Digital Modulation Techniques:** Various techniques of phase shift, amplitude shift and frequency shift keying. Minimum shift keying. Modulation & Demodulation.	8
3	**Error Probability in Digital Modulation:** Calculation of error probabilities for PSK, ASK, FSK & MSK techniques.	8
4	**Information Theory:**Amount of Information, Average Information, Entropy, Information rate, Increase in Average information per bit by coding, Shannon's Theorem and Shannon's bound	4
	Capacity of a Gaussian Channel, BW-S/N trade off, Orthogonal signal transmission.	4
5	**Coding:** Coding of Information, Hamming code, Single Parity-Bit Code, Linear Block code, cyclic code &convolution code.	8

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	R. N. Mutagi: Digital Communication, 2nd ed., Oxford.	2013
2	P. Ramakrishna Rao: Communication Systems, MGH.	2013

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	H. Taub & D.L. Schilling: Principles of Communication Systems, MGH.	2008
2	Simon Haykin: Communication Systems, John Wiley & Sons.	2008
3	Proakis: Digital Communication, MGH.	2008
4	Sklar: Digital Communication, Pearson Education.	2009
5	P. Chakrabarti: Principles of Digital Communications, Danpatrai & Sons.	1999
6	K. Sam Shanmugam: Digital and Analog Communication System, John Wiley Sons.	2006
7	Lathi, B. P.: Modern Digital &Analog Communication System, Oxford Press.	2009
8	A.B. Carlson: Digital Communication Systems, MGH.	1988

6EE7A: CONTROL SYSTEM LAB (Common for EE and EX)

1 Introduction to MATLAB Computing Control Software.
2 Defining Systems in TF, ZPK form.
3 (a) Plot step response of a given TF and system in state-space. Take different values of damping ratio and w_n natural undamped frequency.
(b) Plot ramp response.
4 For a given 2^{nd}order system plot step response and obtain time response specification.
5 To design 1st order R-C circuits and observe its response with the following inputs and trace the curve.
(a) Step
(b) Ramp
(c) Impulse
6 To design 2nd order electrical network and study its transient response for step input and following cases.
(a) Under damped system
(b) Over damped System.
(c) Critically damped system.
7 To Study the frequency response of following compensating Networks, plot the graph and final out corner frequencies.
(a) Log Network
(b) Lead Network
(c) Log-lead Network.
8 To draw characteristics of ac servomotor
9 To perform experiment on Potentiometer error detector.
10 Check for the stability of a given closed loop system.
11 Plot bode plot for a 2^{nd}order system and find GM and PM.

6EE8A: POWER SYSTEM LAB (Common for EE and EX)

1 Study the burden effect on the performance of CT and measure ratio error.
2 Find out the sequence components of currents in three 1-Phase transformers and 3-Phase transformer and compare their results.
3 (i) Study over current relay.
(ii) Draw the current-time characteristic of an over current relay for TMS=1 & 0.5 and PSM=1.25 & 1.0.
4 (i) Study percentage bias differential relay.
(ii) Plot the characteristics of a percentage bias differential relay for 20%, 30% and 40% biasing.
5 Study gas actuated Buchholz relay.
6 Study under frequency relay and check it's setting experimentally.
7 Design a HV transmission line.
8 Study a typical grid substation.
9 Study earthing of power station, substation and building

6EE9A:ADVANCED POWER ELECTRONICS LAB (Common for EE and EX)

1 Study and test AC voltage regulators using triac, antiparallel thyristors and triac&diac.
2 Study and test single phase PWM inverter.
3 Study and test buck, boost and buck- boost regulators.
4 Study and test MOSFET chopper.
5 Study and test Zero voltage switching.
6 Study and test SCR DC circuit breaker.
7 Control speed of a dc motor using a chopper and plot armature voltage versus speed characteristic.
8 Control speed of a single-phase induction motor using single phase AC voltage regulator.
9 (i) Study single-phase dual converter.
(ii) Study speed control of dc motor using single-phase dual converter.
10 Study one, two and four quadrant choppers (DC-DC converters).
11 Study speed control of dc motor using one, two and four quadrant choppers.
12 Study single-phase cycloconverter.

6EE10A: SMART GRID LAB

1. Study different components of smart grid
2. To visit thermal/nuclear power plant
3. To design and simulate hybrid wind-solar power generation system using simulating software
4. Study Different terminology used in power quality assessment
5. Study and measure certain parameters of power quality in laboratory with and without power quality improvement devices.

6EE11A: ENTREPRENEURSHIPDEVELOPMENT (Common for EE and EX)

1 Definition of entrepreneur, qualities of a successful entrepreneur, Charms of being an entrepreneur, achievement- motivation, leadership and entrepreneurial competencies.
2 Decision-making, procedures and formalities for starting own business, financial support system.
3 Identification and selection of business opportunities and market survey, business plan. Implementation and customer satisfaction.
4 Business crises, problem-solving attitude, communication skill. Government policies for entrepreneurs.
5 Knowledge based enterprises, Scope of entrepreneur in present context, area of future entrepreneurship.
6 Marketing & Sales Promotion, Techno-Economic Feasibility Assessment by Preparation of Preliminary & Detailed project report.

Syllabus B.Tech. (Electrical Engineering), 7th semester

7EE1A: POWER SYSTEM PLANNING (Common for EE and EX)

B. Tech. (Electrical) 7th Semester **Max. Marks: 80**
3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	Introduction of power planning, National and Regional Planning, structure of P.S., planning tools	4
	Electricity Regulation, Electrical Forecasting, forecasting techniques modeling.	4
2	Generation planning, Integrated power generation cogeneration/captive power, Power pooling and power trading. Transmission and distribution planning.	4
	Power system Economics. Power sector finance, financial planning, private participation Rural Electrification investment, concept of Rational tariffs.	4
3	Power supply Reliability, Reliability planning. System operation planning, load management, load prediction, reactive power balance	4
	Online power flow studies, state estimation, computerized management, power system simulator.	4
4	Computer aided planning, wheeling. Environmental effects, the greenhouse effect	4
	Technological impacts. Insulation coordination. Reactive compensation.	4
5	Optimal power system expansion planning : Formulation of least cost optimization problem incorporating the capital,	3
	Operating and maintenance cost of candidate plants of different types (Thermal, Hydro, Nuclear, Non-conventional etc.) and minimum assured reliability constraint – optimization techniques for solution by programming.	5

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	X. Wang, J. R. Mc Donald: Modern Power System Planning, MGH.	1994
2	A. S. Pabla: Electrical Power System Planning, Machmillan India Ltd.	2012

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	M. Tllic, F. Faliana and L. Fink: Power System Restructuring Engineering and Economics, Kulwar Academic Publisher.	2010
2	L. L. Lie: Power System Restructuring and Deregulation, John Willey & Sons UK.	2001

7EE2A: POWER SYSTEM ANALYSIS (Common for EE and EX)

B. Tech. (Electrical) 7th Semester **Max. Marks: 80**

3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	Percent and per unit quantities. Single line diagram for a balanced 3-phase system	4
	Admittance Model: Branch and node admittances Equivalent admittance network and calculation of Y bus. Modification of an existing Y bus.	4
2	**Impendence Model:** Bus admittance and impedance matrices. Thevenin's theorem and Z bus. Direct determination of Z bus. Modification of an existing bus.	3
	Symmetrical fault Analysis: Transient on a Transmission line, short circuit of a synchronous machine on no load, short circuit of a loaded synchronous machine. Equivalent circuits of synchronous machine under sub transient, transient and steady state conditions. Selection of circuit breakers, Algorithm for short circuit studies. Analysis of three-phase faults.	5
3	**Symmetrical Components:** Fortescue theorem, symmetrical component transformation. Phase shift in star-delta transformers. Sequence Impedances of transmission lines, Synchronous Machine and Transformers, zero sequence network of transformers and transmission lines. Construction of sequence networks of power system.	4
	Fault Analysis: Analysis of single line to ground faults using symmetrical components, connection of sequence networks under the fault condition.	4
4	**Unsymmetrical Fault Analysis:** (i) Analysis of line-to-line and double line to ground faults using symmetrical components, connection of sequence networks under fault conditions.	5
	Analysis of unsymmetrical shunt faults using bus impedance matrix method.	3
5	**Load Flow Analysis:**Load flow problem, development of load flow equations, bus classification	4
	Gauss Seidel, Newton Raphosn, decoupled and fast decoupled methods for load flow analysis. Comparison of load flow methods.	4

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	J. J. Grainger, William, D. Stevenson Jr.: Power System Analysis, MGH.	2003
2	T. K. Nagsarkar & M. S. Sukhija: Power System Analysis, Oxford University Press.	2007

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	J. D. Glover, M. S. Sharma & T. J. Overbye: Power System Analysis and Design, Cengage Learning.	2007
2	Nasser Tleis: Power System Modelling and Fault Analysis, Elsevier.	2007
3	Kothari & Nagrath: Modern Power System Analysis, MGH.	2011
4	Haadi Saadat: Power System Analysis.	2002

7EE3A: ARTIFICIAL INTELLIGENCE TECHNIQUES (Common for EE and EX)

B. Tech. (Electrical) 7th Semester **Max. Marks: 80**
3L **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**Artificial Intelligence:** Introduction to AI and knowledge based Expert systems,	4
	Introduction, Importance and Definition of AI, ES, ES building tools and shells.	4
2	**Knowledge Representation:** Concept of knowledge, Representation of knowledge using logics rules, frames. Procedural versus. Declarative knowledge, forward versus backward chaining	4
	Control Strategies: Concept of heuristic search, search techniques depth first search, Breath first search, Generate & test hill climbing, best first search.	4
3	**Artificial Neural Network:** Biological Neurons and synapses, characteristics Artificial Neural Networks, types of activation functions.	4
	Perceptions: Perception representation, limitations of perceptrons. Single layer and multiplayer perceptrons. Perceptron learning algorithms.	4
4	**Basic Concepts in Learning ANN:** Supervised learning, Back propagation algorithm, unsupervised learning, Kohonen's top field network & Algorithm.	8
5	**Fuzzy Logic:** Fuzzy logic concepts, Fuzzy relation and membership functions, Defuzzification, Fuzzy controllers,	4
	Genetic Algorithm: concepts, coding, reproduction, crossover, mutation, scaling and fitness.	4

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	Saroj Kaushik: Artificial Intelligence, Cengage Learning.	2007
2	Elaine Rich and Kevin Knight: Artificial Intelligence 3/e, MGH	2004

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	Padhy: Artificial Intelligence & Intelligent Systems, Oxford	2005
2	James Anderson: An introduction to Neural Networks.	1995
3	Dan. W Patterson: Artificial Intelligence and Expert Systems.	1990
4	Kumar Satish: Neural Networks, 2nd ed., MGH.	2004
5	S. Rajsekaran & G. A. Vijayalakshmi Pai: Neural Networks, Fuzzy Logic and Genetic Algorithm- Synthesis and Applications, Prentice Hall of India.	2003
6	Siman Haykin: Neural Netowrks, Prentice Hall of India.	2004

7EE4A: NON CONVENTIONAL ENERGY SOURCES (Common for EE and EX)

B. Tech. (Electrical) 7th Semester **Max. Marks: 80**
3L **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**Introduction:** World energy situation, conventional and non-conventional energy sources, Indian energy scene.	4
	Tidal Energy: Introduction to tidal power. Components of tidal power plants, double basin arrangement. Power generation. Advantages and limitations of tidal power generation. Prospects of tidal energy in India.	4
2	**Solar Energy:** Solar radiation, solar radiation geometry, solar radiation on tilted surface. Solar energy collector. Flat- plate collector, concentrating collector - parabolidal and heliostat.	4
	Solar pond. Basic solar power plant. Solar cell, solar cell array, basic photo-voltaic power generating system.	4
3	**Wind Energy:** Basic principle of wind energy conversion, efficiency of conversion, site selection. Electric power generation-basic components, horizontal axis and vertical axis wind turbines, towers, generators, control and monitoring components. Basic electric generation schemes- constant speed constant frequency, variable speed constant frequency and variable speed variable frequency schemes. Applications of wind energy.	4
	Geothermal Energy: Geothermal fields, estimates of geothermal power. Basic geothermal steam power plant, binary fluid geothermal power plant and geothermal preheat hybrid power plant. Advantages and disadvantages of geothermal energy. Applications of geothermal energy. Geothermal energy in India.	4
4	**Nuclear Fusion Energy:** Introduction, nuclear fission and nuclear fusion. Requirements for nuclear fusion. Plasma confinement – magnetic confinement and inertial confinement.	4
	Basic Tokamak reactor, laser fusion reactor. Advantages of nuclear fusion. Fusion hybridand cold fusion.	4
5	**Biomass Energy:** Introduction, biomass categories, bio-fuels. Introduction to biomass conversion technologies.	3
	Biogas generation, basic biogas plants-fixed dome type, floating gasholder type, Deen Bandhu biogas plant, Pragati design biogas plant. Utilization of bio gas. Energy plantation. Pyrolysis scheme. Alternative liquid fuels –ethanol and methanol. Ethanol production.	5

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	G. D. Rao: Renewable Energy	2010
2	B. H. Khan: Non-Conventional Energy Resources, MGH.	2006

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	A. N. Mathur: Non-Conventional Resources of Energy.	2010
2	Boyle: Renewable Energy, 3rded Oxford.	2007
3	Bent Sorensen, 4th ed.: Renewable Energy, Elsevier.	2009
4	V. V. N. Kishore: Renewable Energy Engineering and Technology, TERI.	2006
5	Garg & Prakash: Solar Energy : Fundamentals and Applications, MGH	2000
6	David Boyles: Bio Energy, Elis Horwood Ltd.,	1984

7	N.K. Bansal, M. Kleemann, M. Heliss: Renewable energy sources and conversion technology, MGH, 1990.	1990

7EE5A: POWER SYSTEM ENGINEERING (Common for EE and EX)

B. Tech.(Electrical) 7th Semester **Max. Marks: 80**

3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**Economic Operation of Power Systems:** Introduction, system constraints, optimal operation of power systems. Input output, heat rate and incremental rate curves of thermal generating units.	3
	Economic distribution of load between generating units within a plant. Economic distribution of load between power stations, transmission loss equation. Introduction to unit commitment and dynamic programming.	5
2	**Power System Stability-I:** Power angle equations and power angle curves under steady state and transient conditions. Rotor dynamics and swing equation (solution of swing equation not included).	5
	Synchronizing power coefficient. Introduction to steady state and dynamic stabilities, steady state stability limit.	3
3	**Power System Stability-II:** Introduction to transient stability. Equal area criterion and its application to transient stability studies under basic disturbances.	4
	Critical clearing angle and critical clearing time. Factors affecting stability and methods to improve stability.	4
4	**Excitation Systems:** Introduction of excitation systems of synchronous machines, types of excitation systems, Elements of various excitation systems and their control (functional block diagrams and their brief description)-DC excitation systems, AC excitation systems, brushless excitation system.	4
	Interconnected Power Systems: Introduction to isolated and interconnected powers systems. Reserve capacity of power stations, spinning and maintenance resaves. Advantages and problems of interconnected power systems. Power systems inter connection in India.	4
5	Tap Changing transformer, phase angle control and phase shifting transformer. Series compensation of transmission lines, location and protection of series capacitors, advantages and problems	5
	Introduction to power system security. Introduction to voltage stability.	3

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	J. Nagrath and D.P. Kothari: Power System Engineering 2/e, MGH.	2011
2	J. J. Grainger and W. D. Stevenson: Power System Analysis, MGH.	2003

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	B. R. Gupta: Power System Analysis and Design, Third Edition, S. Chand & Co.	2008
2	C. L. Wadhwa: Electrical Power Systems, New age international Ltd. Third Edition	2009
3	W. D. Stevenson: Element of Power System Analysis, MGH.	1955
4	B. R. Gupta: Generation of Electrical Energy, S. Chand Publication.	2009

7EE6.1A: ELECTROMAGNETIC FIELD THEORY

B. Tech. (Electrical) 7th Semester **Max. Marks: 80**
3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**Introduction:** Vector Relation in rectangular, cylindrical, spherical and general curvilinear coordinate system.	4
	Concept and physical interpretation of gradient, Divergence and curl, Green's Stoke's and Helmholz theorems	4
2	**Electrostatics:** Electric field vectors-electric field intensity, flux density & polarization. Electric field due to various charge configurations. The potential functions and displacement vector.	4
	Gauss's law, Poisson's and Laplace's equation and their solution. Uniqueness theorem. Continuity equation. Capacitance and electrostatics energy. Field determination by method of images. Boundary conditions. Field mappings and concept of field cells.	4
3	**Magnetostatics:** Magnetic field vector: Magnetic field intensity, flux density & magnetization, Bio-Savart's law, Ampere's law, Magnetic scalar and vector potential, self & mutual inductance.	4
	Energy stored in magnetic field, Boundary conditions, Analogy between electric and magnetic field, Field mapping and concept of field cells.	4
4	**Time Varying Fields:** Faraday's law, Displacement currents and equation of continuity.	4
	Maxwell's equations, Uniform plane wave in free space, dielectrics and conductors, skin effect sinusoidal time variations, reflections, refraction & polarization of UPW, standing wave ratio. Pointing vector and power considerations.	4
5	**Transmission Lines:** The high-frequency circuit. LCR ladder model. The transmission Lin equation. Solution for loss-less lines.	4
	Wave velocity and wave impedance. Reflection and Transmission coefficients at junctions. VSWR.	4

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	Hayt: Engineering Electromagnetics, 7/e, (With CD), MGH	2012
2	Matthew N. O. Sadiku: Principles of Electromagnetics, 4th ed., Oxford	2009

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	G. S. N. Raju: Electromagnetic Field Theory and Transmission Lines, Pearson.	2006
2	J. D. Kraus: Electromagnetic. 5th edition, MGH.	1999
3	S. Baskaran and K. Malathi: Electromagnetic Field and Waves, Scitech Pub.	2013
4	R. S. Kshetrimayum, Electromagnetic Field Theory, Cengage Learning.	2012
5	V.V. Sarwate: Electromagnetic Field and Waves, Willey Eastern Ltd.	1993
6	Bhag Guru: Electromagnetic Field Theory Fundamentals, Cambridge Uni. Press.	2004

7EE6.2A: COMPUTER AIDED DESIGN OF ELECTRICAL MACHINES (Common for EE and EX)

B. Tech. (Electrical) 7th Semester **Max. Marks: 80**

3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**Basic Principles of Electrical Machine Design:** Specifications, Factors affecting the design, Limitations, main dimension, loadings, output equation, factor affecting the size and rating,	4
	Electrical Engineering Materials: conducting, magnetic and insulating materials. Magnetic Circuit Calculation: Ohm's law for magnetic circuit, mmf required for air gap and iron parts, tapered teeth, real and apparent flux density, magnetizing current.	4
2	**Heating and Cooling of Electrical Machines:** heat dissipation and heat flow equations, Newton's law of cooling, equations for temperature rise,	4
	Rating of Machines: Continuous, short and intermittent ratings, mean temperature rise, hydrogen cooling of turbo alternators, quantity of cooling medium.	4
3	**Computer Aided Design of Transformers:** Power and Distribution Transformers, core and yoke cross sections, square and stepped core, output equations, main dimensions, types & design of windings, optimization concepts.	8
4	**Computer Aided Design of Synchronous Machines:** Turbo and Hydro alternators, choice of specific magnetic & electric loading, short circuit ratio and its effects air gap length, output equation, main dimensions, flow charts for design of synchronous machine, design of stator core & winding.	8
5	**Computer Aided Design of Induction Machines:** Output equation, main dimensions, design criteria, flow charts for design of induction motor, air gap length, design of stator core and winding, rotor design.	8

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	A. K. Sawhney: A Course in Electrical Machine Design, Dhanpat Rai & Sons.	1984
2	B. Edikins: Generalized Theory of Electrical Machines.	1995

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	Fitzegerald: Electrical Machinery, Kingsley.	2002
2	M. G. Say: The Performance and Design of AC Machines, Pitman & Sons.	1958
3	R. K. Agrawal: Electrical Machine Design	2009

7EE6.3A: ECONOMIC OPERATION OF POWER SYSTEMS

B. Tech. (Electrical) 7th Semester **Max. Marks: 80**
3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**Economics of Power Generation:** Introduction, cost of electrical energy, expression for cost of electrical energy, depreciation, power plant cost analysis, economics in plant selection,	4
	selection of types of generation and types of equipments, factors effecting economic generations and distributions, generating cost, economics of different types of generating plants	4
2	**Economical Operations of Thermal Power Plants:** Methods of loading turbo generators, input, output and heat rate characteristics, incremental cost, two generations units,	4
	large no of units, sequence of adding units, effects of transmission losses, economic scheduling considering transmission losses, coordination equations, penalty factors	4
3	**Hydro Thermal coordination:** Advantages of combined operation, base load peak load operation requirement, combined working of run-off river and steam plant	4
	Reservoirs hydroplants and thermal plants (long term operational aspects), short term hydro thermal coordination, coordination equations, scheduling methods and applications.	4
4	**Parallel Operations of Generators:** Conditions, synchronizingcurrent and power, two alternators in parallel (effect of change in excitation, load sharing, sharing of load currents),	4
	Infinite bus bars, active and reactive power control, synchronizing power, torque, operating limits of alternators, operating characteristics of cylindrical alternator rotor.	4
5	**Economics for Electrical Engineers:** Concepts of physical and financial efficiencies of electrical goods and services, supply and demand, break even and minimum cost analysis, linear and nonlinear break even, min cist analysis	8

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	J. Wood & B. F. Wollenburg: Power Generation, Operation and Control, John Wiley.	2013
2	D. P. Kothari & I. J. Nagrath: Modern Power System Analysis, MGH.	2003

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	O. I. Elgerd: Electric Energy System Theory, MGH.	1983
2	P. Kundur: Power System Stability and Control, MGH.	1994
3	Arthur R. Bergen and Vijay Vittal: Power System Analysis, Second Edition. PHI.	1999
4	C. L. Wadhwa: Electrical Power Systems, Newage International (P) Ltd.	2000
5	C. Gross, Power Systems Analysis, 2nd Edition. John Wiley &Sons.	1986

7EE7A: POWER SYSTEM PLANNNG LAB (Common for EE and EX)

1. Status of National and Regional Planning, for power system
2. Write components of Structure of power system
3. Explain in detail various planning tools.
4. Write short note on Electricity Regulation
5. Modeling of Electrical Forecasting techniques
6. Transmission and distribution planning
7. concept of Rational tariffs
8. Rural Electrification

7EE8A: POWER SYSTEM MODELLING AND SIMULATION LAB (Common for EE and EX)

1. Simulate Swing Equation in Simulink (MATLAB)
2. Modeling of Synchronous Machine.
3. Modeling of Induction Machine.
4. Simulate simple circuits using Circuit Maker.
5. (a) Modeling of Synchronous Machine with PSS (b) Simulation of Synchronous Machine with FACTS device.
6. (a) Modeling of Synchronous Machine with FACTS device (b) Simulation of Synchronous Machine with FACTS devices.
7. FACTS Controller designs with FACT devices for SMIB system.

7EE9A: INDUSTRIAL ECONOMICS &MANAGEMENT(Common for EE and EX)

UNIT	CONTENTS
1	**Money Banking and Trade:** Functions of money, supply & demand for money, money price level & inflation, black money, meaning, magnitude & consequences. Functions of Commercial banks, banking system in India, shortcomings and improvements. Function of RBI, monetary policy-making, objectives and features. Sources of public revenue, principles of taxation, direct and indirect taxes, Theory of international trade, balance of trade and payment, Foreign exchange control, devaluation New economic policy: Liberalization, extending privatization, globalization.
2	**Management Principles:** Management functions, responsibilities of management to society, development of management thought. Nature of planning, decision making, management by objectives, Line and staff authority relationships, decentralization and delegation of authority, span of management.
3	**Production Management:** Production planning and control, inventory control, quality control and Total quality management. ISO standards Related to quality/Environment/safety etc. **Tools of Project Management:** CPM, PERT, project information systems. Marketing functions, management of sales and advertising marketing research.
4	**Human Resource Management:** Function, application of industrial psychology for selection, training and recruitment. Communication process, media channels and barriers to effective communication, theories of motivation, leadership.
5	**Finance and Account Management:** Engineering Economics: Investment decision, present worth, annual worth and rate of return methods. Payback time. Need for good cost accounting system, cost control techniques of financial control, financial statements, financial ratios, breakeven analysis, budgeting and budgetary control.

Syllabus B.Tech. (Electrical Engineering), 8th semester

8EE1A: EHV AC/DC TRANSMISSION (Common for EE and EX)

B. Tech. (Electrical) 8th Semester **Max. Marks: 80**
3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**EHV AC Transmission:** Need of EHV transmission lines, power handling capacity and surge impedance loading. Problems of EHV transmission,	4
	Bundled Conductors: geometric mean radius of bundle, properties of bundle conductors. Electrostatic fields of EHV lines and their effects, corona effects: Corona loss, audio and radio noise.	4
2	**Load Frequency Control:** Introduction to control of active and reactive power flow, turbine speed governing system. Speed governing characteristic of generating unit and load sharing between parallel operating generators	4
	Method of Load Frequency Control: Flat frequency, flat tie line and tie line load bias control. Automatic generation control (description of block diagram only).	4
3	**Voltage Control:** No load receiving end voltage and reactive power generation. Methods of voltage control. Synchronous phase modifier	4
	Shunt capacitors and reactors, saturable reactors, Thyristorised static VAR compensators- TCR, FC-TCR and TSC- TCR.	4
4	**FACTS:** Introduction to FACTS controllers, types of FACTS controllers, Brief description of STATCOM, Thyristor controlled series capacitors and unified power flow controller.	8
5	**HVDC Transmission:** Types of D.C. links, advantages and disadvantages of HVDC transmission. Basic scheme and equipment of converter station. Ground return.	4
	Basic principles of DC link control and basic converter control characteristics. Application of HVDC transmission.	4

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	E. W. Kimbark: Direct Current Transmission, Vol. 1, Wiley Interscience.	1971
2	K. R. Padiyar: HVDC Power Transmission System, Wiley Eastern Ltd.	1990

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	K. R. Padiyar: HVDC Power Transmission Systems. NEW AGE PUB	1992
2	J. Arrillaga: H.V.D.C Transmission, Peter Peregrines.	1983
3	J. Arrillaga HVDC et. al, : Computer Modelling of Electrical Power System. John Wiley.	1993

8EE2A: ELECTRIC DRIVES AND THEIR CONTROL (Common for EE and EX)

B. Tech. (Electrical) 8th Semester **Max. Marks: 80**
3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**Dynamics of Electric Drives:** Fundamental torque equations, speed-torque conventions and multi-quadrant operation,	4
	Nature and classification of load torques, steady state stability, load equalization, close loop configurations of drives.	4
2	**DC Drives:** Speed torque curves, torque and power limitation in armature voltage and field control,	4
	Starting, **Braking:** Regenerative Braking, dynamic braking and plugging. Speed Control-Controlled Rectifier fed DC drives, Chopper Controlled DC drives.	4
3	**Induction Motor Drives-I:** Starting, Braking-Regenerative braking, plugging and dynamic braking.	4
	Speed Control: Stator voltage control, variable frequency control from voltage source, Voltage Source Inverter (VSI) Control.	4
4	**Induction Motor Drives-II:** Variable frequency control from current source, Current Source Inverter (CSI) Control,	4
	Cycloconverter Control, Static rotor resistance control, Slip Power Recovery- Stator Scherbius drive, Static Kramer drive.	4
5	**Synchronous Motor Drive:** Control of Synchronous Motor-Separately Controlled and VSI fed Self-Controlled Synchronous Motor Drives.	4
	Dynamic and Regenerative Braking of Synchronous Motor with VSI. Control of Synchronous Motor Using Current Source Inverter (CSI).	4

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	G. K. Dubey: Fundamentals of Electrical Drives, Narosa Publishing House, New Delhi.	2002
2	B. K. Bose: Power Electronics and Motor Drives, Elsevier.	2010

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	V. Subrahmanyam: Electric Drives- Concepts and Applications, MGH.	2011
2	Theodore Wildi: Electrical Machines, Drives and Power Systems, Pearson	2007
3	S. K. Pillai: A First Course on Electrical Drives, Wiley Eastern limited, India.	1989
4	N. K. De and Prashant K. Sen: Electric Drives, Prentice Hall of India Ltd.	1999

8EE3A: PROTECTION OF POWER SYSTEM (Common for EE and EX)

B. Tech. (Electrical) 8th Semester **Max. Marks: 80**
3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**Causes and consequences of dangerous currents**: Faults, overloads and switching over currents. Introduction to protection, trip circuit of a circuit breaker. Functional characteristics of a relay, zone of protection, primary and backup protection.	4
	CTs &PTs: Current transformer construction, measurement and protective CTs. Type of potential transformers. Steady state ratio and phase angle errors in CTs and PTs. Transient errors in CT and CVT (Capacitive Voltage Transformer).	4
2	**Overcurrent Protection:** HRC fuse and thermal relay. Overcurrent relays – instantaneous, definite time, inverse time and inverse definite minimum time overcurrent relays, time and current gradings.	4
	Induction disc type relay. Directional overcurrent relay, 30^0, 60^0 and 90^0 connections. Earth fault relay. Brief description of overcurrent protective schemes for a feeder, parallel feeders and ring mains.	4
3	**Generator Protection:** Stator protection–differential and percentage differential protection, protection against stator inter-turn faults, stator overheating protection.	4
	Rotor protection-protection against excitation and prime mover failure, field earth fault and unbalanced stator currents (negative sequence current protection).	4
4	**Transformer Protection:** Percentage differential protection, magnetizing inrush current, percentage differential relay with harmonic restraint. Buchholz relay. Differential protection of generator transfer unit.	4
	Busbar Protection: Differential protection of busbars. Highimpedance relay scheme, frame leakage protection	4
5	**Transmission Line Protection:** Introduction to distance protection. Construction, operating principle and characteristics of an electromagnetic impedance relay. Effect of arc resistance.Induction cup type reactance and mho relays. Comparison between impedance, reactance and mho relays. Three stepped distance protection of transmission line.	4
	Induction Motor Protection: Introduction to various faults and abnormal operating conditions, unbalance supply voltage and single phasing. Introduction to protection of induction motors- HRC fuse and overcurrent, percentage differential, earth fault and negative sequence voltage relays	4

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	Badri Ram: Power System Protection and Switchgear, MGH.	2011
2	Ravindra Nath M. Chander: Power System Protection and Switch Gear, John Wiley Eastern.	1977

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	Sunil S. Rao.: Power System Protection and Switch Gear, Khanna Publishers.	1999
2	Oza: Power System Protection and Switchgear, MGH.	2010

3	T. S. Madhava Rao: Power System Protections (Static Relays), MGH.	1989
4	A. R. Van C Warrington: Protective Relays, Chapman and Hall London.	1968
5	S. K. Basu and S. Chaudhary: Power System Protection, Raju Primlan Oxford.	1983

8EE4.1A: UTILIZATION OF ELECTRICAL POWER (Common for EE and EX)

B. Tech. (Electrical) 8th Semester **Max. Marks: 80**
3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	**Electric Heating:** Different methods of electric heating. Principle of high frequency induction and dielectric heating. Construction, operation, performance and applications of arc furnace and induction furnace	4
	Electric Welding: Welding process, welding transformer, Classification of Electric Welding: arc welding, resistance welding, welding of various metals.	4
2	**Illuminations:** Definitions, laws of illuminations, polar curves, luminous efficiency, photometer, incandescent lamps, filament materials,	4
	Halogen lamp, electric discharge lamps, sodium vapour lamp, mercury vapour lamp and fluorescent lamp. Light Calculations: commercial, industrial, street and flood lighting.	4
3	**Electrolytic Process:** Principles and applications of electrolysis, electro-deposition, Manufactures of chemicals, anodizing, electro-polishing , electro-cleaning, electroextraction, electro-refining, electro-stripping (parting) power supplies for electrolytic process.	4
4	**Electric Traction & Means of Supplying Power:** Systems of Electric Traction: DC & AC Systems, Power Supply for Electric Traction System: Comparison and application of different systems. Sub-station equipment and layout, conductor rail & pantograph.	4
5	**Traction Methods:** Types of services, speed time and speed distance curves, estimation of power and energy requirements, Mechanics of train movement.	4
	Co-efficient of adhesion, Adhesive weight, effective weight. Traction Motor Controls: DC and AC traction motors, Series parallel starting. Methods of electric braking of traction motors.	4

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	C. L. Wadhwa: Utilization of Electric Traction Electric Power.	1989
2	H. Partab: Art and Science of Electrical Energy, Dhanpat Rai & Sons.	1975

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	H. Partab:Modern Electric Traction, Dhanpat Rai & Sons	1973

8EE4.2A: FACTS DEVICES & THEIR APPLICATIONS

B. Tech. (Electrical) 8th Semester **Max. Marks: 80**
3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	Problems of AC transmission systems, power flow inparallel paths and meshed system, factors limiting loading capability,	4
	Stability consideration. Power flow control of an ac transmission line. Basic types of facts controllers. Advantages of FACTS technology.	4
2	**Voltage-Sourced Converters:** Basic concept of voltage-sourced converters, single and three phase bridge converters. Introduction to power factor control. Transformer connections for 12-pulse, 24 pulse and 48 pulse operations.	4
	Static Shunt Compensators: Mid-point and end point voltage regulation of transmission line, and stability improvement. Basic operating principle of Static Synchronous Compensators (STATCOM). Comparison between STATCOM and SVC.	4
3	**Static Series Compensators:** Concept of series capacitive compensation, voltage and transient stabilities, power oscillation and sub synchronous oscillation damping.	4
	Introduction to thyristors witched series capacitor (TSSC), thyristor controlled series capacitor (TCSC), and static synchronous series compensator, - operation, characteristics and applications.	4
4	**Static Voltage and Phase Angle Regulators:** Voltage and phase angle regulation. Power flow control and improvement of stability by phase angle regulator.	4
	Introduction to thyristor controlled voltage and phase angle regulators (TCVR and TCPAR) (ii) Introduction to thyristor controlled braking resistor and thyristor controlled voltage limiter.	4
5	**UPFC:** Unified Power Flow Controller (UPFC), basic operating principles, conventional transmission control capabilities. Comparison of UPFC to series compensators and phase angle regulator. Applications of UPFC.	4
	IPFC: Interline Power Flow Controller (IPFC), basic operating principles and characteristics. Applications of IPFC.	4

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	K. R. Padiyar: Flexible AC Transmission Systems	2009
2	N. G. Hingorani, L. Gyugyi: Understanding FACTS: IEEE Press Book.	2000

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	Yong Hua Song, Allan T Johns : Flexible AC Transmission Systems FACTS	1999
2	Xiao Ping Zhang, Christian Rehtanz, Bikash Pal: Flexible AC Transmission Systems.	2006
3	R. Mohan & R. M. Mathur: Thyristor-based FACTS Controllers for Electrical Transmission Systems, John Wiley	2002

8EE4.3A: POWER SYSTEM TRANSIENTS

B. Tech. (Electrical) 8th Semester **Max. Marks: 80**
3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
1	Wave terminology, Development of wave quotations, Terminal problems, Lattice diagrams,	4
	Origin and Nature of power system transients and surges, Surge parameters of plants, Equivalent Circuit representations. Lumped and distributed circuit transients.	4
2	Line energisation and de-energisation transients-Earth and earthwire effects. Current chopping in circuit breakers.	4
	Short line fault condition and its relation to circuit breaker duty. Trapped charge effects. Effect of source and source representation in short line fault studies.	4
3	Control of transients, Lightening phenomenon, influence of tower footing resistance and earth resistance,	4
	Traveling waves in distributed parameters multiconductor lines, parameters as a function of frequency.	4
4	Mechanism of Lightning Discharge Types of Lightning strokes, Harmful effects of lighting, protections against lightning, overhead Ground wires.	8
5	Lightening Arresters, Types of lightening arresters, Surge Absorber simulation of surge diverters in transient analysis. Fourier integral and z transform methods in power system transient	8

Text Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	C. S. Indulkar and D. P. Kothari: Power System Transients, NEW AGE.	2010
2	Lou Van der Sluis: Transients in Power Systems, John Wiley	2001

Reference Books

S. No.	Name of authors'/books/publisher	Year of pub.
1	N. R. Watson, J. Arrillaga: Power Systems Electromagnetic Transients, John Wiley	2001

8EE5A: COMPUTER BASED POWER SYSTEM LAB (Common for EE and EX)

1. Fault analysis (for 3 to 6 bus) and verify the results using MATLAB or any available software for the cases: (i) LG Fault (ii) LLG Fault (iii) LL Fault and (iv) 3-Phase Fault
2. Load flow analysis for a given system (for 3 to 6 bus) using (i) Gauss Seidal (ii) Newton Raphson (iii) Fast Decoupled Method and verify results using MATLAB or any available software
3. Study of voltage security analysis
4. Study of overload security analysis and obtain results for the given problem using MATLAB or any software.
5. Study of economic load dispatch problem with different methods.
6. Study of transient stability analysis using MATLAB/ETAP Software.

8EE6A: ELECTRICAL DRIVES AND CONTROL LAB (Common for EE and EX)

1. Study and test the firing circuit of three phase half controlled bridge converter.
2. Study and obtain waveforms of 3 phase half controlled bridge converter with R and RL loads.
3. Study and test the firing circuit of 3-phase full controlled bridge converter.
4. Study and obtain waveforms of 3-phase full controlled bridge converter with R and RL loads.
5. Study and test 3-phase AC voltage regulator.
6. Control speed of dc motor using 3-phase half controlled bridge converter. Plot armature voltage versus speed characteristic.
7. Control speed of dc motor using 3-phase full controlled bridge converter. Plot armature voltage versus speed characteristic.
8. Control speed of a 3-phase induction motor in variable stator voltage mode using 3-phase AC voltage regulator.
9. Control speed of a 3-phase BLDC motor.
10. Control speed of a 3-phase PMSM motor using frequency and voltage control
11. Control speed of universal motor using AC voltage regulator.
12. Study 3-phase dual converter.
13. Study speed control of dc motor using 3-phase dual converter.
14. Study three-phase cycloconverter and speed control of synchronous motor using cycloconverter.
15. Control of 3-Phase Induction Motor in variable frequency V/f constant mode using 3-phase inverter

8EE7A: HIGH VOLTAGE ENGINEERING LAB

1. Study filtration and Treatment of transformer oil.
2. Determine dielectric strength of transformer oil.
3. Determine capacitance and dielectric loss of an insulating material using Schering bridge.
4. Study solid dielectrics used in power apparatus.
5. Study applications of insulating materials.
6. Study direct testing and indirect testing of circuit breakers.
7. Study high voltage testing of electrical equipment: line insulator, cable, bushing, power capacitor, and power transformer.
8. Design an EHV transmission line.

Scheme and Syllabus

Rajasthan Technical University, Kota

B. Tech. in Information Technology

(Session 2018-19)

Scheme of

UNDERGRADUATE DEGREE COURSE

Information Technology



Rajasthan Technical University, Kota

Effective from session: 2018 – 2019

Teaching & Examination Scheme
B.Tech. : Information Technology
2nd Year - III Semester

SN	Category	Course		Contact hrs/week			Marks				Cr
		Code	Title	L	T	P	Exm Hrs	IA	ETE	Total	
THEORY											
1	BSC	3IT2-01	Advanced Engineering Mathematics	3	0	0	3	30	120	**150**	**3**
2	HSMC	3IT1-02/ 3IT1-03	Technical Communication/ Managerial Economics and Financial Accounting	2	0	0	2	20	80	**100**	**2**
3	ESC	3IT3-04	Digital Electronics	3	0	0	3	30	120	**150**	**3**
4	PCC	3IT4-05	Data Structures and Algorithms	3	0	0	3	30	120	**150**	**3**
5		3IT4-06	Object Oriented Programming	3	0	0	3	30	120	**150**	**3**
6		3IT4-07	Software Engineering	3	0	0	3	30	120	**150**	**3**
			Sub Total	17	0	0		170	680	**850**	**17**
PRACTICAL & SESSIONAL											
8	PCC	3IT4-21	Data Structures and Algorithms Lab	0	0	3		45	30	**75**	**1.5**
9		3IT4-22	Object Oriented Programming Lab	0	0	3		45	30	**75**	**1.5**
10		3IT4-23	Software Engineering Lab	0	0	3		45	30	**75**	**1.5**
11		3IT4-24	Digital Electronics Lab	0	0	3		45	30	**75**	**1.5**
13	PSIT	3IT7-30	Industrial Training	0	0	1		0	0	**50**	**1**
14	SODECA	3IT8-00	Social Outreach, Discipline & Extra Curricular Activities	0	0	0		0	0	**25**	**0.5**
			Sub- Total	0	0	13		180	120	**375**	**7.5**
			TOTAL OF III SEMESTER	17	0	13		350	800	**1225**	**24.5**

L: *Lecture,* ***T:*** *Tutorial,* ***P:*** *Practical,* ***Cr:*** *Credits*

ETE: *End Term Exam,* ***IA:*** *Internal Assessment*

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Teaching & Examination Scheme
B.Tech. : Information Technology
2nd Year - IV Semester

SN	Category	Course		Contact hrs/week			Marks				Cr
		Code	Title	L	T	P	Exm Hrs	IA	ETE	Total	
THEORY											
1	BSC	4IT2-01	Discrete Mathematics Structure	3	0	0	3	30	120	**150**	**3**
2	HSMC	4IT1-03/ 4IT1-02	Managerial Economics and Financial Accounting / Technical Communication	2	0	0	2	20	80	**100**	**2**
3	ESC	4IT3-04	Principle of Communication	3	0	0	3	30	120	**150**	**3**
4	PCC	4IT4-05	Database Management System	3	0	0	3	30	120	**150**	**3**
5		4IT4-06	Theory of Computation	3	0	0	3	30	120	**150**	**3**
6		4IT4-07	Data Communication and Computer Networks	3	0	0	3	30	120	**150**	**3**
			Sub Total	17	0	0		170	680	**850**	**17**
PRACTICAL & SESSIONAL											
8	PCC	4IT4-21	Linux Shell Programming Lab	0	0	2		30	20	**50**	**1**
9		4IT4-22	Database Management System Lab	0	0	3		45	30	**75**	**1.5**
10		4IT4-23	Network Programming Lab	0	0	3		45	30	**75**	**1.5**
11		4IT4-24	Java Lab	0	0	2		30	20	**50**	**1**
12		4IT4-25	Web Technology Lab	0	0	2		30	20	**50**	**1**
13	SODECA	4IT8-00	Social Outreach, Discipline & Extra Curricular Activities							**25**	**0.5**
			Sub- Total	0	0	12		180	120	**325**	**6.5**
			TOTAL OF IV SEMEESTER	17	0	12		350	800	**1175**	**23.5**

L: *Lecture,* ***T:*** *Tutorial,* ***P:*** *Practical,* ***Cr:*** *Credits*

ETE: *End Term Exam,* ***IA:*** *Internal Assessment*

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

V	Information Technology	Teaching Hrs			Exm Hrs	Max. Marks			Comments
Course Code	Subject Name (Revised)	L	T	P		IA	TE	TOTAL	Common to
5IT1A	Computer Architecture	3			3	20	80	100	CS, IT
5IT2A	Digital Signal Processing	3	-		3	20	80	100	
5IT3A	Telecommunication Fundamentals	3	-	-	3	20	80	100	CS, IT
5IT4A	Database Management Systems	3			3	20	80	100	CS, IT
5IT5A	Operating Systems	3	-		3	20	80	100	CS, IT
5IT6.1A	Advanced Data Structure	3	-		3	20	80	100	CS, IT
5IT6.2A	E-Commerce								
5IT6.3A	Satellite & Microwave Communication								
5IT7A	DataBase Lab			3		60	40	100	CS, IT
5IT8A	Advance Communication Lab.	-	-	3		45	30	75	
5IT9A	Operating Systems Simulation Lab			3		60	40	100	CS, IT
5IT10A	Digital Hardware Design Lab			3		45	30	75	CS, IT
5ITDC	Discipline & Extra Curricular Activities							50	CS, IT
		18	0	12				1000	

VI	Information Technology	Teaching Hrs			Exm Hrs	Max. Marks			Comments
Course Code	Subject Name (Revised)	L	T	P		IA	TE	TOTAL	Common to
6IT1A	Computer Networks	3	-		3	20	80	100	CS, IT
6IT2A	Design and Analysis of Algorithms	3	-		3	20	80	100	CS, IT
6IT3A	Theory Of Computation	3			3	20	80	100	CS, IT
6IT4A	Programming in Java	3			3	20	80	100	
6IT5A	Information Theory & Coding	3	-		3	20	80	100	
6IT6.1A	Advance Topics in Operating Systems	3	-		3	20	80	100	CS, IT
6IT6.2A	Bio Informatics								
6IT6.3A	Human Computer Interface								CS, IT
6IT7A	Java Programming Lab			3		45	30	75	CS, IT
6IT8A	GUI Design Lab.			3		45	30	75	
6IT9A	UML Lab			2		30	20	50	
6IT10A	Design and Analysis of Algorithms Lab.			3		60	40	100	CS, IT
6IT11A	Humanities and Social Sciences			2		30	20	50	CS, IT
6ITDC	Discipline & Extra Curricular Activities							50	CS, IT
		18	0	13				1000	

VII	Information Technology	Teaching Hrs			Exm Hrs	Max. Marks			Comments
Course Code	Subject Name (Revised)	L	T	P		IA	TE	TOTAL	Common to
7IT1A	Software Project management	3	-		3	20	80	100	
7IT2A	Information System Security	3			3	20	80	100	CS, IT
7IT3A	Data Mining & Ware Housing	3			3	20	80	100	CS, IT
7IT4A	Internet Programming	3	-		3	20	80	100	
7IT5A	Computer Graphics & Multimedia Techniques	3			3	20	80	100	
7IT6.1A	Advance Database Management Systems	3	-		3	20	80	100	
7IT6.2A	Intelligent Systems								
7IT6.3A	Speech Processing								
7IT7A	Computer Graphics & Multimedia Lab.			2		30	20	50	
7IT8A	DMW Lab			3		60	40	100	
7IT9A	Internet Programming Lab.			2		30	20	50	
7ITPR	Project-I			2		50		50	CS, IT
7ITTR	Practical Training*			2			100	100	CS, IT
7ITDC	Discipline & Extra Curricular Activities							50	CS, IT
		18	0	11				1000	

VIII	Information Technology	Teaching Hrs			Exm Hrs	Max. Marks			Comments
Course Code	Subject Name (Revised)	L	T	P		IA	TE	TOTAL	Common to
8IT1A	Software Testing & Validation	3			3	20	80	100	
8IT2A	Digital Image Processing	3	-		3	20	80	100	CS, IT
8IT3A	Data Compression Techniques	3			3	20	80	100	
8IT4.1A	Mobile Computing	3	-		3	20	80	100	CS, IT
8IT4.2A	Information Retrieval								CS, IT
8IT4.3A	Robotics								
8IT5A	Software Testing Lab.			3		60	40	100	
8IT6A	Digital Image Processing lab			2		30	20	50	CS, IT
8IT7A	Advanced Web Programming			2		30	20	50	
8IT8A	Mobile Application Development Lab			2		30	20	50	
8ITPR	Project-II			2		120	80	200	CS, IT
8ITSM	Seminar			2		60	40	100	CS, IT
8ITDC	Discipline & Extra Curricular Activities							50	CS, IT
		12	0	13				1000	

Syllabus of

UNDERGRADUATE DEGREE COURSE

Information Technology



Rajasthan Technical University, Kota

Effective from session: 2018 – 2019

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

II Year- III Semester: B.Tech. (Information Technology)

3IT2-01: Advanced Engineering Mathematics

Credit- 3 **Max. Marks : 150 (IA:30,ETE:120)**
3L+0T+0P **End Term Exam: 03 Hours**

SN	CONTENTS	Hours
1	**Random Variables:** Discrete and Continuous random variables, Joint distribution, Probability distribution function, conditional distribution. Mathematical Expectations: Moments, Moment Generating Functions, variance and correlation coefficients, Chebyshev's Inequality, Skewness and Kurtosis.	7
2	**Binomial distribution**, Normal Distribution, Poisson Distribution and their relations, Uniform Distribution, Exponential Distribution. Correlation: Karl Pearson's coefficient, Rank correlation. Curve fitting. Line of Regression.	5
3	**Historical development**, Engineering Applications of Optimization, Formulation of Design Problems as a Mathematical Programming Problems, Classification of Optimization Problems	8
4	**Classical Optimization using Differential Calculus:** Single Variable and Multivariable Optimization with & without Constraints, Langrangian theory, Kuhn Tucker conditions	6
5	**Linear Programming:** Simplex method, Two Phase Method and Duality in Linear Programming. Application of Linear Programming: Transportation and Assignment Problems.	14
	TOTAL	40

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

II Year- III Semester: B.Tech. (Information Technology)

3IT1-02/4IT1-02: Technical Communication

Credit- 2 **Max. Marks : 100 (IA:20,ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**Introduction to Technical Communication-** Definition of technical communication, Aspects of technical communication, forms of technical communication, importance of technical communication, technical communication skills (Listening, speaking, writing, reading writing), linguistic ability, style in technical communication.	4
2	**Comprehension of Technical Materials/Texts and Information Design & development-** Reading of technical texts, Readingand comprehending instructions and technical manuals, Interpreting and summarizing technical texts, Note-making. Introduction of different kinds of technical documents, Information collection, factors affecting information and document design, Strategies for organization, Information design and writing for print and online media.	6
3	**Technical Writing, Grammar and Editing**- Technical writing process, forms of technical discourse, Writing, drafts and revising, Basics of grammar, common error in writing and speaking, Study of advanced grammar, Editing strategies to achieve appropriate technical style, Introduction to advanced technical communication. Planning, drafting and writing Official Notes, Letters, E-mail, Resume, Job Application, Minutes of Meetings.	8
4	**Advanced Technical Writing**- Technical Reports, types of technical reports, Characteristics and formats and structure of technical reports. Technical Project Proposals, types of technical proposals, Characteristics and formats and structure of technical proposals. Technical Articles, types of technical articles, Writing strategies, structure and formats of technical articles.	8
	TOTAL	**26**

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

II Year- III Semester: B.Tech. (Information Technology)

3IT1-03/ 4IT1-03: Managerial Economics and Financial Accounting

Credit- 2 **Max. Marks : 100 (IA:20,ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**Basic economic concepts-** Meaning, nature and scope of economics, deductive vs inductive methods, static and dynamics, Economic problems: scarcity and choice, circular flow of economic activity, national income-concepts and measurement.	4
2	**Demand and Supply analysis-** Demand-types of demand, determinants of demand, demand function, elasticity of demand, demand forecasting –purpose, determinants and methods, Supply-determinants of supply, supply function, elasticity of supply.	5
3	**Production and Cost analysis-** Theory of production- production function, law of variable proportions, laws of returns to scale, production optimization, least cost combination of inputs, isoquants. Cost concepts-explicit and implicit cost, fixed and variable cost, opportunity cost, sunk costs, cost function, cost curves, cost and output decisions, cost estimation.	5
4	**Market structure and pricing theory-** Perfect competition, Monopoly, Monopolistic competition, Oligopoly.	4
5	**Financial statement analysis-** Balance sheet and related concepts, profit and loss statement and related concepts, financial ratio analysis, cash-flow analysis, funds-flow analysis, comparative financial statement, analysis and interpretation of financial statements, capital budgeting techniques.	8
	TOTAL	**26**

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

3IT3-04: Digital Electronics

Credit- 3 **Max. Marks : 150 (IA:30,ETE:120)**
3L+0T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	**Fundamental concepts:** Number systems and codes, Basic logic Gates and Boolean algebra: Sign & magnitude representation, Fixed point representation, complement notation, various codes & arithmetic in different codes & their inter conversion. Features of logic algebra, postulates of Boolean algebra.Theorems of Boolean algebra.	8
2	**Minimization Techniques and Logic Gates**: Principle of Duality - Boolean expression -Minimization of Boolean expressions — Minterm – Maxterm - Sum of Products (SOP) – Product of Sums (POS) – Karnaugh map Minimization – Don't care conditions – Quine - McCluskey method of minimization.	8
3	**Digital Logic Gate Characteristics:** TTL logic gate characteristics. Theory & operation of TTL NAND gate circuitry. Open collector TTL. Three state output logic. TTL subfamilies.MOS& CMOS logic families. Realization of logic gates in RTL, DTL, ECL, C-MOS & MOSFET.	8
4	**Combinational Circuits:** Combinational logic circuit design, adder, subtractor, BCD adder, encoder, decoder, BCD to 7-segment decoder, multiplexer, demultiplexer.	8
5	**Sequential Circuits:** Latches, Flip-flops - SR, JK, D, T, and Master-Slave Characteristic table and equation,counters and their design, Synchronous counters – Synchronous Up/Down counters – Programmable counters – State table and state transition diagram ,sequential circuits design methodology. Registers –shift registers.	8
	TOTAL	**40**

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SYLLABUS

II Year- III Semester: B.Tech. (Information Technology)

3IT4-05: Data Structures and Algorithms

Credit- 3 **Max. Marks : 150 (IA:30,ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	Stacks: Basic Stack Operations, Representation of a Stack using Static Array and Dynamic Array, Multiple stack implementation using single array, Stack Applications: Reversing list, Factorial Calculation, Infix to postfix Transformation, Evaluating Arithmetic Expressions and Towers of Hanoi.	8
2	Queues: Basic Queue Operations, Representation of a Queue using array, Implementation of Queue Operations using Stack, Applications of Queues- Round Robin Algorithm. Circular Queues, DeQueue Priority Queues. Linked Lists: Introduction, single linked list, representation of a linked list in memory, Different Operations on a Single linked list, Reversing a single linked list, Advantages and disadvantages of single linked list, circular linked list, double linked list and Header linked list.	10
3	Searching Techniques: Sequential and binary search.Sorting Techniques: Basic concepts, Sorting by: bubble sort, Insertion sort, selection sort, quick sort, heap sort, merge sort, radix sort and counting sorting algorithms.	7
4	Trees: Definition of tree, Properties of tree, Binary Tree, Representation of Binary trees using arrays and linked lists, Operations on a Binary Tree, Binary Tree Traversals (recursive), Binary search tree, B-tree , B+ tree, AVL tree, Threaded binary tree.	7
5	Graphs: Basic concepts, Different representations of Graphs, Graph Traversals (BFS & DFS), Minimum Spanning Tree(Prims &Kruskal), Dijkstra's shortest path algorithms.Hashing: Hash function, Address calculation techniques, Common hashing functions, Collision resolution: Linear and Quadratic probing, Double hashing.	8
	TOTAL	**40**

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SYLLABUS

II Year- III Semester: B.Tech. (Information Technology)

3IT4-06: Object Oriented Programming

Credit- 3 **Max. Marks : 150 (IA:30,ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	Introduction to different programming paradigm, characteristics of OOP, Class, Object, data member, member function, structures in C++, different access specifiers, defining member function inside and outside class, array of objects.	8
2	Concept of reference, dynamic memory allocation using new and delete operators, inline functions, function overloading, function with default arguments, constructors and destructors, friend function and classes, using this pointer.	8
3	Inheritance, types of inheritance, multiple inheritance, virtual base class, function overriding, abstract class and pure virtual function	9
4	Constant data member and member function, static data member and member function, polymorphism, operator overloading, dynamic binding and virtual function	9
5	Exception handling, Template, Stream class, File handling.	6
	TOTAL	40

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3IT4-07: Software Engineering

Credit- 3 **Max. Marks : 150 (IA:30, ETE:120)**
3L+0T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	Introduction, software life-cycle models, software requirements specification, formal requirements specification, verification and validation.	8
2	Software Project Management: Objectives, Resources and their estimation, LOC and FP estimation, effort estimation, COCOMO estimation model, risk analysis, software project scheduling.	8
3	Requirement Analysis: Requirement analysis tasks, Analysis principles. Software prototyping and specification data dictionary, Finite State Machine (FSM) models. Structured Analysis: Data and control flow diagrams, control and process specification behavioural modeling	8
4	Software Design: Design fundamentals, Effective modular design: Data architectural and procedural design, design documentation.	8
5	Object Oriented Analysis: Object oriented Analysis Modeling, Data modeling. Object Oriented Design: OOD concepts, Class and object relationships, object modularization, Introduction to Unified ModelingLanguage .	8
	TOTAL	**40**

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3IT4-21: Data Structures and Algorithms Lab

Credit- 1.5 **Max. Marks: 75 (IA:45,ETE:30)**

0L+0T+3P

SN	CONTENTS
1	Write a simple C program on a 32 bit compiler to understand the concept of array storage, size of a word. The program shall be written illustrating the concept of row major and column major storage. Find the address of element and verify it with the theoretical value. Program may be written for arrays up to 4-dimensions.
2	Simulate a stack, queue, circular queue and dequeue using a one dimensional array as storage element. The program should implement the basic addition, deletion and traversal operations.
3	Represent a 2-variable polynomial using array. Use this representation to implement addition of polynomials
4	Represent a sparse matrix using array. Implement addition and transposition operations using the representation.
5	Implement singly, doubly and circularly connected linked lists illustrating operations like addition at different locations, deletion from specified locations and traversal.
6	Repeat exercises 2, 3 & 4 with linked structure.
7	Implementation of binary tree with operations like addition, deletion, traversal.
8	Depth first and breadth first traversal of graphs represented using adjacency matrix and list.
9	Implementation of binary search in arrays and on linked Binary Search Tree.
10	Implementation of different sorting algorithm like insertion, quick, heap, bubble and many more sorting algorithms.

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SYLLABUS

II Year- III Semester: B.Tech. (Information Technology)

3IT4-22 : Object Oriented Programming Lab

Credit-1.5 **Max. Marks: 75 (IA:45,ETE:30)**

0L+0T+3P

SN	CONTENTS
1	Understand the basics of C++ library, variables, data input-output.
2	C++ program using with the concept of structures.
3	Implement class and object concepts and function overloading.
4	Write programs to understand dynamic memory allocation and array of objects.
5	Program to understand different types of constructors and destructor.
6	Implement friend function to access private data of a class and usage of this pointer.
7	Write programs to understand the usage of constant data member and member function, static data member and member function in a class.
8	Implement different types of inheritance, function overriding and virtual function
9	Implement Operator overloading concepts.
10	Write programs to understand function template and class template.
11	Write programs to understand exception handling techniques.
12	Write programs to understand file handling techniques.

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3IT4-23: Software Engineering Lab

Credit- 1.5 **Max. Marks: 75 (IA:45,ETE:30)**
0L+0T+3P

SN	CONTENTS
1	Development of requirements specification, function oriented design using SA/SD, object-oriented design using UML, test case design, implementation using Java and testing. Use of appropriate CASE tools and other tools such as configuration management tools, program analysis tools in the software life cycle.
2	Develop Software Requirements Specification (SRS) for a given problem in IEEE template.
3	Develop DFD model (level-0, level-1 DFD and Data dictionary) of the project.
4	Develop structured design for the DFD model developed.
5	Developed all Structure UML diagram of the given project.
6	Develop Behavior UML diagram of the given project.
7	Manage file, using ProjectLibre project management software tool.

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SYLLABUS

II Year- III Semester: B.Tech. (Information Technology)

3IT4-24: Digital Electronics Lab

Credit- 1.5 **Max. Marks: 75 (IA:45,ETE:30)**

0L+0T+3P

SN	CONTENTS
1	To verify the truth tables of basic logic gates: AND, OR, NOR, NAND, NOR. Also to verify truth table of Ex-OR, Ex-NOR (For 2, 3, & 4 inputs using gateswith 2, 3, & 4 inputs).
2	To verify the truth table of OR, AND, NOR, Ex-OR, Ex-NOR realized usingNAND& NOR gates.
3	To realize an SOP and POS expression.
4	To realize Half adder/ Subtractor& Full Adder/ Subtractor using NAND & NOR gatesand to verify their truth tables.
5	To realize a 4-bit ripple adder/ Subtractor using basic Half adder/ Subtractor& basic Full Adder/ Subtractor.
6	To verify the truth table of 4-to-1 multiplexer and 1-to-4 demultiplexer. Realize the multiplexer using basic gates only. Also to construct and 8-to-1 multiplexer and 1-to-8 demultiplexer using blocks of 4-to-1 multiplexer and 1-to-4 demultiplexer.
7	Design & Realize a combinational circuit that will accept a 2421 BCD code and drive a TIL -312 seven-segment display.
8	Using basic logic gates, realize the R-S, J-K and D-flip flops with and without clock signal and verify their truth table.
9	Construct a divide by 2,4& 8 asynchronous counter. Construct a 4-bit binary counter and ring counter for a particular output pattern using D flip flop.
10	Perform input/output operations on parallel in/Parallel out and Serial in/Serial out registers using clock. Also exercise loading only one of multiple values into the register using multiplexer. Note: As far as possible, the experiments shall be performed on bread board. However, experiment Nos. 1-4 are to be performed on bread board only.

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Syllabus of

UNDERGRADUATE DEGREE COURSE

Information Technology

Rajasthan Technical University, Kota

Effective from session: 2018 – 2019

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

II Year- IV Semester: B.Tech. (Information Technology)

4IT2-01: Discrete Mathematics Structure

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	Set Theory: Definition of sets, countable and uncountable sets, Set operations, Partition of set, Cardinality (Inclusion-Exclusion & Addition Principles) Venn Diagrams, proofs of some general identities on sets. Relation: Definition, types of relation, composition of relations, Pictorial representation of relation, Equivalence relation, Partial ordering relation, Job-Scheduling problem. Function: Definition, type of functions, one to one, into and onto function, inverse function, composition of functions, recursively defined functions, pigeonhole principle. Theorem proving Techniques: Mathematical induction, Proof by contradiction. Composition of Functions. The Pigeonhole and Generalized Pigeonhole Principles.	7
3	Propositional Logic: Proposition, First order logic, Basic logical operation, truth tables, tautologies, Contradictions, Algebra of Proposition, logical implications, logical equivalence, predicates, Normal Forms, Universal and existential quantifiers. 2 way predicate logic. Introduction to finite state machine Finite state machines as models of physical system equivalence machines, Finite state machines as language recognizers.	8
4	Posets, Hasse Diagram and Lattices: Introduction, ordered set, Hasse diagram of partially, ordered set, isomorphic ordered set, well ordered set, properties of Lattices, bounded and complemented lattices. Combinatorics: Introduction, Permutation and combination, Binomial Theorem, Multimodal Coefficients Recurrence Relation and Generating Function: Introduction to Recurrence Relation and Recursive algorithms, linear recurrence relations with constant coefficients, Homogeneous solutions, Particular solutions, Total solutions, Generating functions, Solution by method of generating functions.	8
5	Algebraic Structures: Definition, Properties, types: Semi Groups, Monoid, Groups, Abelian group, properties of groups, Subgroup, cyclic groups, Cosets, factor group, Permutation groups, Normal subgroup, Homomorphism and isomorphism of Groups, example and standard results, Rings and Fields: definition and standard results.	8
6	Graph Theory: Introduction and basic terminology of graphs, Planer graphs, Multigraphs and weighted graphs, Isomorphic graphs, Paths, Cycles and connectivity, Shortest path in weighted graph, Introduction to Eulerian paths and circuits, Hamiltonian paths and circuits, Graph coloring, chromatic number, Isomorphism and Homomorphism of graphs, matching, vertex/edge covering.	8
	Total	40

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RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

II Year- IV Semester: B.Tech. (Information Technology)

4IT1-03/3IT1-03: Managerial Economics and Financial Accounting

Credit- 2 **Max. Marks : 100 (IA:20,ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Basic economic concepts-** Meaning, nature and scope of economics, deductive vs inductive methods, static and dynamics, Economic problems: scarcity and choice, circular flow of economic activity, national income-concepts and measurement.	3
3	**Demand and Supply analysis-** Demand-types of demand, determinants of demand, demand function, elasticity of demand, demand forecasting –purpose, determinants and methods, Supply-determinants of supply, supply function, elasticity of supply.	5
4	**Production and Cost analysis-** Theory of production- production function, law of variable proportions, laws of returns to scale, production optimization, least cost combination of inputs, isoquants. Cost concepts-explicit and implicit cost, fixed and variable cost, opportunity cost, sunk costs, cost function, cost curves, cost and output decisions, cost estimation.	5
5	**Market structure and pricing theory-** Perfect competition, Monopoly, Monopolistic competition, Oligopoly.	4
6	**Financial statement analysis-** Balance sheet and related concepts, profit and loss statement and related concepts, financial ratio analysis, cash-flow analysis, funds-flow analysis, comparative financial statement, analysis and interpretation of financial statements, capital budgeting techniques.	8
	TOTAL	26

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SYLLABUS

II Year- IV Semester: B.Tech. (Information Technology)

4IT1-02/3IT1-02: Technical Communication

Credit- 2 **Max. Marks : 100 (IA:20,ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	CONTENTS	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Introduction to Technical Communication-** Definition of technical communication, Aspects of technical communication, forms of technical communication, importance of technical communication, technical communication skills (Listening, speaking, writing, reading writing), linguistic ability, style in technical communication.	3
3	**Comprehension of Technical Materials/Texts and Information Design & development-** Reading of technical texts, Readingand comprehending instructions and technical manuals, Interpreting and summarizing technical texts, Note-making. Introduction of different kinds of technical documents, Information collection, factors affecting information and document design, Strategies for organization, Information design and writing for print and online media.	6
4	**Technical Writing, Grammar and Editing**- Technical writing process, forms of technical discourse, Writing, drafts and revising, Basics of grammar, common error in writing and speaking, Study of advanced grammar, Editing strategies to achieve appropriate technical style, Introduction to advanced technical communication. Planning, drafting and writing Official Notes, Letters, E-mail, Resume, Job Application, Minutes of Meetings.	8
5	**Advanced Technical Writing**- Technical Reports, types of technical reports, Characteristics and formats and structure of technical reports. Technical Project Proposals, types of technical proposals, Characteristics and formats and structure of technical proposals. Technical Articles, types of technical articles, Writing strategies, structure and formats of technical articles.	8
	TOTAL	26

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4IT3-04: Principles of Communication

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**
3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	ANALOG MODULATION: Concept of frequency translation. Amplitude Modulation: Description of full AM, DSBSC, SSB and VSB in time and frequency domains, methods of generation & demodulation, frequency division multiplexing (FDM). Angle Modulation: Phase and frequency modulation. Descriptions of FM signal in time and frequency domains, methods of generation & demodulation, pre- emphasis & de-emphasis, PLL.	7
3	PULSE ANALOG MODULATION: Ideal sampling, Sampling theorem, aliasing, interpolation, natural and flat top sampling in time and frequency domains. Introduction to PAM, PWM, PPM modulation schemes. Time division multiplexing (TDM)	8
4	PCM & DELTA MODULATION SYSTEMS: Uniform and Non-uniform quantization. PCM and delta modulation, Signal to quantization noise ratio in PCM and delta modulation. DPCM, ADM, T1 Carrier System, Matched filter detection. Error probability in PCM system.	8
5	DIGITAL MODULATION: Baseband transmission: Line coding (RZ, NRZ), inter symbol interference (ISI), pulse shaping, Nyquist criterion for distortion free base band transmission, raised cosine spectrum. Pass band transmission: Geometric interpretation of signals, orthogonalization. ASK PSK, FSK, QPSK and MSK modulation techniques, coherent detection and calculation of error probabilities.	8
6	SPREAD-SPECTRUM MODULATION: Introduction, Pseudo-Noise sequences, direct sequence spread spectrum (DSSS) with coherent BPSK, processing gain, probability of error, frequency-hop spread spectrum (FHSS). Application of spread spectrum: CDMA.	8
	Total	40

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SYLLABUS

II Year- IV Semester: B.Tech. (Information Technology)

4IT4-05: Database Management System

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	Introduction to database systems: Overview and History of DBMS. File System v/s DBMS. Advantage of DBMS Describing and Storing Data in a DBMS. Queries in DBMS. Structure of a DBMS. Entity Relationship model: Overview of Data Design Entities, Attributes and Entity Sets, Relationship and Relationship Sets. Features of the ER Model- Key Constraints, Participation Constraints, Weak Entities, Class Hierarchies, Aggregation, Conceptual Data Base, and Design with ER Model- Entity v/s Attribute, Entity vs Relationship Binary vs Ternary Relationship and Aggregation v/s ternary Relationship Conceptual Design for a Large Enterprise.	7
3	Relationship Algebra and Calculus: Relationship Algebra Selection and Projection, Set Operations, Renaming, Joints, Division, Relation Calculus, Expressive Power of Algebra and Calculus. SQL queries programming and Triggers: The Forms of a Basic SQL Query, Union, and Intersection and Except, Nested Queries, Correlated Nested Queries, Set-Comparison Operations, Aggregate Operators, Null Values and Embedded SQL, Dynamic SQL, ODBC and JDBC, Triggers and Active Databases.	8
4	Schema refinement and Normal forms: Introductions to Schema Refinement, Functional Dependencies, Boyce-Codd Normal Forms, Third Normal Form, Normalization-Decomposition into BCNF Decomposition into 3-NF.	8
5	Transaction Processing: Introduction-Transaction State, Transaction properties, Concurrent Executions. Need of Serializability, Conflict vs. View Serializability, Testing for Serializability, Recoverable Schedules, Cascadeless Schedules.	8
6	Concurrency Control: Implementation of Concurrency: Lock-based protocols, Timestamp-based protocols, Validation-based protocols, Deadlock handling, Database Failure and Recovery: Database Failures, Recovery Schemes: Shadow Paging and Log-based Recovery, Recovery with Concurrent transactions.	8
	Total	**40**

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SYLLABUS

II Year- IV Semester: B.Tech. (Information Technology)

4IT4-06: Theory of Computation

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	Finite Automata & Regular Expression: Basic machine, Finite state machine, Transition graph, Transition matrix, Deterministic and non-deterministic finite automation, Equivalence of DFA and NDFA, Decision properties, minimization of finite automata, Mealy & Moore machines. Alphabet, words, Operations, Regular sets, relationship and conversion between Finite automata and regular expression and vice versa, designing regular expressions, closure properties of regular sets, Pumping lemma and regular sets, Myhill- Nerode theorem , Application of pumping lemma, Power of the languages.	7
3	Context Free Grammars (CFG), Derivations and Languages, Relationship between derivation and derivation trees, leftmost and rightmost derivation, sentential forms, parsing and ambiguity, simplification of CFG, normal forms, Greibach and Chomsky Normal form , Problems related to CNF and GNF including membership problem.	8
4	Nondeterministic PDA, Definitions, PDA and CFL, CFG for PDA, Deterministic PDA, and Deterministic PDA and Deterministic CFL , The pumping lemma for CFL's, Closure Properties and Decision properties for CFL, Deciding properties of CFL.	8
5	Turing Machines: Introduction, Definition of Turing Machine, TM language Acceptors and Transducers, Computable Languages and functi Universal TM & Other modification, multiple tracks Turing Machine. Hierarchy of Formal languages: Recursive & recursively enumerable languages, Properties of RL and REL, Introduction of Context sensitive grammers and languages, The Chomsky Hierarchy.	8
6	Tractable and Untractable Problems: P, NP, NP complete and NP hard problems, Un-decidability, examples of these problems like vertex cover problem, Hamiltonian path problem, traveling sales man problem.	8
	Total	40

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4IT4-07: Data Communication and Computer Networks

Credit: 3 **Max. Marks: 150(IA:30, ETE:120)**
3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	Introductory Concepts: Network hardware, Network software, topologies, Protocols and standards, OSI model, TCP model, TCP/IP model, Physical Layer:Digital and Analog Signals, Periodic Analog Signals, Signal Transmission, Limitations of Data Rate, Digital Data Transmission, Performance Measures, Line Coding, Digital Modulation, Media and Digital Transmission System.	7
3	Data Link Layer:Error Detection and Correction,Types of Errors, Two dimensional parity check, Detection verses correction, Block Coding, Linear Block Coding, Cyclic Codes, Checksum, Standardized Polynomial Code, Error Correction Methods, Forward Error Correction, Protocols: Stop and wait, Go-back-N ARQ, Selective Repeat ARQ, Sliding window, Piggy backing, Pure ALOHA, Slotted ALOHA, CSMA/CD, CSMA/CA	8
4	Network Layer:Design issues, Routing algorithms: IPV4, IPV6, Address mapping: ARQ, RARQ, Congestion control, Unicast, Multicast, Broadcast routing protocols, Quality of Service, Internetworking.	8
5	Transport Layer: Transport service, Elements of transport protocols, User Datagram Protocol, Transmission Control Protocol, Quality of service, Leaky Bucket and Token Bucket algorithm.	8
6	Application Layer**:** WWW, DNS, Multimedia, Electronic mail, FTP, HTTP, SMTP, Introduction to network security	8
	Total	40

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4IT4-21: Linux Shell Programming Lab

Credit: 1 **Max. Marks: 50(IA:30, ETE:20)**
0L+0T+2P

List of Experiments:

1. Use of Basic Unix Shell Commands: ls, mkdir, rmdir, cd, cat, banner, touch, file, wc, sort, cut, grep, dd, dfspace, du, ulimit.
2. Commands related to inode, I/O redirection and piping, process control commands, mails.
3. Shell Programming: Shell script based on control structure- **If-then-if, if-then-else-if, nested if-else to find**
   3.1 Greatest among three numbers.
   3.2 To find a year is leap year or not.
   3.3 To input angles of a triangle and find out whether it is valid triangle or not.
   3.4 To check whether a character is alphabet, digit or special character.
   3.5 To calculate profit or loss.
4. Shell Programming - Looping- while, until, for loops
   4.1 Write a shell script to print all even and odd number from 1 to 10.
   4.2 Write a shell script to print table of a given number
   4.3 Write a shell script to calculate factorial of a given number.
   4.4 Write a shell script to print sum of all even numbers from 1 to 10.
   4.5 Write a shell script to print sum of digit of any number.
5. Shell Programming - case structure, use of break
   5.1 Write a shell script to make a basic calculator which performs addition, subtraction,
   Multiplication, division
   5.2 Write a shell script to print days of a week.
   5.3 Write a shell script to print starting 4 months having 31 days.
6. Shell Programming - Functions
   6.1 Write a shell script to find a number is Armstrong or not.
   6.2 Write a shell script to find a number is palindrome or not.
   6.3 Write a shell script to print Fibonacci series.
   6.4 Write a shell script to find prime number.
   6.5 Write a shell script to convert binary to decimal and decimal to binary
7. Write a shell script to print different shapes- Diamond, triangle, square, rectangle, hollow square etc.

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8. Shell Programming – Arrays

8.1 Write a C program to read and print elements of array.

8.2 Write a C program to find sum of all array elements.

8.3 Write a C program to find reverse of an array.

8.4 Write a C program to search an element in an array.

8.5 Write a C program to sort array elements in ascending or descending order.

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4IT4-22: Database Management System Lab

Credit: 1.5 **Max. Marks: 75(IA:45, ETE:30)**
0L+0T+3P

List of Experiments:

1. Design a Database and create required tables. For e.g. Bank, College Database
2. Apply the constraints like Primary Key, Foreign key, NOT NULL to the tables.
3. Write a SQL statement for implementing ALTER,UPDATE and DELETE.
4. Write the queries to implement the joins.
5. Write the query for implementing the following functions: MAX (), MIN (), AVG () and COUNT ().
6. Write the query to implement the concept of Integrity constrains.
7. Write the query to create the views.
8. Perform the queries for triggers.
9. Perform the following operation for demonstrating the insertion , updation and deletion
10. Using the referential integrity constraints.
11. Write the query for creating the users and their role.

Data Base Designing Project:

For better understanding students (group of 3-4 students) should design data base for any data base project, understand the requirement and design methodology of project by its own.

Some example of data base design project like:

College management system, Inventory management system and Hospital management system.

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4IT4-23: Network Programming Lab

Credit: 1.5 **Max. Marks: 75(IA:45, ETE:30)**
0L+0T+3P

List of Experiments:

1. Study of Different Type of LAN& Network Equipments.
2. Study and Verification of standard Network topologies i.e. Star, Bus, Ring etc.
3. LAN installations and Configurations.
4. Write a program to implement various types of error correcting techniques.
5. Write a program to implement various types of framing methods.
6. Write two programs in C: hello_client and hello_server
    a. The server listens for, and accepts, a single TCP connection; it reads all the data it can from that connection, and prints it to the screen; then it closes the connection
    b. The client connects to the server, sends the string “Hello, world!”, then closes the connection
7. Write an Echo_Client and Echo_server using TCP to estimate the round trip time from client to the server. The server should be such that it can accept multiple connections at any given time.
8. Repeat Exercises 6 & 7 for UDP.
9. Repeat Exercise 7 with multiplexed I/O operations.
10. Simulate Bellman-Ford Routing algorithm in NS2.

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4IT4-24: Java Lab

Credit: 1 **Max. Marks: 50(IA:30, ETE:20)**
0L+0T+2P

List of Experiment:

1. Develop an in depth understanding of programming in Java: data types, variables, operators, operator precedence, Decision and control statements, arrays, switch statement, Iteration Statements, Jump Statements, Using break, Using continue, return.
2. Write Object Oriented programs in Java: Objects, Classes constructors, returning and passing objects as parameter, Inheritance, Access Control, Using super, final with inheritance Overloading and overriding methods, Abstract classes, Extended classes.
3. Develop understanding to developing packages & Interfaces in Java: Package, concept of CLASSPATH, access modifiers, importing package, Defining and implementing interfaces.
4. Develop understanding to developing Strings and exception handling: String constructors, special string operations, character extraction, searching and comparing strings, string Buffer class. Exception handling fundamentals, Exception types, uncaught exceptions, try, catch and multiple catch statements.
   Usage of throw, throws and finally.
5. Develop applications involving file handling: I/O streams, File I/O.
6. Develop applications involving concurrency: Processes and Threads, Thread Objects, Defining and Starting a Thread, Pausing Execution with Sleep, Interrupts, Joins, and Synchronization.

Indicative List of exercises:

7. Programs to demonstrate basic concepts e.g. operators, classes, constructors, control & iteration statements, recursion etc. such as complex arithmetic, matrix
   arithmetic, tower of Hanoi problem etc.
8. Development of programs/projects to demonstrate concepts like inheritance, exception handling, packages, interfaces etc. such as application for electricity department, library management, ticket reservation system, payroll system etc.

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9. Development of a project to demonstrate various file handling concepts.
10. Develop applications involving Applet: Applet Fundamentals, using paint method and drawing polygons. It is expected that each laboratory assignments to given to the students with an aim to In order to achieve the above objectives.

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4IT4-25: Web Technology Lab

Credit: 1 **Max. Marks: 50(IA:30, ETE:20)**
0L+0T+2P

List of Experiments:

1. Develop static pages (using only HTML) of an online Book store. The pages should resemble: www.amazon.com. The website should consist of the following pages: Home page, Registration and user Login, User profile page, Books catalog, Shopping cart, Payment By credit card, order confirmation.
2. Validate the registration, user login, user profile and payment by credit card pages using JavaScript.
3. Write an XML file which will display the Book information which includes the following:
   1) Title of the book
   2) Author Name
   3) ISBN number
   4) Publisher name
   5) Edition
   6) Price

   Write a Document Type Definition (DTD) to validate the above XML file. Display the XML file as follows. The contents should be displayed in a table. The header of the table should be in color GREY. And the author name column should be displayed in one color and should be capitalized and bold. Use your own colors for remaining columns. Use XML schemas XSL and CSS for the above purpose.
4. 1) Install TOMCAT web server. While installation assign port number 8080. Make sure that these ports are available i.e., no other process is using this port.

   2) Access the above developed static web pages for books web site, using these servers by putting the web pages developed in practical 1 and 2 in the document root. Access the pages by using the urls : http://localhost:8080/rama/books.html

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5. User Authentication: Assume four users user1, user2, user3 and user4 having the passwords pwd1, pwd2, pwd3 and, pwd4 respectively. Write a servlet for doing the following.
   1.) Create a Cookie and add these four user ids and passwords to this Cookie.
   2.) Read the user id and passwords entered in the Login form (Practical 1) and authenticate with the values (user id and passwords) available in the cookies. If he is a valid user (i.e., user-name and password match) you should welcome him by name (user-name) else you should display "You are not an authenticated user ".
6. Install a database (MySQL or Oracle). Create a table which should contain at least the following fields: name, password, email-id, phone number (these should hold the data from the registration form). Practice 'JDBC' connectivity. Write a java program/servlet/JSP to connect to that database and extract data from the tables and display them. Experiment with various SQL queries. Insert the details of the users who register with the web site, whenever a new user clicks the submit button in the registration page.
7. Write a JSP which does the following job: Insert the details of the 3 or 4 users who register with the web site by using registration form. Authenticate the user when he submits the login form using the user name and password from the database.
8. Create on ODBC link, Compile & execute JAVA JDBC Socket.
9. Design and implement a simple shopping cart example with session tracking API.
10. Mini Project.

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4IT10A- Business Entrepreneurship Development (Common to Computer Science and Engineering& Info. Tech)

Class: IV Sem. B.Tech.	Evaluation
Branch: Information Technology **Schedule per Week** **Practical Hrs : 2**	**Examination Time = Three (3) Hours** **Maximum Marks = 50** **[Sessional/Mid-term (30) & End-term (20)]**

1. Introduction to Entrepreneurship- Concept and need, Entrepreneurship and innovation, Entrepreneurship and economic growth.
2. Entrepreneurial competencies, Leadership, Decision making, Motivation, Risk taking.
3. Business Enterprise Planning- Identification of business opportunity, Idea generation, Demand estimation, Preparation of project report, Feasibility analysis.
4. Intellectual Property rights, Patents, Taxation- Central excise & Sales tax, VAT.
5. Government Policies for Entrepreneurs, Entrepreneurial career opportunities for Engineers, case studies.

5IT1A- COMPUTER ARCHITECTURE (Common to CS & IT)

Class: V Sem. B.Tech.	Evaluation
Branch: I.T. **Schedule per Week** **Lectures: 3, Tutorial: 0**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Introduction to Computer Architecture and Organization. Von Neuman Architecture, Flynn Classification. Register Transfer and Micro operations: Register transfer language, Arithmetic Micro-operations, Logic Micro-operations, Shift Micro-operations, Bus and memory transfers. Computer Organization and Design: Instruction cycle, computer registers, common bus system, computer instructions, addressing modes, design of a basic computer
II	Central Processing Unit: General register organization, stack organization, Instruction formats, Data transfer and manipulation, program control. RISC, CISC characteristics. Pipeline and Vector processing: Pipeline structure, speedup, efficiency, throughput and bottlenecks. Arithmetic pipeline and Instruction pipeline.
III	Computer Arithmetic: Adder, Ripple carry Adder, carry look Ahead Adder, Multiplication: Add and Shift, Array multiplier and Booth Multiplier, Division: restoring and Non-restoring Techniques. Floating Point Arithmetic: Floating point representation, Add, Subtract, Multiplication, Division.
IV	Memory Organization: RAM, ROM, Memory Hierarchy, Organization, Associative memory, Cache memory, and Virtual memory: Paging and Segmentation.
V	Input-Output Organization: Input-Output Interface, Modes of Transfer, Priority Interrupt, DMA, IOP processor.

References:

1. Computer Organization and Architecture - William Stallings (Pearson Education Asia)

2. Computer Organization and Architecture -John P. Hayes (McGraw -Hill)

3. Computer Organization -V. Carl. Hamacher (McGraw-Hill)

5IT2A- Digital Signal Processing

Class: V Sem. B.Tech.	**Evaluation**
Branch: I.T. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	INTRODUCTION: Discrete time signals and systems, properties of discrete time systems, Linear time invariant systems - discrete time. Properties of LTI systems and their block diagrams. Convolution, Discrete time systems described by difference equations.
II	Fourier Transform: Discrete time Fourier transform for periodic and aperiodic signals. Properties of DTFT. Z-transform: The region of convergence for the Z-transform. The Inverse Z-transform. Properties of Z transform.
III	SAMPLING: Mathematical theory of sampling. Sampling theorem. Ideal & Practical sampling. Interpolation technique for the reconstruction of a signal from its samples. Aliasing. Sampling in freq. domain. Sampling of discrete time signals.
IV	THE DISCRETE FOURIER TRANSFORMS (DFT): Properties of the DFT, Linear Convolution using DFT. Efficient computation of the DFT: Decimation–in-Time and Decimation-in frequency FFT Algorithms.
V	FILTER DESIGN TECHNIQUES: Structures for discrete-time systems- Block diagram and signal flow graph representation of LCCD (LCCD – Linear Constant Coefficient Difference) equations, Basic structures for IIR and FIR systems, Transposed forms. Introduction to filter Design: Butterworth & Chebyshev. IIR filter design by impulse invariance & Bilinear transformation. Design of FIR filters by Windowing: Rectangular, Hamming & Kaiser.

References:

1. Oppenheim, Discrete-Time Signal Processing, 2/e, Pearson Education
2. Proakis, Digital Signal Processing, 4/e, Pearson Education
3. S.K.Mitra, Digital Signal Processing, 2/e, Tata McGraw Hill

5IT3A- TELECOMMUNICATION FUNDAMENTALS (Common to CS & IT)

Class: V Sem. B.Tech.	Evaluation
Branch: Info. Tech **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Data Transmission: Terminology, Frequency, spectrum, bandwidth, analog and digital transmission, Transmission impairments, channel capacity, Transmission Media. Wireless Transmission: Antenna and antenna gain. Network Reference Models (OSI/ISO and TCP/IP) Physical Layer: Line Encoding Schemes. Concept of bit period, effect of clock skew, Synchronous and Asynchronous communication. Data Link Layer: Functions of data link layer and design issues Flow Control: Flow control in loss less and lossy channels using stop-and-wait, sliding window protocols. Performance of protocols used for flow control.
II	Error Control Coding: Error Detection, Two Dimensional Parity Checks, and Internet Checksum. Polynomial Codes, Standardized polynomial codes, error detecting capability of a polynomial codes. Linear codes, performance of linear codes, error detection & correction using linear codes. Data Link Control: HDLC & PPP including frame structures. MAC sublayer: Channel Allocation Problem, Pure and slotted Aloha, CSMA, CSMA/CD, collision free multiple access. Throughput analysis of pure and slotted Aloha. Ethernet Performance.
III	Wireless LAN: Hidden node and Exposed node Problems, RTS/CTS based protocol, 802.11 Architecture, protocol stack, Physical layer, MAC Sublayer. Bluetooth Architecture and Protocol Stack Data Link Layer Switching: Bridges (Transparent, Learning and Spanning Tree), Virtual LANs
IV	Multiplexing: Frequency division, time division (Synchronous and statistical) multiplexing. ADSL, DS1 and DS3 carriers. Multiple Accesses: TDMA frame structure, TDMA Burst Structure, TDMA Frame efficiency, TDMA Superframe structure, Frame acquisition and synchronization, Slip rate in digital terrestrial networks. Switching: Qualitative description of Space division, time division and space-time-space division switching.
V	Spread Spectrum Techniques: Direct sequence(DSSS) & frequency hopping(FHSS); Performance consideration in DSSS & FHSS; Code division Multiple access (CDMA): frequency & channel specifications, forward & reverse CDMA channel, pseudo noise(PN) sequences, m-sequence, gold sequence, orthogonal code, gold sequences, Walsh codes, synchronization, power control, handoff, capacity of CDMA system, IMT-2000, WCDM

Text/References:

1. Stallings, Data and computer communication, 8th ed. Pearson
2. Tri.T.Ha, Digital Satellite Communications, 2/e, Tata McGraw Hill
3. Alberto Leon-Garcia, Indra Widjaja, COMMUNICATION NETWORKS, 2nd ed., TMH
4. Wireless Communications, 2/e, Rappaport, PHI
5. Analysis of Computer and Communication Networks, ISBN: 0387744363, Fayez Gebali, 2008, Springer-verlag, 1st Ed.

5IT4A- DATABASE MANAGEMENT SYSTEMS (Common to CS & IT)

Class: V Sem. B.Tech.	**Evaluation**
Branch: I.T. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	INTRODUCTION TO DATABASE SYSTEMS: Overview and History of DBMS. File System v/s DBMS .Advantage of DBMS Describing and Storing Data in a DBMS. Queries in DBMS. Structure of a DBMS.
II	ENTITY RELATIONSHIP MODEL: Overview of Data Design Entities, Attributes and Entity Sets, Relationship and Relationship Sets. Features of the ER Model- Key Constraints, Participation Constraints, Weak Entities, Class Hierarchies, Aggregation, Conceptual Data Base, Design with ER Model-Entity v/s Attribute, Entity vs Relationship Binary vs Ternary Relationship and Aggregation v/s ternary Relationship Conceptual Design for a Large Enterprise.
III	RELATIONSHIP ALGEBRA AND CALCULUS**:** Relationship Algebra Selection and Projection, Set Operations, Renaming, Joints, Division, Relation Calculus, Expressive Power of Algebra and Calculus.
IV	SQL QUERIES PROGRAMMING AND TRIGGERS: The Forms of a Basic SQL Query, Union, Intersection and Except, Nested Queries ,Correlated Nested Queries, Set-Comparison Operations, Aggregate Operators, Null Values and Embedded SQL, Dynamic SQL, ODBC and JDBC, Triggers and Active Databases.
V	SCHEMA REFINEMENT AND NORMAL FORMS: Introductions to Schema Refinement, Functional Dependencies, Boyce-Codd Normal Forms, Third Normal Form, Normalization-Decomposition into BCNF Decomposition into 3-NF.

References:

1. H.f. Korth and Silberschatz: Database Systems Concepts, McGraw Hill
2. Almasri and S.B. Navathe: Fundamentals of Database Systems,
3. C.J. Date: Data Base Design, Addison Wesley
4. Hansen and Hansen : DBM and Design, PHI

5IT5A- OPERATING SYSTEMS (Common to CS & IT)

Class: V Sem. B.Tech.	Evaluation
Branch: I.T. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	**Introduction** and need of operating system, layered architecture/logical structure of operating system, Type of OS, operating system as resource manager and virtual machine, OS services, BIOS, System Calls/Monitor Calls, Firmware- BIOS, Boot Strap Loader. **Process management**- Process model, creation, termination, states & transitions, hierarchy, context switching, process implementation, process control block, Basic System calls- Linux & Windows. **Threads**- processes versus threads, threading, concepts, models, kernel & user level threads, thread usage, benefits, multithreading models.
II	**Interprocess communication**- Introduction to message passing, Race condition, critical section problem, mutual exclusion with busy waiting- disabling interrupts, lock variables, strict alteration, Peterson's solution, TSL instructions, busy waiting, sleep and wakeup calls, semaphore, monitors, classical IPC problems. **Process scheduling**- Basic concepts, classification, CPU and I/O bound, CPU scheduler- short, medium, long-term, dispatcher, scheduling:- preemptive and non-preemptive, Static and Dynamic Priority, Co-operative & Non-cooperative, Criteria/Goals/Performance Metrics, scheduling algorithms- FCFS, SJFS, shortest remaining time, Round robin, Priority scheduling, multilevel queue scheduling, multilevel feedback queue scheduling, Fair share scheduling.
III	**Deadlock**- System model, resource types, deadlock problem, deadlock characterization, methods for deadlock handling, deadlock prevention, deadlock avoidance, deadlock detection, recovery from deadlock. **Memory management**- concepts, functions, logical and physical address space, address binding, degree of multiprogramming, swapping, static & dynamic loading- creating a load module, loading, static & dynamic linking, shared libraries, memory allocation schemes- first fit, next fit, best fit, worst fit, quick fit. Free space management- bitmap, link list/free list, buddy's system, memory protection and sharing, relocation and address translation.
IV	**Virtual Memory**- concept, virtual address space, paging scheme, pure segmentation and segmentation with paging scheme hardware support and implementation details, memory fragmentation, demand paging, pre-paging, working set model, page fault frequency, thrashing, page replacement algorithms- optimal, NRU, FIFO, second chance, LRU, LRU- approximation clock, WS clock; Belady's anomaly, distance string; design issues for paging system- local versus global allocation policies, load

	control, page size, separate instruction and data spaces, shared pages, cleaning policy, TLB (translation look aside buffer) reach, inverted page table, I/O interlock, program structure, page fault handling, Basic idea of MM in Linux & windows.
V	**File System**- concepts, naming, attributes, operations, types, structure, file organization & access(Sequential, Direct ,Index Sequential) methods, memory mapped files, directory structures- one level, two level, hierarchical/tree, acyclic graph, general graph, file system mounting, file sharing, path name, directory operations, overview of file system in Linux & windows. **Input/Output subsystems**- concepts, functions/goals, input/output devices- block and character, spooling, disk structure & operation, disk attachment, disk storage capacity, disk scheduling algorithm- FCFS, SSTF, scan scheduling, C-scan schedule.

Text/Reference Books:

1. A. Silberschatz and Peter B Galvin: Operating System Principals, Wiley India Pvt. Ltd.
2. Achyut S Godbole: Operating Systems, Tata McGraw Hill
3. Tanenbaum: Modern Operating System, Prentice Hall.
4. DM Dhamdhere: Operating Systems – A Concepts Based Approach, Tata McGraw Hill
5. Charles Crowly: Operating System A Design – Oriented Approach, Tata McGraw Hill.

5IT6.1A- ADVANCED DATA STRUCTURE (Common to CS & IT)

Class: V Sem. B.Tech.	**Evaluation**
Branch: I.T. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	ADVANCED TREES: Definitions, Operations on Weight Balanced Trees (Huffman Trees), 2-3 Trees and Red- Black Trees. Dynamic Order Statistics, Interval Tree; Dictionaries.
II	MERGEABLE HEAPS: Mergeable Heap Operations, Binomial Trees, Implementing Binomial Heaps and its Operations, 2-3-4. Trees and 2-3-4 Heaps. Amortization analysis and Potential Function of Fibonacci Heap, Implementing Fibonacci Heap.
III	GRAPH THEORY DEFINITIONS: Definitions of Isomorphic Components. Circuits, Fundamental Circuits, Cut-sets. Cut- Vertices Planer and Dual graphs, Spanning Trees, Kuratovski's two Graphs. GRAPH THEORY ALGORITHMS**:** Algorithms for Connectedness, Finding all Spanning Trees in a Weighted Graph, Breadth First and Depth First Search, Topological Sort, Strongly Connected Components and Articulation Point. Single Min-Cut Max-Flow theorem of Network Flows. Ford-Fulkerson Max Flow Algorithms.
IV	SORTING NETWORK**:** Comparison network, zero-one principle, bitonic sorting and merging network sorter. Priority Queues and Concatenable Queues using 2-3 Trees. Operations on Disjoint sets and its union-find problem, Implementing Sets.
V	NUMBER THEORITIC ALGORITHM: Number theoretic notions, Division theorem, GCD, recursion, Modular arithmetic, Solving Modular Linear equation, Chinese Remainder Theorem, power of an element, Computation of Discrete Logarithms, primality Testing and Integer Factorization.

References:

1. Cormen, Leiserson, Rivest: Introduction to Algorithms, Prentice Hall of India.

2. Horowitz and Sahani: Fundamental of Computer algorithms.

3. Aho A.V , J.D Ulman: Design and analysis of Algorithms, Addison Wesley

4. Brassard : Fundamental of Algorithmics, PHI.

5IT6. 2A- E-Commerce

Class: V Sem. B.Tech.	**Evaluation**
Branch: I.T. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	**Introduction:** Motivation, Forces behind E commerce industry framework, brief history of ecommerce, inter organizational ecommerce, intra organizational ecommerce and consumer to business electronic commerce, architectural framework, Network infrastructure for ecommerce, market forces behind 1 way, component of 1 way acess equipment, global information distribution network, broadband telecommunication.
II	**Mobile commerce:** Introduction to mobile commerce, mobile computing application, wireless application protocols, WAP technology, mobile information devices, web security, introduction to web security, firewalls & transaction security, client server network, emerging client server security threats, firewalls and network security.
III	**Encryption:** World wide web & security, encryption, transaction security, secret key encryption , public key encryption, virtual private network(VPN), implementation management issues.
IV	**Electronic payment:** Overview of electronic payments, digital token based, electronic payment system, smart cards, credit card I, debit card based EPS, emerging financial instruments, home banking, online banking.
V	**Net commerce:** EDA, EDI application in business, legal requirement in Ecommerce, introduction to supply chain management, CRM, issues in customer relationship management.

References:

1. Electronic e-commerce II Edition: Pete Loshin, Paul A Murphy, Jaico book.

2. The Business of e-commerce: Paul May, Cambridge University Press.

5IT6.3A- SATELLITE & MICROWAVE COMMUNICATION

Class: V Sem. B.Tech.	**Evaluation**
Branch: I.T. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	WAVE GUIDES :Introduction of Microwaves and their applications. Rectangular Waveguides , Solution of Wave equation in TE and TM modes. Power transmission and Power losses. Excitation of modes in Rectangular waveguides, circular waveguides : Basic idea of TE and TM modes, field patterns, TEM mode of propagation.
II	WAVEGUIDE COMPONENTS : Scattering matrix representation of networks. Rectangular cavity and circular cavity resonators. Waveguide Tees, Magic Tees. Hybrid rings. Waveguide corners, Bends and twists. Directional couplers, Circulators and isolators. Broadband Wireless 802.16
III	Elements of satellite communication: Frequency bands, Transmission and multiplexing. Modulation, Multiple access techniques.
IV	Satellite orbit and description- orbital period and velocity, effects of orbital inclination, Azimuth and elevation, Coverage angle and slant range, Geostationary orbit,
V	Satellite description: Communications subsystems. Earth Station: Antenna, high-power amplifier, low-noise amplifier, up converter, down converter, monitoring and control, reliability. Satellite Link: basic link analysis.

References:

1. Liao, Microwave Devices and Circuits, 3/e, Pearson Education
2. Tri.T.Ha, Digital Satellite Communications, 2/e, Tata McGraw Hill
3. Communication Systems, Simon Haykin, John Wiley.

5IT7A- DATABASE LAB (Common to CS & IT)

Class: V Sem. B.Tech.	**Evaluation**
Branch: I.T. **Schedule per Week** **Practical Hrs.: 3**	**Examination Time = Four (4) Hours** **Maximum Marks = 100** **[Sessional/Mid-term (60) & End-term (40)]**

Objectives: At the end of the semester, the students should have clearly understood and implemented the following:

1. Stating a database design problem.
2. Preparing ER diagram
3. Finding the data fields to be used in the database.
4. Selecting fields for keys.
5. Normalizing the database including analysis of functional dependencies.
6. Installing and configuring the database server and the front end tools.
7. Designing database and writing applications for manipulation of data for a stand alone and shared data base including concepts like concurrency control, transaction roll back, logging, report generation etc.
8. Get acquainted with SQL.

In order to achieve the above objectives, it is expected that each students will chose one problem. The implementation shall being with the statement of the objectives to be achieved, preparing ER diagram, designing of database, normalization and finally manipulation of the database including generation of reports, views etc. The problem may first be implemented for a standalone system to be used by a single user.

All the above steps may then be followed for development of a database application to be used by multiple users in a client server environment with access control. The application shall NOT use web techniques.

One exercise may be assigned on creation of table, manipulation of data and report generation using SQL.

Suggested Tools:

For standalone environment, Visual FoxPro or any similar database having both the database and manipulation language may be used.

For multi-user application, MYSql is suggested. However, any other database may also be used. For front end, VB.Net, Java, VB Script or any other convenient but currently used by industry may be chosen.

Indicative List of exercises:

1. Student information system for your college.
2. Student grievance registration and redressal system.
3. A video library management system for a shop.
4. Inventory management system for a hardware/ sanitary item shop.
5. Inventory management system for your college.
6. Guarantee management system for the equipments in your college.

5IT8A- ADVANCE COMMUNICATION LAB. (Info. Tech)

Class: V Sem. B.Tech.	**Evaluation**
Branch: I.T. **Schedule per Week** **Practical Hrs : 3**	**Examination Time = Four (4) Hours** **Maximum Marks = 75** **[Sessional/Mid-term (45) & End-term (30)]**

S. No.	**List of Experiments**
1.	TDM of two band limited signals.
2.	ASK and FSK generation and detection
3.	PSK generation and detection
4.	DPSK generation and detection
5.	QPSK generation and detection
6.	PCM generation and detection using a CODEC Chip
7.	Measurement of losses in a given optical fiber (propagation loss, bending loss) and numerical aperture
8.	Analog and Digital (with TDM) communication link using optical fiber.
9.	Measurement of frequency, guide wavelength, power, VSWR and attenuation in a microwave test bench
10.	Measurement of directivity and gain of antennas: Standard dipole (or printed dipole), microstrip patch antenna and Yagi antenna (printed).
11	Determination of coupling and isolation characteristics of a stripline (or microstrip) directional coupler (a) Measurement of resonance characteristics of a microstrip ring resonator and determination of dielectric constant of the substrate. (b) Measurement of power division and isolation characteristics of a microstrip 3 dB power divider.

5IT9A- OPERATING SYSTEMS SIMULATION LAB(Common to CS & IT)

Class: V Sem. B.Tech.	**Evaluation**
Branch: I.T. **Schedule per Week** **Practical Hrs : 3**	**Examination Time = Four (4) Hours** **Maximum Marks = 100** **[Sessional/Mid-term (60) & End-term (40)]**

Objectives:

- Understand the basic functions of operating systems.
- In depth knowledge of the algorithms used for implementing the tasks performed by the operating systems.
- Understand & simulate strategies used in Linux & Windows operating systems.
- Develop aptitude for carrying out research in the area of operating system.

Suggested Tools:

Operating system simulator- MOSS preferably on Linux platform. (Available for free download from http://www.ontko.com/moss/).

Recommended Excercises:

A. Exercises shall be given on simulation of algorithms used for the tasks performed by the operating systems. Following modules of the simulator may be used:
   - Scheduling
   - Deadlock
   - Memory Management Systems
   - File system simulator

Algorithms described in the text may be assigned. The simulation results such as average latency, hit & Miss Ratios or other performance parameters may be computed.

B. One exercise shall be on simulation of algorithms reported in the recent conferences/ journals and reproducing the results reported therein.

5IT10A- DIGITAL HARDWARE DESIGN LAB (Common to CS & IT)

Class: VI Sem. B.Tech.	**Evaluation**
Branch: I.T. **Schedule per Week** **Practical Hrs : 3**	**Examination Time = Four (4) Hours** **Maximum Marks = 75** **[Sessional/Mid-term (45) & End-term (30)]**

Tech)

Objectives: At the end of course, the students shall be able to

- Should be able to design datapath for digital systems
- Create a digital system using discrete digital ICs
- Design a hard wired / micro-programmed control circuit
- Simulate a digital datapath in Hardware Description Language
- Understand IC descriptions and select proper IC in a given circuit based on its timing characteristics

Suggested Methodology and tools: Hardware description language like verilog /VHDL can be used for simulation.

The exercise shall involve design of datapath, its simulation and finally realization on breadboard. Library of digital ICs have to be built. Similarly, manuals of Digital IC families have to be placed in the laboratories for reference by students.

Suggested Exercises

- Create a microprocessor from ALU 74181. For this, the students may design a small instruction set and attach necessary registers and suitable control unit to realize a microprocessor.
- Simulate and realize a Cordic calculator.
- Simulate & realize a Four bit Adder
    - Design and simulation of a 4-bit Adder
    - VHDL/verilog HDL (Hardware description language)
    - Interfacing 7-segment decoder
- Combinational Multiplier
    - 4x4-bit multiplier
    - Binary-to-BCD conversion
    - Timing Constraints
- CRC checksum generator & verifier
- Realizing a carry look ahead adder

6IT1A- COMPUTER NETWORKS (Common to CS & IT)

Class: VI Sem. B.Tech.	Evaluation
Branch: Info. Tech **Engg. Schedule per** **Week Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Network layer-design issue, routing algorithms: Distance vector, link state, hierarchical, Broadcast routing. Congestion control: congestion prevention policies, congestion control in Datagram subnets, load shedding, jitter control, Leaky bucket and token bucket algorithms.

NOTE: The first 2 lectures shall be devoted to review of the basis architectures and responsibilities of different layers.

II	Internetworking: Differences in networks, Tunneling, Internetwork routing, Fragmentation Network layer in the Internet: IPv4 classful and classless addressing, subnetting Network layer protocols(only working and purpose; packet headers etc. not included), Differences in IPV6 over IPV4. Routing to Mobile Hosts and Mobile IP
III	Elements of transport protocols: addressing, connection establishment and release, flow control and buffering, multiplexing and demultiplexing, crash recovery, introduction to UDP protocol. Principles of Reliable Data Transfer: Reliable data transfer over a perfectly reliable channel, Channel with bit errors and Lossy Channel with bit errors.
IV	Transport Layer in the Internet: Introduction to TCP, TCP service Model, TCP Header and segment structure, TCP connection establishment and release, transmission policy, timer management, Transactional TCP. Mobile TCP TCP Congestion Control: Fairness, TCP delay modeling.
V	Application Layer: World Wide Web (WWW), Domain Name System (DNS), E-mail, File Transfer Protocol (FTP), Introduction to Network security. P2P File Sharing: Centralized Directory, Query flooding, exploiting heterogeneity.

Text/References:

1. Tanenbaum; Computer Network, 4th Ed., Pearson.
2. Kurose; Computer Networking, 3rd Ed., Pearson.
3. Peterson, Davie; Computer Networks, 4rd Ed., ELSEVIER

6IT2A- DESIGN AND ANALYSIS OF ALGORITHMS (Common to CS & IT)

Class: VI Sem. B.Tech.	Evaluation
Branch: Info. Tech Engg. Schedule per Week Lectures: 3	Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]

Units	Contents of the subject
I	BACKGROUND: Review of Algorithm Complexity, Order Notations: definitions and calculating complexity. DIVIDE AND CONQUER METHOD: Binary Search, Merge Sort, Quick sort and strassen's matrix multiplication algorithms. GREEDY METHOD: Knapsack Problem, Job Sequencing, Optimal Merge Patterns and Minimal Spanning Trees.
II	DYNAMIC PROGRAMMING: Matrix Chain Multiplication. Longest Common Subsequence and 0/1 Knapsack Problem. BRANCH AND BOUND: Traveling Salesman Problem and Lower Bound Theory. Backtracking Algorithms and queens problem.
III	PATTERN MATCHING ALGORITHMS: Naïve and Rabin Karp string matching algorithms, KMP Matcher and Boyer Moore Algorithms. ASSIGNMENT PROBLEMS: Formulation of Assignment and Quadratic Assignment Problem.
IV	RANDOMIZED ALGORITHMS. Las Vegas algorithms, Monte Carlo algorithms, randomized algorithm for Min-Cut, randomized algorithm for 2-SAT. Problem definition of Multicommodity flow, Flow shop scheduling and Network capacity assignment problems.
V	PROBLEM CLASSES NP, NP-HARD AND NP-COMPLETE: Definitions of P, NP-Hard and NP-Complete Problems. Decision Problems. Cook's Theorem. Proving NP-Complete Problems - Satisfiability problem and Vertex Cover Problem. Approximation Algorithms for Vertex Cover and Set Cover Problem.

References:

1. Cormen, Leiserson, Rivest: Introduction to Algorithms, Prentice Hall of India.
2. Horowitz and Sahani: Fundamental of Computer algorithms.
3. Aho A.V , J.D Ulman: Design and analysis of Algorithms, AddisonWesley
4. Brassard : Fundamental of Algorithmics, PHI.

6IT3A- THEORY OF COMPUTATION (Common to CS & IT)

Class: VI Sem. B.Tech.	**Evaluation**
Branch: I.T. **Schedule per Week** **Lectures: 3, Tutorial:1**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Finite Automata & Regular Expression: Basic Concepts of finite state system, Deterministic and non-deterministic finite automation and designing regular expressions, relationship between regular expression & Finite automata minimization of finite automation mealy & Moore Machines.
II	Regular Sets of Regular Grammars**:** Basic Definition of Formal Language and Grammars. Regular Sets and Regular Grammars, closure proportion of regular sets, Pumping lemma for regular sets, decision Algorithms for regular sets, Myhell_Nerod Theory & Organization of Finite Automata.
III	Context Free Languages& Pushdown Automata: Context Free Grammars – Derivations and Languages – Relationship between derivation and derivation trees – ambiguity – simplification of CEG – Greiback Normal form – Chomsky normal forms – Problems related to CNF and GNF Pushdown Automata: Definitions – Moves – Instantaneous descriptions – Deterministic pushdown automata – Pushdown automata and CFL - pumping lemma for CFL - Applications of pumping Lemma.
IV	Turing Machines: Turing machines – Computable Languages and functions – Turing Machine constructions – Storage in finite control – multiple tracks – checking of symbols – subroutines – two way infinite tape. Undecidability: Properties of recursive and Recursively enumerable languages – Universal Turing Machines as an undecidable problem – Universal Languages – Rice's Theorems.
V	Linear bounded Automata Context Sensitive Language: Chomsky Hierarchy of Languages and automata, Basic Definition & descriptions of Theory & Organization of Linear bounded Automata Properties of context-sensitive languages

References

1. Aho, Hopcropt and Ullman, Introduction to Automata Theory, Formal Languages and Computation, Narosa
2. Cohen, Introduction to Computer Theory, Addison Wesley.
3. Papadimitriou, Introduction to Theory of Computing, Prentice Hall.

6IT4A- PROGRAMMING IN JAVA

Class: VI Sem. B.Tech.	**Evaluation**
Branch: I.T. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	JAVA: Introduction to Object Orientated Programming, Abstraction, Object Oriented Programming Principles, Features of JAVA, Introduction to Java byte code, Java Virtual machine. PROGRAM ELEMENTS: Primitive data types, variables, assignment, arithmetic, short circuit logical operators, Arithmetic operators, bit wise operators, relational operators, Boolean logic operators, the assignment operators, operator precedence, Decision and control statements, arrays.
II	CONTROL STATEMENTS: Java's Selection Statements, if statement, switch statement, Iteration Statements, while, do-while, for, for-each, Nested Loops, Jump Statements, Using break, Using continue, return. OBJECTS AND CLASSES: Objects, constructors, returning and passing objects as parameter, Nested and inner classes, Single and Multilevel Inheritance, Extended classes, Access Control, usage of super, Overloading and overriding methods, Abstract classes, Using final with inheritance.
III	PACKAGE AND INTERFACES: Defining package, concept of CLASSPATH, access modifiers, importing package, Defining and implementing interfaces. STRING HANDLING: String constructors, special string operations, character extraction, searching and comparing strings, string Buffer class.
IV	EXCEPTION HANDLING: Exception handling fundamentals, Exception types, uncaught exceptions, try, catch and multiple catch statements. Usage of throw, throws and finally FILE HANDLING: I/O streams, File I/O.
V	CONCURRENCY: Processes and Threads, Thread Objects, Defining and Starting a Thread, Pausing Execution with Sleep, Interrupts, Joins, Synchronization. APPLET: Applet Fundamentals, using paint method and drawing polygons.

References

1. Herbert Schildt: JAVA 2 - The Complete Reference, TMH, Delhi
2. U.K. Chakraborty and D.G. Dastidar: Software and Systems – An Introduction, Wheeler Publishing, Delhi.
3. Joseph O'Neil and Herb Schildt: Teach Yourself JAVA, TMH, Delhi.

6IT5A- INFORMATION THEORY & CODING

Units	Contents of the subject
I	Introduction to information theory. Uncertainty, Information and Entropy, Information measures for continuous random variables, source coding theorem. Discrete Memory less channels, Mutual information, Conditional entropy.
II	Source coding schemes for data compaction: Prefix code, Huffman code, Shanon-Fane code & Hempel-Ziv coding channel capacity. Channel coding theorem. Shannon limit.
III	Linear Block Code: Introduction to error connecting codes, coding & decoding of linear block code, minimum distance consideration, conversion of non systematic form of matrices into systematic form.
IV	Cyclic Code: Code Algebra, Basic properties of Galois fields (GF) polynomial operations over Galois fields, generating cyclic code by generating polynomial, parity check polynomial. Encoder & decoder for cyclic codes.
V	Convolutional Code: Convolutional encoders of different rates. Code Tree, Trllis and state diagram. Maximum likelihood decoding of convolutional code: The viterbi Algorithm fee distance of a convolutional code.

References

1. Digital Communication, Simon Haykin,

6IT6.1A- ADVANCE TOPICS IN OPERATING SYSTEMS (Common to CS & IT)

Class: VI Sem. B.Tech.	**Evaluation**
Branch: I.T. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	**Operating system structures** – policies & mechanism, Structures- monolithic, layered, virtual machines, micro kernel, exokernels, client- server model. Examples from Linux & Windows. **Threads Advance Concepts**– Libraries- Pthreads, win32 threads, Java threads, Introduction to threading issues, system calls, cancellation, signal handling, thread pool, thread specific data, window threads, Linux threads, Solaris Threads. **Massage Passing System** – Need of Message Passing Systems, design issues, naming, synchronization, Implementation–buffering and delivery; mailboxes; RPC & RMI. Examples Systems – Linux, Windows.
II	**File System-** file system layouts, file system implementation, contagious allocation, link list allocation, indexed allocation, file allocation table, virtual file system, directory implementation- linear list and hash table. File System reliability and integrity. **I/O system**: device drivers/ controllers, busses and interfaces- USB, IDE, SCSI, IEEE1394, RAID system, disk caching and buffering, disk management-disk formatting, RAID Structure, boot block, bad block, swap-space management. **System Security:** Security Problems, Program Threats, System Network Threats, Cryptography as a Security Tool, User Authentication, Implementing Security Defenses, Firewalling to Protect Systems and Network, Computer Security Classifications. Overview of security in Windows. [4]
III	**The Linux OS:** Unix Vs Linux, Design Principles, Kernel Structure, components Kernel Modules, Shell- usage, types; An overview of- Process Management, Thread Management and Scheduling, Memory Management, Process Scheduling in Linux, File System structure & implementation, I/O Management, Network File System, Inter-process Communications, Booting and login process, security.[3]
IV	**The Window OS:** Design Principles, System Components- Hardware Abstraction layer, Kernel, Executives; Environmental Subsystems- MS-DOS Environment, 16-bit Windows Environment, Win32 API, POSIX subsystem; Exception and Interrupts; An overview of-memory management, process management and thread; Process Scheduling in Windows; File Systems: Internal Layout, recovery, Volume Management and Fault Tolerance, FAT and NTFS, Security features, window registry, OS organizations.[3]

V	**Multiprocessor Operating Systems:** Architecture of Multiprocessor Systems, Overview of Multiprocessor OS, Kernal Structure and Multiprocessing support in Linux & Windows, Process Synchronization- Queued Lock, Spin Lock, Sleep Lock; Process Scheduling. **Multimedia Operating System**- Introduction to Multimedia & Data Compression- concepts, common graphics file formats, common audio file formats; Video server, Process management- real time scheduling; Multimedia file systems, Multimedia file storage mechanisms, Video sever organization.[2] **Mobile Operating System**- Windows CE, Palm OS, Symbian OS, JAVA card, Multos.

Text/Reference Books:

1. DM Dhamdhere: Operating Systems – A Concepts Based Approach, Tata McGraw Hill
2. Achyut S Godbole: Operating Systems, Tata McGraw Hill
3. Tanenbaum: Modern Operating System, Prentice Hall
4. A. Silberschatz and Peter B Galvin: Operating System Principals, Wiley India Pvt. Ltd.
5. Charles Crowly: Operating System A Design – Oriented Approach, Tata McGraw Hill.
6. Bach, Design of Unix Operating Systems.

6IT6.2A- BIO INFORMATICS

Class: VI Sem. B.Tech.	Evaluation
Branch: I.T. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Principles of mass and energy conservation. Thermodynamic properties of pure substances. Equations of state. Correlations for physical and transport properties. Material and energy balances for steady state processes involving single and multiphase systems. Reactive and non-reactive processes.
II	Energy flow in biological systems. Energetic of metabolic path ways. Coupled reactions, microbial growth kinetics, Stoichiometry and energetic analysis of cell growth and product formation. Yield and maintenance coefficients. Oxygen consumption and heat evolution in aerobic cultures. Thermodynamic efficiency of growth.
III	Introduction to fermentation, Design of a an industrial fermented, Process calculations for design of typical industrial fermentation processes. Medium formulation. Batch and continuous heat sterilisation of liquid media. Requirements for process utilities (compressed air, cooling water, steam etc.). Material and energy balances for downstream processing and waste water treatment processes, Bioremediation.
IV	Introduction to industrial bio-process: A historical overview of industrial fermentation processes and products. Role of a bio-process engineer in the biotechnology industry. Outline of the various unit operations involved in an integrated bio-process. Process flow sheeting. A brief survey of organisms, processes products and market economics relating to modern industrial bio-technology. Raw materials for fermentation process: Isolation, preservation and improvement of industrial micro-organisms for overproduction of primary and secondary metabolites. Medium requirements for fermentation process carbon, nitrogen, minerals, vitamins and other nutrients. Examples of simple and complex media. Production of primary metabolites: A brief outline of processes for the production of some commercially important organic acids (e.g. citric acid, itaconic acid, lactic acid, acetic acid, gluconic acid etc.), amino acids (glutamic acid, lysine, aspartic acid, phenylalanine etc.) and alcohols (ethanol 2.3, butanediol etc.)
V	Production of secondary metabolites: Study of production processes for various classes of low molecular weight secondary metabolites. Antibiotics-beta-lactams (penicillins, cephalosporins etc.), aminoglycosides (streptomycin, kanamycin etc.), macrolides (erythromycin), quinines, aromatics etc. Vitamins and steroids. Production of commercially important enzymes and recombinant proteins: Proteases, amylases, lipases, cellulases, pectinases, isomerases and other commercially important enzymes for the food and pharmaceutical industries. Production of recombinant poteins having therapeutic and diagnostic applications. Production of vaccines.

References

1. Bryan Bergerson, Bioinformatics Computing, Pearson Education.
2. Pierre Baldi, Bioinformatics: The Machine Learning Approach, Second Edition (Adaptive Computation and Machine Learning), MIT Press
3. David W. Mount, Bioinformatics: Sequence and Genome Analysis, Cold Spring Harbor Laboratory
4. Warren J. Ewens & Gregory R. Grant, Statistical Methods in Bioinformatics, Springer Verlag
5. Andreas D. Baxevanis & B. F. Francis Ouellette, Bioinformatics: A Practical Guide to the Analysis of Genes and Proteins, Wiley Interscience

6IT6.3A- HUMAN COMPUTER INTERFACE (Common to CS & IT)

Class: VI Sem. B.Tech.	**Evaluation**
Branch: I.T. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	The Human: input-output channels, Human memory, thinking, emotions, individual differences, psychology and the design of interactive systems. The Computer: Text entry devices with focus on the design of key boards, positioning, pointing and drawing, display devices. The Interaction: Models of interaction, ergonomics, interaction styles, elements of WIMP interfaces, interactivity, experience, engagement and fun. Paradigms for Interaction.
II	Design Process: The process of design, user focus, scenarios, navigation design screen design and layout, iteration & prototyping. Usability Engineering Design rules: Principles to support usability, standards, guidelines, rules and heuristics, HCI patterns.
III	Evaluation Techniques: Definition and goals of evaluation, evaluation through expert analysis and user participation, choosing an evaluation method. User support, requirement, approaches, adaptive help systems, designing user support systems
IV	Cognitive methods: Goals and task hierarchies, linguistic models, challenges of display based systems, physical and device models, cognitive architectures.
V	Communications and collaborations models: Face to Face communication, conversations, Text based communication, group working. Task Analysis: Differences between task analysis and other techniques, task decomposition, knowledge based analysis, ER based analysis, sources of information and data collection, use of task analysis.

References:

1. Human Computer Interaction; Alan Dix et.al, 3rd ed., Pearson

6IT7A- JAVA PROGRAMMING LAB (Common to CS & IT)

Class: VI Sem. B.Tech.	**Evaluation**
Branch: I.T. **Schedule per Week** **Practical Hrs.: 3**	**Examination Time = Five (4) Hours** **Maximum Marks = 75** **[Sessional/Mid-term (60) & End-term (40)]**

Objectives: At the end of the semester, the students should have clearly understood and implemented the following:

1. **Develop an in depth understanding of programming in Java**: data types, variables, operators, operator precedence, Decision and control statements, arrays, switch statement, Iteration Statements, Jump Statements, Using break, Using continue, return.
2. **Write Object Oriented programs in Java**: Objects, Classes constructors, returning and passing objects as parameter, Inheritance, Access Control, Using super, final with inheritance Overloading and overriding methods, Abstract classes, Extended classes.
3. **Develop understanding to developing packages & Interfaces in Java:** Package, concept of CLASSPATH, access modifiers, importing package, Defining and implementing interfaces.
4. **Develop understanding to developing Strings and exception handling:** String constructors, special string operations, character extraction, searching and comparing strings, string Buffer class. Exception handling fundamentals, Exception types, uncaught exceptions, try, catch and multiple catch statements. Usage of throw, throws and finally.
5. **Develop applications involving file handling**: I/O streams, File I/O.
6. **Develop applications involving concurrency**: Processes and Threads, Thread Objects, Defining and Starting a Thread, Pausing Execution with Sleep, Interrupts, Joins, and Synchronization.
7. **Develop applications involving Applet:** Applet Fundamentals, using paint method and drawing polygons.

It is expected that each laboratory assignments to given to the students with an aim to In order to achieve the above objectives

Indicative List of exercises:

1. Programs to demonstrate basic concepts e.g. operators, classes, constructors, control & iteration statements, recursion etc. such as complex arithmetic, matrix arithmetic, tower of Hanoi problem etc.
2. Development of programs/projects to demonstrate concepts like inheritance, exception handling, packages, interfaces etc. such as application for electricity department, library management, ticket reservation system, payroll system etc.
3. Development of a project to demonstrate various file handling concepts.
4. Development of a project to demonstrate various applet concepts.

6IT8A- GUI DESIGN LAB

Class: VI Sem. B.Tech.	**Evaluation**
Branch: I.T. **Schedule per Week** **Practical Hrs : 3**	**Examination Time = Four (4) Hours** **Maximum Marks =75** **[Sessional/Mid-term (60) & End-term (40)]**

S. No.	**List of Experiments**
1.	Adding buttons, edit fields, and other child-window components
2.	Implement the CObject debugging ability and Common MFC problems
3.	Implement GDI Functions, and the CDC class (Text, Drawing shapes, Bitmaps)
4.	Implementing View class functions I. Interacting with the user II. Event Handling III. Responding to events from different control types
5.	Implementing View class functions I. GDI Functions, and the CDC class II. Text III. Drawing shapes IV. Bitmaps
6.	Implementing Dialog Block class Creating a Dialog box Invoking and displaying Setting and retrieving values from a dialog box
7.	Implementing Dialog Boxes, Completion Database Classes I. ODBC vs. DAO II. Databases and Record sets III. Queries (filtering and ordering)
8.	Printing and Print Preview I. Database-style reports II Common Dialog interface

6IT9A- UML LAB

Class: VI Sem. B.Tech.	**Evaluation**
Branch: I.T. **Schedule per Week** **Practical Hrs : 2**	**Examination Time = Four (4) Hours** **Maximum Marks = 50** **[Sessional/Mid-term (30) & End-term (20)]**

Objectives:

1. The students shall be able to use following modules of UML for system description, implementation and finally for product development.

   - Capture a business process model.
   - The User Interaction or Use Case Model - describes the boundary and interaction between the system and users. Corresponds in some respects to a requirements model.
   - The Interaction or Communication Model - describes how objects in the system will interact with each other to get work done.
   - The State or Dynamic Model - State charts describe the states or conditions that classes assume over time. Activity graphs describe the workflows the system will implement.
   - The Logical or Class Model - describes the classes and objects that will make up the system.
   - The Physical Component Model - describes the software (and sometimes hardware components) that make up the system.
   - The Physical Deployment Model - describes the physical architecture and the deployment of components on that hardware architecture.

The students are expected to use the UML models, prepare necessary documents using UML and implement a system. Some hardware products like digital clock, digital camera, washing machine controller, air conditioner controller, an elctronic fan regulator, an elementary mobile phone etc. may also be chosen.

The students shall be assigned one problem on software based systems and another involving software as well as hardware

6IT10A- DESIGN AND ANALYSIS OF ALGORITHMS Lab (Common to CS & IT)

Class: VI Sem. B.Tech.	**Evaluation**
Branch: I.T. **Schedule per Week** **Practical Hrs : 3**	**Examination Time = Four (4) Hours** **Maximum Marks = 100** **[Sessional/Mid-term (60) & End-term (40)]**

Objectives: Upon successful completion of this course, students should be able to:

- Prove the correctness and analyze the running time of the basic algorithms for those classic problems in various domains;
- Apply the algorithms and design techniques to solve problems;
- Analyze the complexities of various problems in different domains.

Suggested Tools: For implementation and estimation of running time on various sizes of input(s) or output(s) as the case may be, Linux platform is suggested.

Suggested Exercises:

A. It is expected that teachers will assign algorithms to the students for estimation of time & space complexity. Algorithms reported in various research journals may be chosen by the teachers.

B. Problem on designing algorithms to meet complexity constraints may be assigned. For example, a problem on design, analysis and implementation for transposing a sparse matrix requiring not more than one pass from the original matrix may be assigned.

C. **A guide to such problems is given below:**

1. **Exploring a Binary Heap:** Consider a binary heap containing n numbers (the root stores the greatest number). You are given a positive integer $k < n$ and a number x. You have to determine whether the k^{th} largest element of the heap is greater than x or not. Your algorithm must take $O(k)$ time. You may use $O(k)$ extra storage.

2. **Merging two search trees**: You are given two height balanced binary search trees T and T', storing m and n elements respectively. Every element of tree T is smaller than every element of tree T'. Every node u also stores height of the subtree rooted at it. Using this extra information how can you merge the two trees in time $O(\log m + \log n)$ (preserving both the height balance and the order)?

3. **Complete binary tree as an efficient data-structure:**
   You are given an array of size n (n being a power of two). All the entries of the array are initialized to zero. You have to perform a sequence of the following online operations :
   1. **(i)** *Add(i,x)* which adds x to the entry A[i].

2. **(ii)** Report *sum(i,j)* = sum of the entries in the array from indices *i* to *j* for any *0 < i < j <= n*.

It can be seen easily that we can perform the first operation in *O(*1*)* time whereas the second operation may cost *O(n)* in worst case. Your objective is to perform these operations efficiently. Give a data-structure which will guarantee *O(log n)* time per operation.

4. Problems on Amortized Analysis

a. Delete-min in constant time !!! Consider a binary heap of size *n* , the root storing the smallest element. We know that the cost of insertion of an element in the heap is *O(* log *n)* and the cost of deleting the smallest element is also *O(* log *n)*. Suggest a valid potential function so that the amortized cost of insertion is *O(* log *n)* whereas amortized cost of deleting the smallest element is *O(* 1*)*.

b. Implementing a queue by two stack

c. Show how to implement a queue with two ordinary stacks so that the amortized cost of each Enqueue and each Dequeue operation is O(1).

5. **Computing a spanning tree having smallest value of largest edge weight:** Describe an efficient algorithm that, given an undirected graph *G*, determines a spanning tree of *G* whose largest edge weight is minimum over all spanning trees of *G*.

6. **Shortest Path Problems:**

i. From a subset of vertices to another subset of vertices

a. Given a directed graph G(V,E), where edges have nonnegative weights. S and D are two disjoint subsets of the set of vertices. Give an O(|V| log |V| + |E|) time algorithm to find the shortest path among the set of paths possible from any node in S to any node in D.

ii. Paths in Directed Acyclic Graph

a. Counting the number of paths

Given two nodes *u,v* in a directed acyclic graph *G(V,E)*. Give an *O(|E|)* time algorithm to count all the paths from *u* to *v*.

b. **Path passing through a subset of nodes**

Given two nodes *u,v* and a set of vertices $w_1, w_2,...,w_k$ in a directed acyclic graph *G(V,E)*. Give an *O(|E|)* time algorithm to output a path(if exists) from *u* to *v* which passes through each of the nodes $w_1,...,w_k$. If there is no such path then your algorithm must report that *"no such path exists"*.

7. **Searching for a friend:**

You are standing at a crossing from where there emerge four roads extending to infinity. Your friend is somewhere on one of the four roads. <u>You do not know on which road he is and how far he is from you</u>. You have to walk to your friend and the total distance traveled by you must be at most a constant times the actual distance of your friend from you. In terminology of algorithms, you should traverse *O(d)* distance, where *d* is the distance of your friend from you.

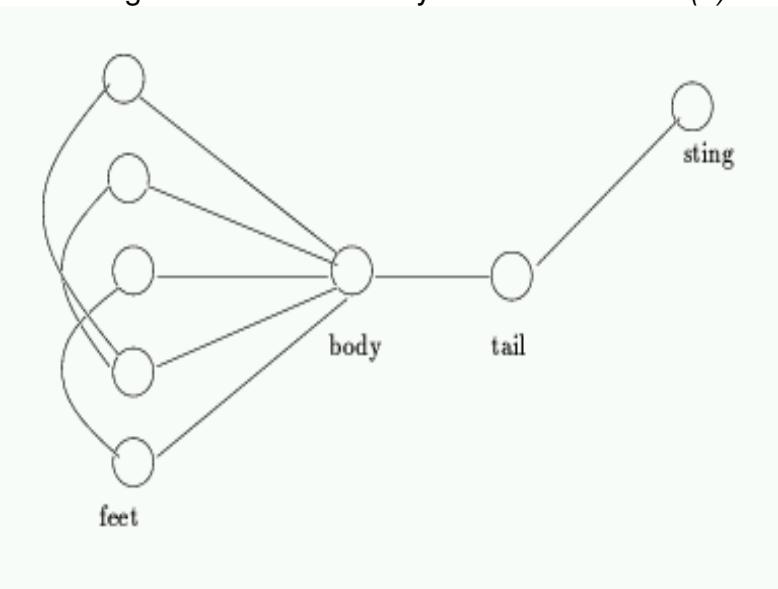
8. **A simple problem on sorted array:** Design an *O(n)*-time algorithm that, given a real number *x* and a sorted array *S* of *n* numbers, determines whether or not there exist two elements in *S* whose sum is exactly *x* .

9. **Finding the decimal dominant in linear time**: You are given *n* real numbers in an array. A number in the array is called a decimal dominant if it occurs more than *n/10* times in the array. Give an *O(n)* time algorithm to determine if the given array has a decimal dominant.

10. **Finding the first one**: You are given an array of infinite length containing zeros followed by ones. How fast can you locate the first one in the array?

11. **Searching for the Celebrity:** Celebrity is a person whom everybody knows but he knows nobody. You have gone to a party. There are total *n* persons in the party. Your job is to find the celebrity in the party. You can ask questions of the form Does Mr. *X* know Mr. *Y* ?. You will get a binary answer for each such question asked. Find the celebrity by asking only *O(n)* questions.

12. **Checking the Scorpion**: An *n*-vertex graph is a *scorpion* if it has a vertex of degree 1(the sting) connected to a vertex of degree two (the tail) connected to a vertex of degree *n-2* (the body) connected to the other *n-3* (the feet). Some of the feet may be connected to other feet. Design an algorithm that decides whether a given adjacency matrix represents a scorpion by examining only *O(n)* entries.



13. **Endless list:** You are having a pointer to the head of singly linked list. The list either terminates at null pointer or it loops back to some previous location(not necessarily to the head of the list). You have to determine whether the list loops back or ends at a null location in time proportional to the length of the list. You can use at most a constant amount of extra storage.

14. **Nearest Common Ancestor**:
    Given a rooted tree of size *n*. You receive a series of online queries: "Give nearest common ancestor of *u, v* ". Your objective is to preprocess the tree in *O(n)* time to get a data structure of size *O(n)* so that you can answer any such query in *O(log n)* time.

6IT11A- Humanities and Social Sciences (Common to CS & IT)

Class: VI Sem. B.Tech.	Evaluation
Branch: Information Technology Schedule per Week Practical: 2	Examination Time = Three (3) Hours Maximum Marks = 50 [Sessional/Mid-term (30) & End-term (20)]

Units	Contents of the subject
I	India-brief history of Indian constitution ,framing-features fundamental rights, duties,directive principles of states, History of Indian National movement,Socio economic growth after independence.
II	Society-Social groups-concepts and types,socialization-concept theory, social control:concept,social problem in contempory India,status and role.
III	The fundamental of Economics-meaning, definition and importance of economics, Logic of choice, central economic problems, positive and normative approaches, economic systems-socialism and capitalism.
IV	Microeconomics-Law of demand and supply, utility approach, indifference curves, elasticity of demand & supply and applications, consumer surplus, Law of returns to factors and returns to scale.
V	Macroeconomics- concept relating to National product National income and its measurement,simple Keynesian theory, simple multiplier, money and banking. Meaning, concept of international trade, determination of exchange rate, Balance of payments.

References:

1. Economics-Lipsey & Chrystal, Oxford Univ.Press,2010
2. Nordhaus, William, Samuelson,Paul-2009-10

7IT1 A- SOFTWARE PROJECT MANAGEMENT

Class: VII Sem. B.Tech.	**Evaluation**
Branch: Info. Tech **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Project Management: The management spectrum, the people, the product, the process, the project, the W^5HH principle, critical practices Metrics for Process and Project: Metrics in the process and project Domains, software measurements, metrics for software quality, integrating metrics within software process, metrics for small organizations, establishing a software metrics program.
II	Estimation: Observations, Project planning Process, software scope and feasibility, resources, software project estimation, decomposition techniques, empirical estimation models, estimation for object oriented projects, estimation for Agile development and web engineering projects, the make/buy decision.
III	Project Scheduling: Basic concepts, project scheduling, defining a task set and task network, scheduling, earned value analysis. Risk Management: Reactive V/S proactive Risk Strategies, software risks, Risk identification, Risk projection, risk refinement, risk mitigation, monitoring and management, the RMMM plan **Quality Planning.**: Quality Concepts, Procedural Approach to Quality Management, Quantitative Approaches to Quality Management, Quantitative Quality Management Planning, Setting the Quality Goal, Estimating Defects for Other Stages, Quality Process Planning, Defect Prevention Planning.
IV	Quality Management: Quality Concepts, Software Quality assurances, software reviews, formal technical reviews, Formal approaches to SQA, Statistical Software Quality assurances, Change Management: software Configuration Management, The SCM repository, SCM Process, Configuration Management for Web Engineering
V	**Project Execution And Closure:** **Reviews.** The Review Process, Planning, Overview and Preparation, Group Review Meeting, Rework and Follow-up, One-Person Review, Guidelines for Reviews in Projects, Data Collection, Analysis and Control Guidelines, Introduction of Reviews and the NAH Syndrome. **Project Monitoring and Control:** Project Tracking, Activities Tracking, Defect Tracking, Issues Tracking, Status Reports, Milestone Analysis, Actual Versus Estimated Analysis of Effort and Schedule, Monitoring Quality, Risk-Related Monitoring. **Project Closure:** Project Closure Analysis, The Role of Closure Analysis, Performing Closure Analysis.

References:

1. R. S. Pressman, Software Engineering
2. Pankaj Jalote, Software project management in practice, Addison-Wesley
3. B. Hughest & M. Cotterell, Software Project Management.

7 IT 2 A- INFORMATION SYSTEM SECURITY (Common to CS & IT)

Class: VII Sem. B.Tech.	Evaluation
Branch: Info. Tech **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Introduction to security attacks, services and mechanism, classical encryption techniques- substitution ciphers and transposition ciphers, cryptanalysis, stream and block ciphers. Modern Block Ciphers: Block ciphers principals, Shannon's theory of confusion and diffusion, fiestal structure, data encryption standard(DES), differential and linear cryptanalysis of DES, block cipher modes of operations, triple DES.
II	AES, RC6, random number generation. S-box theory: Boolean Function, S-box design criteria, Bent functions, Propagation and nonlinearity, construction of balanced functions, S-box design.
III	Public Key Cryptosystems: Principles of Public Key Cryptosystems, RSA Algorithm, security analysis of RSA, Exponentiation in Modular Arithmetic. Key Management in Public Key Cryptosystems: Distribution of Public Keys, Distribution of Secret keys using Public Key Cryptosystems. X.509 Discrete Logarithms, Diffie-Hellman Key Exchange.
IV	Message Authentication and Hash Function: Authentication requirements, authentication functions, message authentication code, hash functions, birthday attacks, security of hash functions and MAC, MD5 message digest algorithm, Secure hash algorithm(SHA). Digital Signatures: Digital Signatures, authentication protocols, digital signature standards (DSS), proof of digital signature algorithm. Remote user Authentication using symmetric and Asymmetric Authentication
V	Pretty Good Privacy. IP Security: Overview, IP Security Architecture, Authentication Header, Encapsulation Security Payload in Transport and Tunnel mode with multiple security associations (Key Management not Included). Strong Password Protocols: Lamport's Hash, Encrypted Key Exchange.

Text/References:

1. Stalling Williams: Cryptography and Network Security: Principles and Practices, 4th Edition, Pearson Education, 2006.
2. Kaufman Charlie et.al; Network Security: Private Communication in a Public World, 2nd Ed., PHI/Pearson.
3. Pieprzyk Josef and et.al; Fundamentals of Computer Security, Springer-Verlag, 2008.
4. Trappe & Washington, Introduction to Cryptography, 2nd Ed. Pearson.

7IT3 A- DATA MINING & WARE HOUSING (Common to CS & IT)

Class: VII Sem. B.Tech.	Evaluation
Branch: Info. Tech Schedule per Week Lectures: 3	Examination Time = Three (3) Hours Maximum Marks = 100 [Mid-term (20) & End-term (80)]

Units	Contents of the subject
I	Overview, Motivation(for Data Mining),Data Mining-Definition & Functionalities, Data Processing, Form of Data Preprocessing, Data Cleaning: Missing Values, Noisy Data, (Binning, Clustering, Regression, Computer and Human inspection), Inconsistent Data, Data Integration and Transformation. Data Reduction:-Data Cube Aggregation, Dimensionality reduction, Data Compression, Numerosity Reduction, Clustering, Discretization and Concept hierarchy generation.
II	Concept Description: Definition, Data Generalization, Analytical Characterization, Analysis of attribute relevance, Mining Class comparisons, Statistical measures in large Databases. Measuring Central Tendency, Measuring Dispersion of Data, Graph Displays of Basic Statistical class Description, Mining Association Rules in Large Databases, Association rule mining, mining Single-Dimensional Boolean Association rules from Transactional Databases– Apriori Algorithm, Mining Multilevel Association rules from Transaction Databases and Mining Multi- Dimensional Association rules from Relational Databases.
III	What is Classification & Prediction, Issues regarding Classification and prediction, Decision tree, Bayesian Classification, Classification by Back propagation, Multilayer feed-forward Neural Network, Back propagation Algorithm, Classification methods K-nearest neighbour classifiers, Genetic Algorithm. Cluster Analysis: Data types in cluster analysis, Categories of clustering methods, Partitioning methods. Hierarchical Clustering- CURE and Chameleon. Density Based Methods-DBSCAN, OPTICS. Grid Based Methods- STING, CLIQUE. Model Based Method –Statistical Approach, Neural Network approach, Outlier Analysis
IV	Data Warehousing: Overview, Definition, Delivery Process, Difference between Database System and Data Warehouse, Multi Dimensional Data Model, Data Cubes, Stars, Snow Flakes, Fact Constellations, Concept hierarchy, Process Architecture, 3 Tier Architecture, Data Mining.
V	Aggregation, Historical information, Query Facility, OLAP function and Tools. OLAP Servers, ROLAP, MOLAP, HOLAP, Data Mining interface, Security, Backup and Recovery, Tuning Data Warehouse, Testing Data Warehouse.

Text Books & References:

1. Data Warehousing in the Real World – Anahory and Murray, Pearson Education.
2. Data Mining – Concepts and Techniques – Jiawai Han and Micheline Kamber.
3. Building the Data Warehouse – WH Inmon, Wiley.

7 IT 4 A- INTERNET PROGRAMMING

Class: VII Sem. B.Tech.	**Evaluation**
Branch: Info. Tech **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Introduction , Editing XHTML , First XHTML Example , W3C XHTML Validation service , Headings, Linking , Images , Special Characters and horizontal rules, Lists, Tables, forms, Internet linking, web resources. Cascading Style Sheets Introduction , Inline Styles, Embedded Style Sheets, Conflicting Styles , Linking External Style Sheets, Positioning Elements , Backgrounds , Element Dimensions , Box Model and Text Flow Media types, Building a CSS drop-down menu, User Style Sheets ,CSS3, Web Resources
II	JavaScript: Introduction to Scripting, Control Structures, Functions, Arrays, Objects, and Document object model (DOM): Objects and Collections, Events. XML and RSS: Introduction, XML basics, structuring data, XML namespaces, document type definitions (DTDs), W3C XML schema documents ,XML vocabularies, Extensible style sheet language and XSL transformations, Document object model(DOM),RSS
III	Ajax-enabled rich internet applications: introduction , traditional web applications vs Ajax application , rich internet application (RIAs)with Ajax, history of Ajax, "Raw" Ajax example using the XMLHttpRequest object , using XML and the DOM, creating a full-scale Ajax –enabled application ,dojo toolkit Web Servers (IIS and Apache): introduction, HTTP transactions , multi tier application architecture ,client-side scripting versus server-side scripting ,accessing web servers, Microsoft internet information services(IIS), Apache HTTP server, requesting documents.
IV	PHP: Introduction, PHP basics, string processors and regular expressions , form processing and business logic, connecting to a database, using cookies, dynamic content, operator precedence chart ASP.NET 2.0 and ASP.NET Ajax: introduction, creating and running a simple web form example, web controls, session tracking case study : connecting to a database in ASP.NET
V	Java Server Faces Web applications: introduction, java web technologies, creating and running a simple application in NetBeans, JSF components , session tracking

References

1. Internet & WWW, How to program, DEITEL P.J., H.M., Prentice Hall

7 IT 5 A- COMPUTER GRAPHICS & MULTIMEDIA TECHNIQUES

Class: VII Sem. B.Tech.	**Evaluation**
Branch: Info. Tech **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Introduction to Raster scan displays, Storage tube displays, refreshing, flicking, interlacing, color monitors, display processors, resolution, Introduction to Interactive. Computer Graphics: Picture analysis, Overview of programmer's model of interactive graphics, Fundamental problems in geometry. Scan Conversion: point, line, circle, ellipse polygon, Aliasing, and introduction to Anti Aliasing (No anti aliasing algorithm).
II	2D & 3D Co-ordinate system**:** Homogeneous Co-ordinates, Translation, Rotation, Scaling, Reflection, Inverse transformation, Composite transformation. Polygon Representation, Flood Filling, Boundary filling. Point Clipping, Cohen-Sutherland Line Clipping Algorithm, Polygon Clipping algorithms.
III	Hidden Lines & Surfaces: Image and Object space, Depth Buffer Methods, Hidden Facets removal, Scan line algorithm, Area based algorithms. Curves and Splines: Parametric and Non parametric Representations, Bezier curve, B-Spline Curves.
IV	Rendering: Basic illumination model, diffuse reflection, specular reflection, phong shading, Gourand shading, ray tracing, color models like RGB, YIQ, CMY, HSV
V	Multimedia components, Multimedia Input/Output Technologies: Storage and retrieval technologies, Architectural and telecommunication considerations. Animation: Introduction, Rules, problems and Animation techniques.

References

1. J. Foley, A. Van Dam, S. Feiner, J. Hughes: Computer Graphics- Principles and Practice, Addison Wesley.
2. D. Hearn and Baker: Computer Graphics, PHI
3. Multimedia Systems Design, Prabhat Andleigh and Thakkar, PHI.
4. Multimedia Information Networking, N.K.Sharda, PHI.

7 IT 6.1 A- ADVANCED DATA BASE MANAGEMENT SYSTEMS

Class: VII Sem. B.Tech.	**Evaluation**
Branch: Info. Tech **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Query Processing and Optimization – Measures of query cost, Implementation of database operations, External Sorting, Materialization vs. Pipelining, Size Estimations of various database operations, Evaluation plans, Cost-based vs. Heuristic optimization, Materialized views.
II	Transaction Processing and Serializability – Database Transaction and its processing, Transaction properties, Concurrent Transactions, Need of Serializability, Conflict vs. View Serializability.
III	Concurrency and Recovery – Implementation of Concurrency: Lock-based protocols and Timestamp-based protocols, Deadlock handling, Database Failures, Recovery Schemes: Shadow Paging and Log-based Recovery, Recovery with Concurrent transactions.
IV	Database Security and Advanced SQL – Database Integrity Constraints, Assertions and Triggers in SQL, Authorization and Authentication in SQL, Nested Subqueries, Views, and Embedded SQL.
V	Distributed Database Systems – Data Storage in Distributed systems, Local vs. Global Transactions, Transaction processing, Concurrency and Recovery in Distributed database systems, Distributed Query processing.

References

1. Elmasri R and Navathe SB, Fundamentals of Database Systems, 3rd Edition, Addison Wesley, 2000.
2. Connolly T, Begg C and Strachan A, Database Systems, 2nd Edition, Addison Wesley, 1999
3. Ceri Pelagatti , Distributed Database: Principles and System - (McGraw Hill)
4. Simon AR, Strategic Database Technology: Management for the Year 2000, Morgan Kaufmann, 1995
5. Gray J and Reuter A, Transaction Processing: Concepts and Techniques, Morgan Kaufmann, 1993

7 IT 6.2 A- Intelligent Systems

Class: VIII Sem. B.Tech.	**Evaluation**
Branch: Info. Tech **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Introduction to AI: knowledge Importance of AI, Knowledge Base System, Knowledge organization & manipulation, LISP and other AI programming Languages.
II	Knowledge Representation: Syntax Semantics, Inference Rules, Non-deductive Inference methods, representations using rules, Fuzzy Logic & Natural language computations. Probabilistic Reasoning. Object Oriented Representations.
III	Knowledge Organization & Manipulation: Search & control strategies, matching techniques, knowledge organization & management.
IV	Knowledge Systems Architecture: Rule based, non-production, uncertainty knowledge system building tools.
V	Knowledge Acquisition: General concepts, learning by induction.

References:

1. Artificial Intelligence: Elaine Rich, Kevin Knight, Mc-Graw Hill.
2. Introduction to AI & Expert System: Dan W. Patterson, PHI.
3. Artificial Intelligence by Luger (Pearson Education)
4. Russel & Norvig, Artificial Intelligence: A Modern Approach, Prentice-Hall

7IT 6.3 A- SPEECH PROCESSING

Class: VII Sem. B.Tech.	**Evaluation**
Branch: Info. Tech **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	**The speech chain**: current capabilities in synthesis and recognition. Acoustic phonetics. Vocal tract physiology: voiced excitation, unvoiced excitation (bursts, frication). Acoustics of uniform tubes, of two- and three-tube models. Comparison to speech data.
II	**Synthesis**: Formant synthesis (series, parallel), Articulatory synthesis, Concatenative Synthesis, Text-to-Speech (normalisation, linguistic units, rules). Articulatory parameters, shape-to-sound transformation, vocal tract imaging, revising the acoustic model.
III	**Letter-sound relations**, phonology; prosody, intelligibility, quality assessment. Ear physiology. Auditory perception. Speech perception.
IV	**Recognition**: Template matching. (Training, distance measures, dynamic time warping), Stochastic models. (Hidden Markov models, Baum-Welch and Forward-Backward algorithms). Large-Vocabulary Recognition. (Phonemic baseforms, language models), Artificial Neural Networks. (Overview, hybrid systems).
V	**Assessing recognition performance**; improving recognition performance; Knowledge-based approaches, auditory models.

References:

1. J N Holmes and W. Holmes, Speech Synthesis and Recognition, 2nd ed., Taylor and Francis, 2001.
2. B. Gold and N. Morgan, Speech and Audio Signal Processing, Wiley and Sons, 2000.
3. G. Childers, Speech Processing and Synthesis Toolboxes, Wiley and Sons, 2000.
4. J. R. Deller, J. R. Proakis, J. H. L. Hansen, Discrete-Time Processing of Speech Signals, Prentice-Hall 1993.
5. P. B. Denes and E. N. Pinson, The Speech Chain, W. H. Freeman & Co 1993.
6. S Furui, Digital Speech Processing, Synthesis and Recognition, Marcel Dekker Inc 1989.
7. D O'Shaughnessy, Speech Communications: Human & Machine, IEEE Press 1999.
8. L R Rabiner and R W Schafer, Digital Processing of Speech Signals, Prentice-Hall 1978.
9. K. N. Stevens, Acoustic Phonetics, MIT

7IT7A- COMPUTER GRAPHICS & MULTIMEDIA LAB

Class: VII Sem. B.Tech.	Evaluation
Branch: Info. Tech. Schedule per Week Practical Hrs.: 2	Examination Time = Four (4) Hours Maximum Marks = 50 [Sessional /Mid-term (30) & End-term (20)]

Objectives:

At the end of the semester, the students should have clearly understood and implemented the following:

1. To produce a single pixel and pre specified pattern on screen:
2. To implement features like changing background color, foreground color, resizing of window, repositioning of window:
3. To implement mid point algorithm to draw circle and ellipse:
4. Use the line drawing & circle drawing programs to draw composite objects containing only circle & lines. You can take shapes like a cart, car etc.
5. To Implement Clipping (various algorithms).
6. Simple fonts, graphical fonts, scalable fonts.
7. Input a polynomial by drawing lines, use appropriate methods for filling and filling convex & concave polynomials.

It is expected that each laboratory assignments to given to the students with an aim to In order to achieve the above objectives

Suggested Platform/Tools:

1. For this lab, the students can choose any platform either Microsoft Windows or Linux.
2. Compilers & Libraries: Microsoft Platform- Visual Studio.Net, Linux – Xlib.
3. No turbo C/C++. No library function except the one required to put a single pixel on the screen.

Indicative List of Experiments:

1. Programs to produce a single pixel produce a pre specified pattern with features like changing background color, foreground color, resizing of window, repositioning of window must be demonstrated.
2. Use Mid Point algorithm to draw line between two points. The program must be independent of the slope i.e. lines of all slopes must be drawn.
3. Use Mid Point algorithm to draw ellipse. Implement circle drawing as a special case of ellipse. Extend this to draw arcs between points.
4. Programs to draw composite objects containing circles & lines, drawing lines thicker than one pixel, you can take shapes like a cart, car etc.
5. Programs to demonstrate text generation e.g. simple fonts, graphical fonts, and scalable fonts.
6. Programs to demonstrate filling algorithms eg. filling convex & concave polynomials. The program must be able to (i) input a polynomial by drawing lines (ii) determine whether convex or concave (iii) use appropriate methods for filling.
7. Programs to demonstrate clipping algorithms eg. program to clip a (i) line and (ii) polygon using Cohen-Sutherland Clipping algorithm(s), clipping lines, circles against a rectangular clip area.
8. Programs to demonstrate presentation of geometrical objects e.g.circle and rectangle with audio description i.e. size, color of boundary and interior etc. played synchronously one after another.

7 IT 8 A- DMW Lab

Class: VII Sem. B.Tech.	**Evaluation**
Branch: I.T. **Schedule per Week** **Practical Hrs : 3**	**Examination Time = Four (4) Hours** **Maximum Marks = 100** **[Sessional/Mid-term (60) & End-term (40)]**

Objectives:

1. The students shall be able to use following modules of UML for system description, implementation and finally for product development.

   - Capture a business process model.
   - The User Interaction or Use Case Model - describes the boundary and interaction between the system and users. Corresponds in some respects to a requirements model.
   - The Interaction or Communication Model - describes how objects in the system will interact with each other to get work done.
   - The State or Dynamic Model - State charts describe the states or conditions that classes assume over time. Activity graphs describe the workflows the system will implement.
   - The Logical or Class Model - describes the classes and objects that will make up the system.
   - The Physical Component Model - describes the software (and sometimes hardware components) that make up the system.
   - The Physical Deployment Model - describes the physical architecture and the deployment of components on that hardware architecture.

The students are expected to use the UML models, prepare necessary documents using UML and implement a system. Some hardware products like digital clock, digital camera, washing machine controller, air conditioner controller, an elctronic fan regulator, an elementary mobile phone etc. may also be chosen.

The students shall be assigned one problem on software based systems and another involving software as well as hardware.

7 IT 9 A- INTERNET PROGRAMMING LAB

Class: VII Sem. B.Tech.	**Evaluation**
Branch: I.T. **Schedule per Week** **Practical Hrs : 2**	**Examination Time = Four (4) Hours** **Maximum Marks = 50** **[Sessional/Mid-term (30) & End-term (20)]**

Objectives: At the end of the semester, the students should have clearly understood and implemented the following:

1. Develop basic understanding of HTML script: overview of HTML, basic HTML tags, title, head and body.
2. Write web pages in HTML: formatting text in HTML, inserting photographs on the page, drawing tables, creating hyperlinks-internal and external, creating hyperlinks of external web sites.
3. Develop understanding of creating standard view of web site: displaying multiple pages over a single page, displaying it as standard view like header and footer, creating standard text formatting over the web site.
4. Develop understanding common formation over a web site: creating and using css, understanding importance of common text formatting over a website.
5. Develop understanding of server side scripting language: basic concepts of scripting language, client side and server side scripting, introduction to php, variable, control statements, loops .
6. Develop applications using php and MySQL: using php to access database, mysql database selection, create, update and delete script in php.

It is expected that each laboratory assignments to given to the students with an aim to In order to achieve the above objectives

Indicative List of Experiments:

1. Develop a static html page using style sheet to show your own profile. Add pages one by one to show 5 photos, to show your academics in tabular format, a page containing 5 links to your favorite website, navigational links to all above pages (menu), header, footer, left-sidebar, right sidebar etc.
2. Use Cascading Style Sheets to format your all pages in a common format.
3. Write a simple "hello word" program using php.
4. Write a program to accept two strings (name and age) from user. Print welcome statement e.g. “Hi Ram, your age is 24."
5. Write a program to create a calculator, which can support addition, subtraction, multiply and division operations.
6. Write a program to take input parameters for a table (no. of rows and no. of columns) and create the desired table.
7. Create a "Contact Me" page -Ask user to enter his name, email ID, Use Java-Script to verify entered email address. Store submitted value in a MySql database. Display latest 5 submitted records in contact me page. Display above record with navigation support. e.g. (next, previous, first, last)

8 IT 1 A- Software Testing & Validation

Class: VIII Sem. B.Tech.	Evaluation
Branch: Information Technology **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	**Software verification and validation**-introduction, verification, methods of verification, validation, level of validation, principle of testing, context of texting in producing software, white box testing- definition, static testing, structural testing, black box testing
II	**Integration Testing-**Scenario Testing, defect bash, system and acceptance testing, functional, non-functional testing, performance testing, methodology, tools and process
III	**Regression Testing, internationalization**-Introduction, test phases of internationalization testing, enabling testing, locale testing, language testing ,localization testing ,ad-hoc testing-overview, buddy testing, pair testing, explainatory testing, iterative testing agile and extreme testing.
IV	**Testing of object oriented systems**- Introduction, primer on object-oriented software, Differences in OO testing. **Usability and Accessibility Testing-** what is usability testing, approach to usability,when to do usability testing, how to achieve usability, quality factors for usability, accessibility testing, tools for usability.
V	Test planning, Test management, Test process and reporting,Software test automation, design and architecture of automation, process model for automation, test matrices and measurement- type of metrics, project metrics, productivity metrics, progress metrics, release metrics

8IT2 A- Digital Image Processing (Common to CS & IT)

Class: VIII Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	**Introduction to Image Processing:** Digital Image representation, Sampling & Quantization, Steps in image Processing, Image acquisition, color image representation
II	**Image Transformation & Filtering:** Intensity transform functions, histogram processing, Spatial filtering, Fourier transforms and its properties, frequency domain filters, colour models, Pseudo colouring, colour transforms, Basics of Wavelet Transforms
III	**Image Restoration:** Image degradation and restoration process, Noise Models, Noise Filters, degradation function, Inverse Filtering, Homomorphism Filtering
IV	**Image Compression:** Coding redundancy, Interpixel redundancy, Psychovisual redundancy, Huffman Coding, Arithmetic coding, Lossy compression techniques, JPEG Compression
V	**Image Segmentation & Representation:** Point, Line and Edge Detection, Thresholding, Edge and Boundary linking, Hough transforms, Region Based Segmentation, Boundary representation, Boundary Descriptors, Regional

Suggested Readings/Books

1. Gonzalez and Woods: Digital Image Processing ISDN 0-201-600- 781, Addison Wesley 1992.

Boyle and Thomas: Computer Vision - A First Gurse 2nd Edition, ISBN 0-632-028-67X, Blackwell

Science 1995.

2. Gonzalez and Woods: Digital Image Processing ISDN 0-201-600- 781, Addison Wesley 1992.

3. Pakhera Malay K: Digital Image Processing and Pattern Recogination, PHI.

4. Trucco&Verri: Introductory Techniques for 3-D Computer Vision, Prentice Hall, Latest Edition

5. Low: Introductory Computer Vision and Image Processing, McGraw-Hill 1991, ISBN 0-07-707403-3.

8 IT3A- Data Compression Techniques

Class: VII Sem. B.Tech.	Evaluation
Branch: Computer Engg. **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Compression Techniques: Lossless, lossy, measure of performance, modeling & coding. Lossless compression: Derivation of average information, data models, uniquely decodable codes with tests, prefix codes, Kraft-Mc Millan inequality. Huffman coding: Algorithms, minimum variance Huffman codes, optimality, length extended codes, adaptive coding, Rice codes, using Huffman codes for lossless image compression.
II	Arithmetic coding with application to lossless compression. Dictionary Techniques: LZ77, LZ78, LZW Predictive coding: Burrows-Wheeler Transform and move-to-front coding, JPEG-LS Facsimile Encoding: Run length, T.4 and T.6
III	Lossy coding- Mathematical preliminaries: Distortion criteria, conditional entropy, average mutual information, differential entropy, rate distortion theory, probability and linear system models. Scalar quantization: The quantization problem, uniform quantizer, Forward adaptive quantization, non-uniform quantization-Formal adopting quantization, companded Quantization Vector quantization: Introduction, advantages, The Linde-Ruzo-Grey algorithm, lattice vector quantization.
IV	Differential encoding – Introduction, Basic algorithm, Adaptive DPCM, Delta modulation, speech and image coding using delta modulation. Sampling in frequency and time domain, z-transform, DCT, DST, DWHT, quantization and coding of transform coefficient.
V	Sub band coding: Introduction, Filters, Basic algorithm, Design of Filter banks, G.722, MPEG. Wavelet based compression: Introduction, wavelets multi-resolution analysis and the scaling function implementation using filters.

Text Books & References:

1. Sayood K: Introduction to Data Compression: ELSEVIER 2005.

8 IT 4.1 A- MOBILE COMPUTING (Common to CS & IT)

Class: VIII Sem. B.Tech.	**Evaluation**
Branch: Info. Tech **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	**Contents of the subject**
I	Mobile computing: Definitions, adaptability issues (transparency, Environmental Constraints, application aware adaptation), mechanisms for adaptation and incorporating adaptations. Mobility management: mobility management, location management principle and techniques, PCS location management Scheme.
II	Data dissemination and management: challenges, Data dissemination, bandwidth allocation for publishing, broadcast disk scheduling, mobile cache maintenance schemes, Mobile Web Caching. Introduction to mobile middleware.
III	Middleware for application development: adaptation, Mobile agents. Service Discovery Middleware: Service Discovery & standardization Methods (universally Unique Identifiers, Textual Description & using interfaces), unicast Discovery, Multicast Discovery & advertisement, service catalogs, Garbage Collection, Eventing.
IV	Mobile IP, Mobile TCP, Database systems in mobile environments, World Wide Web and mobility
V	Ad Hoc networks, localization, MAC issues, Routing protocols, global state routing (GSR), Destination sequenced distance vector routing (DSDV), Dynamic source routing (DSR), Ad Hoc on demand distance vector routing (AODV), Temporary ordered routing algorithm (TORA), QoS in Ad Hoc Networks, applications.

References:

1. Frank Adelstein, Sandeep Gupta, Golden Richard III, Loren Schwiebert, Fundamentals of Mobile and Pervasive Computing, TMH.
2. Principles of mobile computing Hansmann & Merk., Springer
3. Mobile communications Jochen Schiller , Pearson
4. 802.11 wireless networks Matthew S.Gast, O'REILLY.
5. Wireless LANs: Davis & McGuffin, McGraw Hill
6. Mobile Communications Handbook by Jerry D. Gybson
7. Mobile Communications Handbook by Raymond Steel

8 IT 4.2 A- Information Retrieval (Common to CS & IT)

Class: VIII Sem. B.Tech.	Evaluation
Branch: Info. Tech **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Units	Contents of the subject
I	Knowledge Representation: Knowledge representation, Basics of Prepositional logic, Predicate logic, reasoning using first order logic, unification, forward chaining, backward chaining, resolution Production rules, frames, semantic networks scripts.
II	Ontology Development: Description logic-taxonomies, Topic maps Ontology, Definition expressing ontology, logically ontology representations, – XML, RDF, RDFS, OWL, OIL, ontology development for specific domain, ontology engineering, Semantic web services.
III	Information Retrieval Modeling: Information retrieval, taxonomy, formal characterization, classic information retrieval, set theoretic model, algebraic model, probabilistic model, structured text, retrieval models, models for browsing, retrieval performance evaluation, keyword based querying, pattern matching, structural queries, query operations.
IV	Text and Multimedia Languages and Properties: Introduction, metadata, markup languages, multimedia. Text operations: document preprocessing, document clustering text Compressionbasic concepts - statistical methods. Indexing and searching: inverted files, suffix trees, signature file,Booleanqueries, sequential searching, pattern matching.
V	Recent Trends in IR: Parallel and distributed IR, multimedia IR, data modeling, query languages, A generic Multimedia indexing Approach, one dimensional time series, two dimensional color

	images, Automatic feature extraction. Web Searching, Characterizing the Web, Search Engines, Browsing, Meta searchers, Searching using hyperlinks

TEXT BOOKS

1. Stuart Russell and Peter Norvig, "Artificial Intelligence – A Modern Approach", Pearson Education, Second edition, 2003. (UNIT I)

2. Michael C. Daconta, Leo J. Obart and Kevin J. Smith,"Semantic Web – A Guide to the Future of XML, Web Services and Knowledge Management", Wiley Publishers, 2003 (UNIT II)

3. Ricardo Baeza-Yates, BerthierRibeiro-Neto, "Modern Information Retrieval", Addison Wesley, 1999. (UNITs III, IV & V)

REFERENCES

1. Elain Rich and Kevin Knight, "Artificial Intelligence", Tata McGraw-Hill, Third edition, 2003

2. Christopher D. Manning, PrabhakarRaghavan and HinrichSchutze, "Introduction to Information Retrieval", Cambridge University Press, 2008.

8 IT 4.3A- Robotics

Class: Vlll Sem. B.Tech.	Evaluation
Branch: Info. Tech **Schedule per Week** **Lectures: 3**	**Examination Time = Three (3) Hours** **Maximum Marks = 100** **[Mid-term (20) & End-term (80)]**

Unit	Contents
I	Introduction -- brief history, types, classification and usage, Science and Technologyof robots, Some useful websites, textbooks and research journals.
II	Elements of robots -- joints, links, actuators, and sensorsPosition and orientation of a rigid body, Homogeneous transformations,Representation of joints, link representation using D-H parameters, Examples of D-Hparameters and link transforms, different kinds of actuators – stepper, DC servo andbrushless motors, model of a DC servo motor, Types of transmissions, Purpose ofsensors, internal and external sensors, common sensors – encoders, tachometers,strain gauge based force-torque sensors
III	Introduction, Direct and inverse kinematics problems, Examples of kinematics of common serial manipulators, workspace of a serial robot, Inverse kinematics of constrained and redundant robots, Tractrix based approach for fixed and free robots and multi-body systems, simulations and experiments, Solution procedures usingtheory of elimination, Inverse kinematics solution for the general 6R serialmanipulator.
IV	Degrees-of-freedom of parallel mechanisms and manipulators, Active and passivejoints, Constraint and loop-closure equations, Direct kinematics problem, Mobility ofparallel manipulators, Closed-from and numerical solution, Inverse kinematics ofparallel manipulators and mechanisms, Direct kinematics of Gough-Stewart platform.
V	Linear and angular velocity of links, Velocity propagation, Manipulator Jacobians forserial and parallel manipulators, Velocity ellipse and ellipsoids, Singularity analysisfor serial and parallel manipulators, Loss and gain of degree of freedom, Statics ofserial and parallel manipulators, Statics and force transformation matrix of a Gough-Stewart platform, Singularity analysis and statics.

TEXT BOOKS

1. Mittal and Nagrath, Robotics and Control, Tata McGraw-Hill Education, 2003.

2. Fred G. Martin, Robotic Explorations: A Hands On Introduction to Engineering, Pearson Education, 2001.

8IT5A- Software Testing Lab

Class: VIII Sem. B.Tech.	**Evaluation**
Branch: I.T. **Schedule per Week** **Practical Hrs.: 3**	**Examination Time = Four (4) Hours** **Maximum Marks = 100** **[Sessional/Mid-term (60) & End-term (40)]**

S. No.	**List of Experiments**
1	Hands on Software Engineering principles Infrastructure.
2.	usage of Front-end and Back-end technologies and packages Prepare the following documents for three of the experiments listed below using software engineering methodology. 1. Program Analysis and Project Planning. 2. Thorough study of the problem – Identify project scope, Objectives, 3. Software requirement Analysis
3	Describe the individual Phases / Modules of the project, Identify deliverables
4	Software Design a. Use work products – Data dictionary, Use case diagrams and activity diagrams, build and test class diagrams, b. Sequence diagrams and add interface to class diagrams, DFD, ER diagrams c. Software Development and Debugging using any Front end and Back end tool d. Software Verification and Validation procedures

8IT6A- Digital Image Processing lab (Common to CS & IT)

Class: VIII Sem. B.Tech.	Evaluation
Branch: Info. Tech **Schedule per Week** **Practical Hrs:2**	**Examination Time = Three (3) Hours** **Maximum Marks =50** **[Mid-term (30) & End-term (20)]**

S. No.	List of Experiment
1	Color image segmentation algorithm development
2	Wavelet/vector quantization compression
3	Deformable templates applied to skin tumor border finding
4	Helicopter image enhancement
5	High-speed film image enhancement
6	Computer vision for skin tumor image evaluation
7	New Border Images

8IT7A- Advance Web Programming

Class: VIIl Sem. B.Tech.	Evaluation
Branch: Info. Tech **Schedule per Week** **Practical Hrs: 2**	**Examination Time = Three (3) Hours** **Maximum Marks = 50** **[Mid-term (60) & End-term (40)]**

S. No.	List of Experiment
1	. Creation of HTML Files
2	Working with Client Side Scripting : VBScript, JavaScript
3	Configuration of web servers: Apache Web Server, Internet Information Server (IIS)
4	Working with ActiveX Controls in web documents
5	Experiments in Java Server Pages: Implementing MVC Architecture using Servlets, Data Access Programming (using ADO), Session and Application objects, File System Management
6	Working with other Server Side Scripting: Active Server Pages, Java Servlets, PHP
7	Experiments in Ajax Programming
8	Developing Web Services
9	Developing any E-commerce application (Mini Project)
10	Application Development in cloud computing Environment
11	Experiment Using Open Source Tool e.g. ANEKA

8IT8A- MOBILE APPLICATION DEVELOPMENT LAB

Class: VIII Sem. B.Tech.	Evaluation
Branch: Info. Tech **Schedule per Week** **Practical Hrs:2**	**Examination Time = Three (3) Hours** **Maximum Marks = 50** **[Mid-term (30) & End-term (20)]**

S. No.	List of Experiment
1	Installation of Java wireless toolkit (J2ME)
2	Working with J2ME Features
3	Threads and High Level UI
4	Working on drawing and images
5	Developing Networked Applications using the wireless toolkit
6	Authentication with a web server

Scheme and Syllabus

Rajasthan Technical University, Kota

B. Tech. in Mechanical Engineering

(Session 2018-19)

Scheme of
UNDERGRADUATE DEGREE COURSE

Mechanical Engineering

Rajasthan Technical University, Kota
Effective from session: 2018 – 2019

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Teaching & Examination Scheme

B.Tech. : Mechanical Engineering
2nd Year - III Semester

THEORY												
SN	**Category**	**Course**		**Contact hrs/week**			**Marks**				**Cr**
		Code	**Title**	**L**	**T**	**P**	**Exm Hrs**	**IA**	**ETE**	**Total**	
1	BSC	3ME2-01	Advance Engineering Mathematics-I	3	0	0	3	30	120	**150**	**3**
2	HSMC	3ME1-02/ 3ME1-03	Technical Communication/ Managerial Economics and Financial Accounting	2	0	0	2	20	80	**100**	**2**
3	ESC	3ME3-04	Engineering Mechanics	2	0	0	2	20	80	**100**	**2**
4	PCC	3ME4-05	Engineering Thermodynamics	3	0	0	3	30	120	**150**	**3**
5		3ME4-06	Materials Science and Engineering	3	0	0	3	30	120	**150**	**3**
6		3ME4-07	Mechanics of Solids	3	1	0	3	40	160	**200**	**4**
			Sub Total	16	1	0		170	680	**850**	**17**
PRACTICAL & SESSIONAL											
7	PCC	3ME4-21	Machine drawing practice	0	0	3		45	30	**75**	**1.5**
8		3ME4-22	Materials Testing Lab	0	0	3		45	30	**75**	**1.5**
9		3ME4-23	Basic Mechanical Engineering Lab	0	0	3		45	30	**75**	**1.5**
10		3ME4-24	Programming using MATLAB	0	0	3		45	30	**75**	**1.5**
11	PSIT	3ME7-30	Industrial Training	0	0	1		0	0	**50**	**1**
12	SODECA	3ME8-00	Social Outreach, Discipline & Extra Curricular Activities	0	0	0		0	0	**25**	**0.5**
			Sub- Total	0	0	13		180	120	**375**	**7.5**
			TOTAL OF III SEMESTER	16	1	13		350	800	**1225**	**24.5**

L: *Lecture,* ***T:*** *Tutorial,* ***P:*** *Practical,* ***Cr:*** *Credits*

ETE: *End Term Exam,* ***IA:*** *Internal Assessment*

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

Teaching & Examination Scheme

B.Tech. : Mechanical Engineering
2nd Year - IV Semester

THEORY												
SN	**Category**	**Course**		**Contact hrs/week**			**Marks**				**Cr**
		Code	**Title**	**L**	**T**	**P**	**Exm Hrs**	**IA**	**ETE**	**Total**	
1	BSC	4ME2-01	Data analytics	2	0	0	2	20	80	**100**	**2**
2	HSMC	4ME1-03/ 4ME1-02	Managerial Economics and Financial Accounting/ Technical Communications	2	0	0	2	20	80	**100**	**2**
3	ESC	4ME3-04	Digital Electronics	2	0	0	2	20	80	**100**	**2**
4	PCC	4ME4-05	Fluid Mechanics and Fluid Machines	3	1	0	3	40	160	**200**	**4**
5		4ME4-06	Manufacturing Processes	3	0	0	3	30	120	**150**	**3**
6		4ME4-07	Theory of machines	3	1	0	3	40	160	**200**	**4**
			Sub Total	15	2	0		170	680	**850**	**17**
PRACTICAL & SESSIONAL											
7	PCC	4ME3-21	Digital Electronics lab	0	0	3		45	30	**75**	**1.5**
8		4ME4-22	Fluid Mechanics lab	0	0	3		45	30	**75**	**1.5**
9		4ME4-23	Production practice lab	0	0	3		45	30	**75**	**1.5**
10		4ME4-24	Theory of machines Lab	0	0	3		45	30	**75**	**1.5**
11	SODECA	4ME8-00	Social Outreach, Discipline & Extra Curricular Activities	0	0	0		0	0	**25**	**0.5**
			Sub- Total	0	0	12		180	120	**325**	**6.5**
			TOTAL OF IV SEMESTER	15	2	12		350	800	**1175**	**23.5**

L: *Lecture,* ***T:*** *Tutorial,* ***P:*** *Practical,* ***Cr:*** *Credits*

ETE: *End Term Exam,* ***IA:*** *Internal Assessment*

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

Rajasthan Technical University, Kota
B.Tech. (Mechanical Engineering Scheme)

SEMESTER V	5	5	Hrs. / Week			*IA*	*Exam*	Total
Subject Code	Title		L	T	P			
5ME1A	Heat Transfer		3	1	Theory Subjects	20	80	100
5ME2A	Dynamics of Machines		3	1		20	80	100
5ME3A	Measurement & Metrology		3			20	80	100
5ME4A	Quality Assurance and Reliability		3			20	80	100
5ME5A	Sociology and Economics for Engineers		3			20	80	100
5ME6.1A	Computer Aided Design and Graphics		3			20	80	100
5ME6.2A	Automobile Engg.							
5ME6.3A	Statistics for Decision Making							
	Practicals and Sessionals							
5ME7A	Heat Transfer Lab		Lab courses		3	75	50	125
5ME8A	Dynamics of Machines Lab				2	45	30	75
5ME9A	Production Engineering Lab				3	60	40	100
5ME10A	Professional Ethics and Disaster Management				2	30	20	50
5MEDC	Discipline & Extra Curricular Activity							50
	Total		18	2	10			1000
SEMESTER VI	6	6	Hrs. / Week			*IA*	*Exam*	Total
Subject Code	Title		L	T	P			
6ME1A	Design of Machine Elements - II		3		Theory Subjects	20	80	100
6ME2A	Newer Machining Methods		3			20	80	100
6ME3A	Mechatronics		3			20	80	100
6ME4A	Vibration Engineering		3	1		20	80	100
6ME5A	Steam Engineering		3	1		20	80	100
6ME6.1A	Non Destructive Evaluation and Testing		3			20	80	100
6ME6.2A	Design and Manufacture of Plastic Products							
6ME6.3A	Maintenance Management							
	Practicals and Sessionals							
6ME7A	Machine Design Sessional -II		Lab courses		3	75	50	125
6ME8A	Industrial Engineering Lab-I				2	45	30	75
6ME9A	Mechatronics Lab				2	45	30	75
6ME10A	Vibration Engineering Lab				2	45	30	75
6MEDC	Discipline & Extra Curricular Activity							50
	Total		18	2	9			1000
SEMESTER VII	7	7	Hrs. / Week			*IA*	*Exam*	Total

Applicable from Academic Session 2012-13

Rajasthan Technical University, Kota
B.Tech. (Mechanical Engineering Scheme)

Subject Code	Title		L	T	P			
7ME1A	Finite Element Methods		3		Theory Subjects	20	80	100
7ME2A	Refrigeration & Air-conditioning		3	1		20	80	100
7ME3A	Operations Research		3	1		20	80	100
7ME4A	Turbomachines		3			20	80	100
7ME5A	Operations Management		3			20	80	100
7ME6.1A	Micro and Nano Manufacturing		3			20	80	100
7ME6.2A	Robotics							
7ME6.3A	CNC Machines and Programming							
	Practicals and Sessionals							
7ME7A	Thermal Engineering Lab-II		Lab courses		3	60	40	100
7ME8A	FEM Lab				3	60	40	100
7METR	Practical Training & Industrial visit				2		100	100
7MEPR	Project-1				2	50		50
7MEDC	Discipline & Extra Curricular Activity							50
	Total		18	2	10			1000
SEMESTER VIII	8	8	Hrs. / Week			*IA*	*Exam*	Total
Subject Code	Title		L	T	P			
8ME1A	Computer Integrated Manufacturing Systems		3		Theory Subjects	20	80	100
8ME2A	Laws for Engineers		3			20	80	100
8ME3A	Power Generation		3	1		20	80	100
8ME4.1A	Product Development and Launching		3			20	80	100
8ME4.2A	Computational Fluid Dynamics							
8ME4.3A	Total Quality Management							
	Practicals and Sessionals							
8ME5A	CAM Lab		Lab courses		2	45	30	75
8ME6A	CAD Lab				3	60	40	100
8ME7A	Industrial Engineering Lab - II				2	45	30	75
8MEPR	Project-2				4	120	80	200
8MESM	Seminar				2	60	40	100
8MEDC	Discipline & Extra Curricular Activity							50
	Total		12	1	13			1000

Syllabus of

UNDERGRADUATE DEGREE COURSE

Mechanical Engineering

Rajasthan Technical University, Kota

Effective from session: 2018 – 2019

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

2nd Year - III Semester: B.Tech. (Mechanical Engineering)

3ME2-01: ADVANCE ENGINEERING MATHEMATICS-I

Credit: 3 **Max. Marks: 150 (IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Numerical Methods – 1:** Finite differences, Relation between operators, Interpolation using Newton's forward and backward difference formulae. Gauss's forward and backward interpolation formulae. Stirling's Formulae. Interpolation with unequal intervals: Newton's divided difference and Lagrange's formulae. Numerical Differentiation, Numerical integration: Trapezoidal rule and Simpson's 1/3rd and 3/8 rules.	**10**
2	**Numerical Methods – 2:** Numerical solution of ordinary differential equations: Taylor's series, Euler and modified Euler's methods. Runge- Kutta method of fourth order for solving first and second order equations. Milne's and Adam's predicator-corrector methods. Solution of polynomial and transcendental equations-Bisection method, Newton-Raphson method and Regula-Falsi method.	**8**
3	**Laplace Transform:** Definition and existence of Laplace transform, Properties of Laplace Transform and formulae, Unit Step function, Dirac Delta function, Heaviside function, Laplace transform of periodic functions. Finding inverse Laplace transform by different methods, convolution theorem. Evaluation of integrals by Laplace transform, solving ODEs by Laplace transforms method.	**10**
4	**Fourier Transform:** Fourier Complex, Sine and Cosine transform, properties and formulae, inverse Fourier transforms, Convolution theorem, application of Fourier transforms to partial ordinary differential equation (One dimensional heat and wave equations only).	**7**
5	**Z-Transform:** Definition, properties and formulae, Convolution theorem, inverse Z-transform, application of Z-transform to difference equation.	**5**
	Total	**40**

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

2nd Year - III Semester: B.Tech. (Mechanical Engineering)

3ME1-02/4ME1-02: TECHNICAL COMMUNICATION

Credit: 2 **Max. Marks: 100 (IA:20, ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	Contents	Hours
1	**Introduction to Technical Communication-** Definition of technical communication, Aspects of technical communication, forms of technical communication, importance of technical communication, technical communication skills (Listening, speaking, writing, reading writing), linguistic ability, style in technical communication.	4
2	**Comprehension of Technical Materials/Texts and Information Design & development-** Reading of technical texts, Reading and comprehending instructions and technical manuals, Interpreting and summarizing technical texts, Note-making. Introduction of different kinds of technical documents, Information collection, factors affecting information and document design, Strategies for organization, Information design and writing for print and online media.	6
3	**Technical Writing, Grammar and Editing**- Technical writing process, forms of technical discourse, Writing, drafts and revising, Basics of grammar, common error in writing and speaking, Study of advanced grammar, Editing strategies to achieve appropriate technical style, Introduction to advanced technical communication. Planning, drafting and writing Official Notes, Letters, E-mail, Resume, Job Application, Minutes of Meetings.	8
4	**Advanced Technical Writing**- Technical Reports, types of technical reports, Characteristics and formats and structure of technical reports. Technical Project Proposals, types of technical proposals, Characteristics and formats and structure of technical proposals. Technical Articles, types of technical articles, Writing strategies, structure and formats of technical articles.	8
	Total	26

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3ME1-03/4ME1-03 : MANAGERIAL ECONOMICS AND FINANCIAL ACCOUNTING

Credit: 2 **Max. Marks: 100 (IA:20, ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN		Hours
1	**Basic economic concepts-** Meaning, nature and scope of economics, deductive vs inductive methods, static and dynamics, Economic problems: scarcity and choice, circular flow of economic activity, national income-concepts and measurement.	4
2	**Demand and Supply analysis-** Demand-types of demand, determinants of demand, demand function, elasticity of demand, demand forecasting –purpose, determinants and methods, Supply-determinants of supply, supply function, elasticity of supply.	5
3	**Production and Cost analysis-** Theory of production- production function, law of variable proportions, laws of returns to scale, production optimization, least cost combination of inputs, isoquants. Cost concepts-explicit and implicit cost, fixed and variable cost, opportunity cost, sunk costs, cost function, cost curves, cost and output decisions, cost estimation.	5
4	**Market structure and pricing theory**- Perfect competition, Monopoly, Monopolistic competition, Oligopoly.	4
5	**Financial statement analysis-** Balance sheet and related concepts, profit and loss statement and related concepts, financial ratio analysis, cash-flow analysis, funds-flow analysis, comparative financial statement, analysis and interpretation of financial statements, capital budgeting techniques.	8
	TOTAL	26

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

2nd Year - III Semester: B.Tech. (Mechanical Engineering)

3ME3-04: ENGINEERING MECHANICS

Credit: 2 **Max. Marks: 100 (IA:20, ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

Applicable to the students admitted from 2018-19 onwards

SN	Contents	Hours
1	Statics of particles and rigid bodies: Fundamental laws of mechanics, Principle of transmissibility, System of forces, Resultant force, Resolution of force, Moment and Couples, Varignon's theorem, Resolution of a force into a force and a couple, Free body diagram, Equilibrium, Conditions for equilibrium, Lami's theorem. Plane trusses: Types of structures, Trusses, Support Conditions, Types of Loadings, Classification of trusses, Determinacy of trusses, Basic assumptions of truss analysis, Method of joints, Method of sections. Virtual work: Principle of Virtual Work, Active forces and active force diagram, Stability of equilibrium.	5
2	**Centroid & Moment of inertia**: Location of centroid and center of gravity, Moment of inertia, Parallel axis and perpendicular axis theorem, Radius of gyration, M.I of composite section, Polar moment of inertia, M.I of solid bodies. **Lifting machines**: Mechanical advantage, Velocity Ratio, Efficiency of machine, Ideal machine, Ideal effort and ideal load, Reversibility of machine, Law of machine, Lifting machines; System of pulleys, Simple wheel and axle, Wheel and differential axle, Weston's differential pulley block, Worm and worm wheel, Single purchase winch crab, Double purchase winch crab, Screw jack, Differential screw jack.	5
3	**Friction**: Types of Friction, Laws of friction, Angle of friction, Angle of repose, Ladder, Wedge, Belt Friction. **Belt and Rope drive**: Types of belts, Types of belt drives, Velocity ratio, Effect of slip on Velocity ratio, Crowing of pulleys, Length of belt, Ratio of tensions in flat belt drive, Power transmission by belt drives, Advantage and disadvantages of V-Belt over Flat Belt.	5
4	**Kinematics of particles and rigid bodies**: Velocity, Acceleration, Types of Motion, Equations of Motion, Rectangular components of velocity and acceleration, Angular velocity and Angular acceleration, Radial and transverse velocities and accelerations, Projectiles motion on plane and Inclined Plane, Relative Motion. **Kinetics of particles and rigid bodies**: Newton's second law, Equation of motion in rectangular coordinate, Equation of motion in radial and transverse components, Equation of motion in plane for a rigid body, D'Alembert principle.	5
5	**Work, Energy and power**: Work of a force, weight, spring force and couple, Power, Efficiency, Energy, Kinetic energy of rigid body, Principle of work and energy, Conservative and Non-conservative Force, Conservation of energy. Office of Dean Academic Affairs Rajasthan Technical University, Kota	6

	Impulse and momentum: Linear and angular momentum, Linear and angular impulse, Principle of momentum for a particle and rigid body, Principle of linear impulse and momentum for a particle and rigid body, Principle of angular momentum and Impulse, Conservation of angular momentum, Angular momentum of rigid body, Principle of impulse and momentum for a rigid body, Central impact, Oblique impact, System of variable mass, Rocket.	
	TOTAL	**26**

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

3ME3-04: ENGINEERING MECHANICS

Credit: 2 **Max. Marks: 100 (IA:20, ETE:80)**
2L+0T+0P **End Term Exam: 2 Hours**

Applicable to the students admitted in 2017-18 only

SN	Contents	Hours
1	**Statics of particles and rigid bodies:** Fundamental laws of mechanics, Principle of transmissibility, System of forces, Resultant force, Resolution of force, Moment and Couples, Varignon's theorem, Resolution of a force into a force and a couple, Free body diagram, Equilibrium, Conditions for equilibrium, Lami's theorem. **Plane trusses:** Types of structures, Trusses, Support Conditions, Types of Loadings, Classification of trusses, Determinacy of trusses, Basic assumptions of truss analysis, Method of joints, Method of sections. Virtual work: Principle of Virtual Work, Active forces and active force diagram, Stability of equilibrium.	5
2	**Centroid & Moment of inertia**: Location of centroid and center of gravity, Moment of inertia, Parallel axis and perpendicular axis theorem, Radius of gyration, M.I of composite section, Polar moment of inertia, M.I of solid bodies. **Lifting machines**: Mechanical advantage, Velocity Ratio, Efficiency of machine, Ideal machine, Ideal effort and ideal load, Reversibility of machine, Law of machine, Lifting machines; System of pulleys, Simple wheel and axle, Wheel and differential axle, Weston's differential pulley block, Worm and worm wheel, Single purchase winch crab, Double purchase winch crab, Screw jack, Differential screw jack.	5
3	**Friction**: Types of Friction, Laws of friction, Angle of friction, Angle of repose, Ladder, Wedge, Belt Friction. **Belt and Rope drive**: Types of belts, Types of belt drives, Velocity ratio, Effect of slip on Velocity ratio, Crowing of pulleys, Length of belt, Ratio of tensions in flat belt drive, Power transmission by belt drives, Advantage and disadvantages of V-Belt over Flat Belt.	5
4	**Kinematics:** Fundamentals of rectilinear motion and curvilinear motion, applications of general equations, Projectiles motion on plane and on inclined plane, Concept of Relative motion. **Dynamics:** Principles of dynamics, D'Alembert's principle, conservation of momentum and energy, Work and Energy and impulse momentum methods, central impact, oblique impact, system of variable mass.	6
5	**Vibrations:** Introduction to vibrations, Free vibrations of particles, Simple, compound and torsional pendulum, Energy Method.	5
	TOTAL	**26**

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3ME4-05 : ENGINEERING THERMODYNAMICS

Credit: 3 **Max. Marks: 150 (IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Basic Concepts and definitions of Thermodynamics**: System, Surroundings, Property, Energy, Thermodynamic Equilibrium, Process, work and modes of work.	2
	Zeroth and First Law of Thermodynamics: Zeroth of Thermodynamics**,** Temperature scale, First law of thermodynamics, First law analysis of some elementary processes. Steady and unsteady flow energy equations.	5
2	**Second Law of Thermodynamics:** Heat engine, Heat pump and refrigerator, Second law of thermodynamics, Equivalence of the Kelvin-Plank and Clausius statements. Reversible and Irreversible Processes, Carnot engine, Efficiency of a Carnot engine, Carnot principle, thermodynamic temperature scale, Clausis Inequality.	4
	Entropy: Entropy, Calculation of Entropy change, Principle of entropy increase. Temperature-Entropy diagram, Second law analysis of a control volume.	3
	Availability: Available energy, Loss in available energy, Availability Function, Irreversibility.	3
3	**Thermodynamic Properties of Fluids:** Pure substance, Concept of Phase, Graphical representation of p-v-T data, Properties of steam. Steam tables, Mollier chart	4
	Ideal Gas and Real Gas: Ideal gas, Real gas, Internal energy, enthalpy and specific heats of an ideal gas, equations of state, Dalton's law of partial pressures, Gibbs Dalton law, Thermodynamic properties of gas mixtures.	4
4	**Thermodynamic Relations:** Thermodynamic variables, Independent and dependent variables, Maxwell's thermodynamic relations, Thermodynamic relations involving entropy, Thermodynamic relations involving enthalpy and internal energy, Joule-Thomson coefficient, Clapeyron equation.	4
	Power Cycles: Otto cycle, Diesel cycle, Dual cycle, Brayton cycle and Ericsson cycle.	4
5	**Vapour power cycle:** Rankine cycle, effect of operating conditions on its efficiency, properties of ideal working fluid in vapour power cycle	3
	Reheat cycle, regenerative cycle, bleeding extraction cycle, feed water heating co-generation cycle.	3
	TOTAL	39

Office of Dean Academic Affairs
Rajasthan Technical University, Kota

RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

2nd Year - III Semester: B.Tech. (Mechanical Engineering)

3ME4-06 : MATERIAL SCIENCE AND ENGINEERING

Credit: 3 **Max. Marks: 150 (IA:30, ETE:120)**

3L+0T+0P **End Term Exam: 3 Hours**

SN	CONTENTS	Hours
1	Crystal structure – BCC, FCC and HCP, unit cell, crystallographic planes and directions, miller indices. Crystal imperfections, point, line, surface and volume defects.	4
	Frank Reed source of dislocation, Elastic & plastic modes of deformation, Bauschinger's effect, slip & twinning, strain hardening, cold/hot working recovery, re-crystallization and grain growth.	4
2	Classification of Engineering Materials: Solidification of metals and of some typical alloys, mechanism of crystallization (I) nuclear formation (ii) crystal growth, general principles of phase transformation in alloys, phase rule and equilibrium diagrams, equilibrium diagram of binary system having complete mutual solubility in liquid state and limited solubility in solid state, binary isomorphous alloy system, Hume-Rothery rule , binary system with limited solid solubility of terminal phase and in which solubility decreases with temperature and also alloy with a peritectic transformation, equilibrium diagram of a system whose components are subject to allotropic change.	5
	Iron carbon equilibrium diagram, phase transformation in the iron carbon diagram, eutectic, peritectic, eutectoid and peritectoid reactions and microstructures.	3
3	Isothermal transformation diagrams –cooling curves superimposed on Isothermal Transformation diagram, critical cooling rate. (i) Formation of Austenite from Pearlite (ii) Transformation of Austenite into Pearlite.	4
	Full annealing, stress relief, spheroidizing – normalizing, hardening and tempering of steel. Hardenability, Jominey end quench test – Austempering, martempering. Case hardening, carburising, nitriding, cyaniding, carbonitriding. Flame and Induction hardening.	4
4	Non-Metallic Materials- Polymers – types of polymer, commodity and engineering polymers – Properties and applications of PE, PP, PS, PVC, PMMA, PET, PC, PA, ABS, PI, PAI, PPO,PPS, PEEK, PTFE Polymers. Urea and Phenol formaldehydes.	4
	Constitution of alloys: Solid solutions - substitutional and interstitial. Ferrous and Non Ferrous Metals- Effect of alloying additions on steel (Mn, Si, Cr, Mo, V, Ti & W) - stainless and tool steels – HSLA steel.	4
5	Mechanical Properties and Testing: Types of fracture, testing of materials under tension, compression and shear loads – hardness tests (Brinell, Vickers and Rockwell) Impact test Izod and charpy, fatigue and creep test.	4

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	Classification of steels and cast iron constitution and properties. BIS standards. Engineering Ceramics – Properties and applications of Al2O3, SiC, Si3N4, PSZ etc. Fiber and particulate reinforced composites and resin plastics. Introduction to Nano materials- Nano structured materials. Nano clusters & Nano crystals.	**3**
	TOTAL	**39**

Office of Dean Academic Affairs
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RAJASTHAN TECHNICAL UNIVERSITY, KOTA

SYLLABUS

2nd Year - III Semester: B.Tech. (Mechanical Engineering)

3ME4-07 : MECHANICS OF SOLIDS

Credit: 4 **Max. Marks: 200 (IA:40, ETE:160)**

3L+1T+0P **End Term Exam: 3 Hours**

S.No	CONTENTS	Hours
1	**Stress and Strain:** Elementary definition of stress and strain, stress-strain relationship, elastic, plastic and visco-elastic behavior of common materials in tension and compression test, stress-strain curves, Hooke's law, Poisson's ratio, elastic constants and their relations for an isotropic hookean material, anisotropic and orthotropic materials.	3
	Tension, compression, shearing stress and strain, thermal stresses, composite bars, equations of static equilibrium, concept of free body diagram. Strain energy due to axial loading.	5
2	**Members Subjected to Flexural Loads:** Theory of simple bending, bending moment and shear force diagrams for different types of static loading and support conditions on beams.	4
	bending stresses, section modulus and transverse shear stress distribution in circular, hollow circular, I, Box, T, angle sections etc. Strain energy due to bending.	5
3	**Principal Planes, Stresses and Strains:** Members subjected to combined axial, bending and torsional loads, maximum normal and shear stresses, concept of equivalent bending and equivalent twisting moments, Mohr's circle of stress and strain.	5
	Theories of Elastic Failures: The necessity for a theory, different theories, significance and comparison, applications.	2
4	**Torsion:** Torsional shear stress in solid, hollow and stepped circular shafts, angular deflection and power transmission capacity. Strain energy due to torsional loads.	4
	Stability of Equilibrium: Instability and elastic stability, long and short columns, ideal strut, Euler's formula for crippling load for columns of different ends, concept of equivalent length, eccentric loading, Rankine formulae and other empirical relations.	3
5	**Transverse Deflection of Beams:** Relation between deflection, bending moment, shear force and load, transverse deflection of beams and shaft under static loading, area moment method, direct integration method.	6
	Thin-walled Pressure Vessels: Stresses in cylindrical and spherical vessels	2
	TOTAL	**39**

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3ME4-21 : MACHINE DRAWING PRACTICE

Credit: 1.5 **Max. Marks: 75 (IA:45, ETE:30)**

0L+0T+3P

SN	CONTENTS
1.	Assembly drawing with sectioning and bill of materials of the following: Lathe tail stock, shaper tool head, swivel machine vice etc (1 drawing sheet of any assembly)
2.	Detailed part drawings from assembly drawing indicating fits, tolerances and surface finish symbols by referring BIS codes: Check-valve, Junction Valve etc (1 drawing sheet)
3.	Computer Aided Drafting: Introduction to different features of the CAD Software (AutoCAD/ProE/ Creo/Solidworks). At least one drawing problem related to a. 2-D Drafting. b. 3-D Modeling. c. 3-D Advanced Modeling. d. Assembly modeling. e. Feature Modification and Manipulation f. Detailing. g. Surface Modeling

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3ME4-22 : MATERIALS TESTING LAB

Credit: 1.5 **Max. Marks: 75 (IA:45, ETE:30)**

0L+0T+3P

SN	
1	(a) Study of various crystals structures through models BCC, FCC, HCP, tetrahedral and octahedral voids. Material identification of, say, 50 common items kept in a box.
2	Specimen preparation for metallographic examination /micro structural examination-cutting, grinding, polishing, etching.
3	Comparative study of microstructures of different given specimens (mild steel, gray C.I., brass, copper etc.)
4	Heat treatment experiments such as annealing, normalizing, quenching, case hardening and comparison of hardness before and after.
5	Study of Microstructure and hardness of steel at different rates of cooling. Microstructure examination of white cast iron.
6	To perform Tensile/Compressive/Shear/torsion test on a given material and to determine its various mechanical properties under tensile/compression/Shear/torsional loading
7	To determine Rockwell/ Vickers/Brinell hardness of a given material
8	To perform Impact test on a given material and to determine its resilience.
9	To study and perform Fatigue test on a given material and to determine fatigue strength of the material
10	To perform Bending test and to determine the Young's Modulus of Elasticity via deflection of beam.
11	Creep testing on creep testing machine

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3ME4-23 : BASIC MECHANICAL ENGINEERING LAB

Credit: 1.5 **Max. Marks: 75 (IA:45, ETE:30)**

0L+0T+3P

SN	
1	Exposure to a wide range of applications of mechanical engineering through a variety of activities, including hands-on assembly and disassembly of machines, such as, bicycle, sewing machine, pumps, engines, air-conditioners, machine-tools, amongst others; observational study of complex systems via cut sections, visits, videos and computer simulations; design of simple machines/systems including specifications formulation; visits to industries.
2	Note: Student will be required to submit written report indicating the learning achieved by Hands on assembly/Disassembly.

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3ME4-24: PROGRAMMING USING MATLAB

Credit: 1.5 **Max. Marks: 75 (IA:45, ETE:30)**
0L+0T+3P

SN	
1	1. Basics of MATLAB computer programming 2. Use of formulae and inbuilt functions 3. MATLAB scripts and functions (m-files) 4. Loops and nested loops 5. Array, vector and matrices 6. Plotting functions and vector plots 7. Solving differential equations using MATLAB 8. Reading and writing data, file handling 9. Using MATLAB toolboxes 10. MATLAB graphic functions

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Syllabus of

UNDERGRADUATE DEGREE COURSE

Mechanical Engineering

Rajasthan Technical University, Kota

Effective from session: 2018 – 2019

4ME2-01: DATA ANALYTICS

Credit: 2 **Max. Marks: 100 (IA:20, ETE:80)**

2L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	Introduction to Multivariate Statistics-Degree of Relationship among Variables-Review of Univariate and Bivariate Statistics-Screening Data Prior to Analysis-Missing Data, Outliers, Normality, Linearity, and Homoscedasticity.	4
3	Multiple Regression- Linear and Nonlinear techniques- Backward Forward-Stepwise- Hierarchical regression-Testing interactions (2way interaction) - Analysis of Variance and Covariance (ANOVA & ANCOVA) - Multivariate Analysis of Variance and Covariance (MANOVA & MANCOVA).	6
4	Logistic regression: Regression with binary dependent variable - Simple Discriminant Analysis- Multiple Discriminant analysis Assessing classification accuracy- Conjoint analysis (Full profile method).	5
5	Principal Component Analysis -Factor Analysis- Orthogonal and Oblique Rotation-Factor Score Estimation-Multidimensional Scaling-Perceptual Map-Cluster Analysis (Hierarchical Vs Nonhierarchical Clustering).	5
6	Latent Variable Models an Introduction to Factor, Path, and Structural Equation Analysis- Time series data analysis (ARIMA model) – Decision tree analysis (CHAID, CART) - Introduction to Big Data Management.	5
	TOTAL	26

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4ME1-03/3ME1-03 : MANAGERIAL ECONOMICS AND FINANCIAL ACCOUNTING

Credit: 2 **Max. Marks: 100 (IA:20, ETE:80)**
2L+0T+0P **End Term Exam: 2 Hours**

SN		Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Basic economic concepts-** Meaning, nature and scope of economics, deductive vs inductive methods, static and dynamics, Economic problems: scarcity and choice, circular flow of economic activity, national income-concepts and measurement.	3
3	**Demand and Supply analysis-** Demand-types of demand, determinants of demand, demand function, elasticity of demand, demand forecasting –purpose, determinants and methods, Supply-determinants of supply, supply function, elasticity of supply.	5
4	**Production and Cost analysis-** Theory of production- production function, law of variable proportions, laws of returns to scale, production optimization, least cost combination of inputs, isoquants. Cost concepts-explicit and implicit cost, fixed and variable cost, opportunity cost, sunk costs, cost function, cost curves, cost and output decisions, cost estimation.	5
5	**Market structure and pricing theory**- Perfect competition, Monopoly, Monopolistic competition, Oligopoly.	4
6	**Financial statement analysis-** Balance sheet and related concepts, profit and loss statement and related concepts, financial ratio analysis, cash-flow analysis, funds-flow analysis, comparative financial statement, analysis and interpretation of financial statements, capital budgeting techniques.	8
	TOTAL	26

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4ME1-02/3ME1-02: TECHNICAL COMMUNICATION

Credit: 2 **Max. Marks: 100 (IA:20, ETE:80)**

2L+0T+0P **End Term Exam: 2 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Introduction to Technical Communication-** Definition of technical communication, Aspects of technical communication, forms of technical communication, importance of technical communication, technical communication skills (Listening, speaking, writing, reading writing), linguistic ability, style in technical communication.	3
3	**Comprehension of Technical Materials/Texts and Information Design & development-** Reading of technical texts, Reading and comprehending instructions and technical manuals, Interpreting and summarizing technical texts, Note-making. Introduction of different kinds of technical documents, Information collection, factors affecting information and document design, Strategies for organization, Information design and writing for print and online media.	6
4	**Technical Writing, Grammar and Editing**- Technical writing process, forms of technical discourse, Writing, drafts and revising, Basics of grammar, common error in writing and speaking, Study of advanced grammar, Editing strategies to achieve appropriate technical style, Introduction to advanced technical communication. Planning, drafting and writing Official Notes, Letters, E-mail, Resume, Job Application, Minutes of Meetings.	8
5	**Advanced Technical Writing**- Technical Reports, types of technical reports, Characteristics and formats and structure of technical reports. Technical Project Proposals, types of technical proposals, Characteristics and formats and structure of technical proposals. Technical Articles, types of technical articles, Writing strategies, structure and formats of technical articles.	8
	Total	26

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4ME3-04: DIGITAL ELECTRONICS

Credit: 2 **Max. Marks: 100 (IA:20, ETE:80)**
2L+0T+0P **End Term Exam: 2 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Semiconductor Devices and Applications:** Introduction to P-N junction Diode and V-I characteristics, Half wave and Full-wave rectifiers, capacitor filter. Zener diode and its characteristics, Zener diode as voltage regulator. Regulated power supply IC based on 78XX and 79XX series, Introduction to BJT, its input-output and transfer characteristics, BJT as a single stage CE amplifier, frequency response and bandwidth.	4
3	**Operational amplifier and its applications**: Introduction to operational amplifiers, Op-amp input modes and parameters, Op-amp in open loop configuration, op-amp with negative feedback, study of practical op-amp IC 741, inverting and non-inverting amplifier applications: summing and difference amplifier, unity gain buffer, comparator, integrator and differentiator.	5
4	**Timing Circuits and Oscillators:** RC-timing circuits, IC 555 and its applications as astable and mono-stable multi-vibrators, positive feedback, Barkhausen's criteria for oscillation, R-C phase shift and Wein bridge oscillator.	5
5	**Digital Electronics Fundamentals**: Difference between analog and digital signals, Boolean algebra, Basic and Universal Gates, Symbols, Truth tables, logic expressions, Logic simplification using K- map, Logic ICs, half and full adder/subtractor, multiplexers, de-multiplexers, flip-flops, shift registers, counters, Block diagram of microprocessor/microcontroller and their applications.	6
6	**Electronic Communication Systems**: The elements of communication system, IEEE frequency spectrum, Transmission media: wired and wireless, need of modulation, AM and FM modulation schemes, Mobile communication systems: cellular concept and block diagram of GSM system.	5
	TOTAL	26

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4ME4-05: FLUID MECHANICS AND FLUID MACHINES

Credit: 4 **Max. Marks: 200 (IA:40, ETE:160)**

3L+1T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	**Fluid Properties:** Units and dimensions- Properties of fluids- mass density, specific weight, specific volume, specific gravity, viscosity, compressibility, vapor pressure, surface tension and capillarity.	2
	Fluid Statics and Flow Characteristics: Basic equation of fluid statics, Manometers, Force on plane areas and curved surfaces, center of pressure, Buoyant force, Stability of floating and submerged bodies. Flow characteristics – concept of control volume - application of continuity equation, energy equation and momentum equation.	5
3	**Flow Through Circular Conduits:** Hydraulic and energy gradient - Laminar flow through circular conduits and circular annuli-Boundary layer concepts – types of boundary layer thickness – Darcy Weisbach equation –friction factor- Moody diagram-minor losses – Flow through pipes in series and parallel.	8
4	**Dimensional Analysis:** Need for dimensional analysis – methods of dimensional analysis – Similitude –types of similitude - Dimensionless parameters- application of dimensionless parameters – Model analysis.	8
5	**Pumps:** Impact of jets - Euler's equation - Theory of roto-dynamic machines – various efficiencies– velocity components at entry and exit of the rotor- velocity triangles - Centrifugal pumps– working principle - work done by the impeller - performance curves - Reciprocating pump- working principle – Rotary pumps –classification.	8
6	**Turbines:** Classification of turbines – heads and efficiencies – velocity triangles. Axial, radial and mixed flow turbines. Pelton wheel, Francis turbine and Kaplan turbines- working principles - work done by water on the runner – draft tube. Specific speed - unit quantities – performance curves for turbines – governing of turbines.	7
	TOTAL	39

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4ME4-06: MANUFACTURING PROCESSES

Credit: 3 **Max. Marks: 150 (IA:30, ETE:120)**
3L+0T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	General Classification and Introduction to Manufacturing processes. **Foundry Technology**: Casting: Definition and major classification; Casting materials, Patterns: types, material and pattern allowances. Moulding sands; composition, preparation, properties and testing; Grain fineness; moisture content, clay content and permeability test. Core & core prints; Gating system: types, pouring basin, sprue, runner and risers; Melting, pouring and solidification.	3
	Principles and method of floor mould casting, shell mould casting, pit mould and loam mould casting; centrifugal casting, investment casting; Permanent mould casting. Die casting; Slush casting. Casting defects; types, causes and remedy	5
3	**Forming Processes**: Classification; Hot working and cold working; principle, advantages, disadvantages and applications.	3
	Forging: Classification, drop forging and press forging methods and use; Forging dies; types, materials.	4
	Rolling: Characteristics and applications of hot rolling and cold rolling;	3
4	Extrusion; Work materials and products; Press tool works; Basic principles, system, operations and applications. Shearing; Parting, notching, trimming, nibbling, blanking and piercing,	4
	Drawing: wire drawing, tube drawing and deep drawing.	3
5	**Metal Joining Processes**: Welding, Brazing and soldering, classification of welding process, Principle, characteristics and applications of gas welding, thermit welding, electrical arc welding; Submerged arc welding; TIG and MIG welding; Resistance welding; Spot welding; Butt welding; Seam welding; Projection welding.	6
	Principles and process details of Forge welding; Friction welding; Diffusion welding; Ultrasonic welding. Explosive welding. Welding defects; Types, causes, effects and remedy. Electrodes and Electrode Coatings	3
6	**Powder Metallurgy**: Properties of Powder processed materials, Powder manufacturing, mechanical pulverization, sintering, Electrolytic Process, chemical reduction, atomization, properties of metal powders, compacting of powders sintering, advantages and applications of Powder metallurgy.	4
	TOTAL	39

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4ME4-07: THEORY OF MACHINES

Credit: 4 **Max. Marks: 200 (IA:40, ETE:160)**
3L+1T+0P **End Term Exam: 3 Hours**

SN	Contents	Hours
1	**Introduction:** Objective, scope and outcome of the course.	1
2	Introduction to mechanism: Basic concept of machines, links, kinematic pair, kinematic chain and mechanism. Inversions of kinematic chains: four bar chain mechanisms, quick return mechanisms, inversions of double slider crank mechanisms.	4
	Velocity and acceleration in mechanism: Velocity and acceleration polygons, relative velocity and instantaneous centre method	3
3	Friction devices: Types and laws of friction. Pivots and collars. Power screws such as lead screw of the lathe.	3
	Clutches: Single and multi-plate clutches. Brakes: Band, block and band and block brakes.	4
4	Gears: Laws of gearing, gears terminology; tooth form; interference, undercutting and minimum number of teeth on pinion. Rack and pinion, Spur, helical, basic introduction of bevel, worm and worm gears.	6
	Gear Trains: Simple, compound and epicyclic gear trains.	3
5	Cams: Type of cams; displacement, velocity and acceleration curves for different cam followers; consideration of pressure angle and wear.	4
	Gyroscope: Principles of gyroscopic couple, effect of gyroscopic couple and centrifugal force on vehicles taking a turn, stabilization of ship.	4
6	Balancing: Balancing of rotating masses in same and different planes, balancing of reciprocating masses, swaying couple, hammer blow and tractive effort.	7
	TOTAL	39

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4ME3-21: DIGITAL ELECTRONICS LAB

Credit: 1.5 **Max. Marks: 75 (IA:45, ETE:30)**
0L+0T+3P

SN	
1	To verify the truth tables of basic logic gates: AND, OR, NOR, NAND, NOR. Also to verify the truth table of Ex-OR, Ex-NOR (For 2, 3 & 4 inputs using gates with 2, 3, & 4 inputs).
2	To verify the truth table of OR, AND, NOR, Ex-OR. Ex-NOR realized using NAND & NOR gates.
3	To realize an SOP and POS expression.
4	To realize Half adder/ Subtractor & Full Adder/ Subtractor using NAND & NOR gates and to verify their truth tables.
5	To realize a 4-bit ripple adder/ Subtractor using basic half adder/ Subtractor & basic Full Adder/ Subtractor.
6	To verify the truth table of 4-to-l multiplexer and l-to-4 demultiplexer. Realize the multiplexer using basic gates only. Also to construct and 8-to-1 multiplexer and l-to-8 demultiplexer using blocks of 4-to-1 multiplexer and 1-to-4 demulriplexer.
7	Design & Realize a combinational circuit that will accept a 2421 BCD code and drive a TIL -3 I 2 seven-segment display.
8	Using basic logic gates, realize the R-S, J-K and D-flip flops with and without clock signal and verify their truth table.
9	Construct a divide by 2, 4 & 8 asynchronous counter. Construct a 4-bit binary counter and ring counter for a particular output pattern using D flip flop.
10	Perform input/output operations on parallel in/parallel out and Serial in/Serial out registers using clock. Also exercise loading only one of multiple values into the register using multiplexer.

Note: As far as possible, the experiments shall be performed on bread board. However experiment Nos. l-4 are to be performed on bread board only

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4ME4-22: FLUID MECHANICS LAB

Credit: 1.5 **Max. Marks: 75 (IA:45, ETE:30)**
0L+0T+3P

SN	
1	Determination of Meta-centric height of a given body.
2	Determination of Cd, Cv & Cc for given orifice.
3	Calibration of contracted Rectangular Notch and / Triangular Notch and determination of flow rate.
4	Determination of velocity of water by Pitot tube.
5	Verification of Bernoulli's theorem.
6	Calibration and flow rate determination using Venturimeter & Orifice meter and Nozzle meter
7	Determination of head loss in given length of pipe.
8	Determination of the Reynold's number for laminar, turbulent and transient flow in pipe.
9	Determination of Coefficient for minor losses in pipes.
10	To study the velocity distribution in a pipe and also to compute the discharge by integrating the velocity profile.
11	To study the boundary layer velocity profile over a flat plate and to determine the boundary layer thickness.
12	Conducting experiments and drawing the characteristic curves of centrifugal pump/submergible pump.
13	Conducting experiments and drawing the characteristic curves of reciprocating pump.
14	Conducting experiments and drawing the characteristic curves of Pelton wheel.
15	Conducting experiments and drawing the characteristics curves of Francis turbine.
16	Conducting experiments and drawing the characteristic curves of Kaplan turbine.

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4ME4-23: PRODUCTION PRACTICE LAB

Credit: 1.5 **Max. Marks: 75 (IA:45, ETE:30)**
0L+0T+3P

SN	
	Turning Shop
1	To study lathe machine construction and various parts including attachments, lathe tools cutting speed, feed and depth of cut.
2	To perform step turning, knurling and chamfering on lathe machine as per drawing.
3	To cut multi-start Square/Metric threads on lathe machine.
4	Boring using a boring bar in a centre lathe and cut BSW/Metric internal threads on lathe machine.
5	To perform taper turning using compound rest.
	Machine shop
1	To study the milling machine, milling cutters, indexing heads and indexing methods and to prepare a gear on milling machine.
2	To machine a hexagonal /octagonal nut using indexing head on milling machine.
3	To study of single point cutting tool geometry and to grind the tool as per given tool geometry.
4	To study shaper machine, its mechanism and calculate quick return ratio. To prepare a job on shaper from given mild steel rod.
5	Cylindrical grinding using grinding attachment in a centre lathe
	Demonstration and study
1	Demonstration for job by eccentric turning on lathe machine.
2	Study of capstan lathe and its tooling and prepare a tool layout & job as per given drawing.
3	Demonstration on milling machine for generation of plane surfaces and use of end milling cutters.
4	Grinding of milling cutters and drills.
	Foundry Shop
1	To prepare mould of a given pattern requiring core and to cast it in aluminium.
2	To perform moisture test and clay content test.
3	To perform permeability test
4	A.F.S. Sieve analysis test.
5	Strength Test (compressive, Tensile, Shear Transverse etc. in green and dry conditions) and Hardness Test (Mould and Core).
	Welding Shop
1	Hands-on practice on spot welding.

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4ME4-24: THEORY OF MACHINES LAB

Credit: 1.5 **Max. Marks: 75 (IA:45, ETE:30)**
0L+0T+3P

SN	
1	To study inversions of four bar chain and slider crank mechanism and their practical applications.
2	To study Steering Mechanisms: Davis and Ackerman.
3	Study of quick return mechanism and its practical applications.
4	Study of inversion of Double slider chain: Oldham Coupling, Scotch Yoke and Elliptical Trammel.
5	Study of various cam-follower arrangements. To plot displacement v/s angle of rotation curve for various cams
6	To determine co-efficient of friction using two roller oscillating arrangement.
7	Study of various types of dynamometers, Brakes and Clutches.
8	Study of differential gear box.
9	To verify the torque relation for gyroscope.
10	To perform wheel balancing. To perform static and dynamic balancing on balancing set up.
11	Study of a lathe gear box, sliding mesh automobile gear box, planetary gear box.

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	engine with the help of cut section models	
2	Study of working of two stroke petrol and two stroke diesel engine with the help of cut section models.	
3	To draw valve timing diagram for a single cylinder diesel engine.	
4	Study of various types of boilers.	
5	Study of various types of mountings and accessories.	
6	Demonstration of steering system and measurement of steering geometry angles and their impact on vehicle performance.	
7	Study of braking system with specific reference to types of braking system, master cylinder, brake shoes.	
8	Study of transmission system including clutches, gear box assembly and differential.	
9	Study of fuel supply system of a petrol engine (fuel pump and simple carburetor)	
10	Study of fuel supply system of a Diesel engine (fuel pump and fuel injector)	
11	Study of Ignition systems of an IC Engine (Battery and Magneto ignition system) and Electronic ignition system.	
12	Study of Lubrication system of an IC Engine (mist, splash and pressure lubrication)	
13	Study of cooling systems of an IC Engine (air cooling and water cooling)	

5ME1A: HEAT TRANSFER

B.Tech. (Mechanical) 5th Semester **Max. Marks: 100**
3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**Introduction:** Heat transfer processes, conduction and radiation. Fourier's law of heat conduction, thermal conductivity, thermal conductivity of solids, liquids and gases, effect of temperature on thermal conductivity. Newton's law of cooling, definition of overall heat transfer coefficient. General parameters influence the value of heat transfer coefficient.	**4**
	Conduction: General 3-Dimensoinal conduction equation in Cartesian, cylindrical and spherical coordinates; different kinds of boundary conditions; nature of differential equations; one dimensional heat conduction with and without heat generation; electrical analogy; heat conduction through composite walls; critical thickness of insulation	**3**
II	**Heat transfer from extended surfaces:** Governing differential equation of fin, fin efficiency and effectiveness for different boundary conditions.	**3**
	Unsteady state heat conduction for slab, cylinder and sphere, Heisler chart.	**2**
	Convection: Review of Navier – Stokes and energy equation, hydrodynamic and thermal boundary layers; laminar boundary layer equations; forced convection appropriate non dimensional members; effect of Prandtl number; empirical relations for flow over a flat plate and flow through pipes.	**4**
III	**Natural convection:** Dimensional analysis, Grashoff number, boundary layers in external flows (flow over a flat plate only), boundary layer equations and their solutions, heat transfer correlations.	**4**
	Heat transfer with change of phase: Nature of vaporization phenomena; different regimes of boiling heat transfer; correlations for saturated liquid vaporization; condensation on flat plates; correlation of experimental results, drop wise condensation.	**4**

IV	**Heat exchanger:** Types of heat exchangers, arithmetic and logarithmic mean temperature differences, heat transfer coefficient for parallel, counter and cross flow type heat exchanger; effectiveness of heat exchanger, N.T.U. method, fouling factor. Constructional and manufacturing aspects of Heat Exchangers.	**8**
V	**Thermal Radiation:** Plank distribution law, Krichoff's law; radiation properties, diffuse radiations; Lambert's law. Radiation intensity, heat exchange between two black bodies heat exchanger between gray bodies. Shape factor; electrical analogy; reradiating surfaces heat transfer in presence of reradiating surfaces.	**8**
		40

TEXT BOOK		
1	J.P. Halman, Heat Transfer, Mc Graw Hill	
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Incropera and Dewitt, Fundamental of Heat and Mass transfer, John Wiley	**2007**
2	Cengel, Heat and Mass transfer, Mc Graw Hill	**2011**
3	M.Thirumaleshwar, Fundamental of Heat and Mass Transfer, Pearson Education	**2006**
4	Ozisik, Heat and Mass Transfer, Mc Graw Hill	**2009**

5ME2A: DYNAMICS OF MACHINES

B.Tech. (Mechanical) 5th semester **Max. Marks: 100**
3L+1T **Exam Hours: 3**

UNIT	**CONTENTS**	**CONTACT HOURS**
I	**Governors:** Comparison between flywheel and governor, Types of governor, Watt, Porter, Proell, Hartnell and spring controlled governors, sensitiveness of governors, stability of governors, isochronous and hunting, governor effort, power, controlling force diagram.	**8**
II	**Gyroscope:** Principle of gyroscopic couple, effect of gyroscopic couple and centrifugal force on aeroplanes, ships and vehicle taking a turn, stabilization of sea vessels, stability of four wheeled vehicle moving in a curved path, curved path with banking, stability of two wheeled vehicle, gyroscopic effect on inclined rotating disc	**5**
	Inertia force analysis: Velocity and acceleration of slider crank and four bar mechanism, inertia force, piston thrust and forces on connecting rod, turning moment diagram, flywheel.	**3**
III	**Gears:** Classification, terminology, law of gearing, velocity of sliding, gear tooth profile, comparison of cycloidal and involute tooth profile, standard interchangeable tooth profile, length of path of contact, arc of contact, contact ratio, interference, undercutting, minimum number of teeth on pinion in contact with gear or rack, bevel, helical and spiral gears.	**9**
IV	**Gear Trains**: Simple, compound, reverted and epicyclic gear trains, analytical, tabular, graphical and vector methods for finding velocity ratio, gear boxes- sliding and constant mesh, synchromesh and differential gear box.	**7**
V	**Balancing:** Need of balancing, Balancing of rotating masses, single plane, different planes, balancing of reciprocating masses, single cylinder engine, multi-cylinder inline engines, V-engines, concept of direct and reverse cranks, partial balancing of locomotives, IC engines, V engines and balancing machines.	**8**
	TOTAL	**40**

TEXT BOOK		
1	Rattan, S.S., "Theory of Machines", 2nd Ed., Tata McGraw Hill.	2006
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Bevan, T., "Theory of Machines", Pearson Education.	2013
2	Uicker, J.J., Pennocle, G.R, and Shigley, J.E, "Theory of Machines and Mechanisms", 3rd Ed., Oxford University Press.	2009
3	Ambekar , A. G., "Mechanism And Machine Theory", Prentice-hall Of India	2007
4	Ghosh, A., "Theory of Mechanisms and Machines", Affiliated East West Press.	
5	Singh, S., "Theory of Machines", Pearson Education	2013

5ME3A: MEASUREMENT & METROLOGY

B.Tech. (Mechanical) 5th semester **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**Concept of measurement:** General concept of measurement, Need for measurement, Generalized measuring system, Units, Standards, Sensitivity, Readability, Range of accuracy, Precision, Accuracy Vs precision, Uncertainty.	4
	Repeatability and reproducibility, Errors in measurement, Types of error, Systematic and random error, Comparison between systematic error and random error, Correction, Calibration, Interchangeability.	3
II	**Linear and angular measurements:** Linear measuring instruments: Vernier caliper, Micrometer, Interval measurements:- Slip gauges, Checking of slip gauges for surface quality, Optical flat, Limit gauges:- Gauge design, Problems on gauge design, Application of limit gauges;	3
	Comparators:- Mechanical comparators, Electrical comparator, Optical comparator, Pneumatic comparator;	2
	Sine bar, Use of sine bar, Limitations of sine bars, Sources of error in sine bars, Bevel protractor, Applications of bevel protractor , Autocollimator, Angle dekkor	4
III	**Form measurement:** Introduction, Screw thread measurement, Thread gauges, Measurement of gears: Gear errors, Spur gear measurement, Parkinson gear tester, Problems on gear measurement.	4
	Surface finish measurement:-Introduction, Elements of surface texture, Analysis of surface finish, Methods of measuring surface finish, Straightness measurement, Flatness testing, Roundness measurements	4
IV	**Laser and advances in metrology:** Laser metrology, Laser telemetric system, Laser and led based distance measuring instruments, pattern formed in a laser, Principle of laser, Interferometry, Use of laser in interferometry, Laser interferometry.	3
	Machine tool metrology: Various geometrical checks on machine tool, Laser equipment for alignment testing, Machine tools tests, Alignment tests on lathe, milling machine, pillar type drilling machine, Acceptance tests for surface grinders, Coordinate measuring machine (CMM):- Types of CMM, Features of CMM, Computer based inspection, Computer aided inspection using robots.	5
V	**Measurement of power, flow and temperature related properties** Measurement of force, Direct methods, Indirect methods:- Accelerometer, Load cells, Bourdon tube. **Torque measurement:** Prony brake, Torque measurement using strain gauges, Torque measurement using torsion bars,	4

	Measurement of power: Mechanical dynamometers, D.C. dynamometer, Eddy current or inductor dynamometers	
	Measurement of flow: Orifice meter, Venturimeter, Flow nozzle, Variable area meters – rotameter, Hot wire anemometer, Pitot tube. Temperature measurement, Bimetallic strip, Calibration of temperature measuring devices, Thermocouples (Thermo electric effects), Thermistors, Pyrometers	**4**
	TOTAL	**40**

TEXT BOOK		
1	G.K. Vijayaraghavan & R. Rajappan, Engineering Metrology and Measurements, A.R.S. Publications, Chennai, Fourth Edition June	2009
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Mechanical Measurements , Beckwith T.G. ,N.L. Buck, and R.D. Marangoni , Addison Wesley	
2	Dimensional Metrology . Khare & Vajpayee, Oxford & IBH	
3	Engineering Metrology, Jain R.K., Khanna Publishers	
4	Metrology & Precision Engineering , Scarr, McGraw Hill	
5	Handbook of Industrial Metrology, ASTME	
6		

5ME4A: QUALITY ASSURANCE AND RELIABILITY

B.Tech. (Mechanical) 5th semester **Max. Marks: 100**

3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	The meaning of Quality and quality improvement, dimensions of quality, history of quality methodology, quality control, Quality of design and quality of conformance, Quality policy and objectives, Economics of quality.	**5**
	Modeling process quality: Describing variation, frequency distribution, continuous and discrete, probability distributions, pattern of variation, Inferences about process quality: sampling distributions and estimation of process parameters. Analysis of variance.	**4**
II	Statistical Quality Control: Concept of SQC, Chance and assignable causes of variation, statistical basis of control chart, basic principles, choice of control limits, sample size and sampling frequency, analysis of patterns on control charts. The magnificent seven.	**4**
	Control chart for variables,: X-bar and R charts, X-bar and S charts, control chart for individual measurement. Application of variable control charts.	**4**
III	Control chart for attributes: control chart for fraction non conforming P-chart, np-chart, c-chart and u-chart. Demerit systems, choice between attribute and variable control chart. SPC for short production runs. Process capability analysis using histogram and probability plot, capability ratios and concept of six sigma.	**7**
IV	Quality Assurance: Concept, advantages, field complaints, quality rating, quality audit.	**2**
	Acceptance Sampling: Fundamental concepts in acceptance sampling, operating characteristics curve. Acceptance sampling plans, single, double and multiple sampling plans, LTPD, AOQL, AOQ.	**4**
	Introduction to Quality systems like ISO 9000 and ISO 14000.	**2**

V	Reliability and Life Testing- Failure models of components, definition of reliability, MTBF, Failure rate, common failure rate curve, types of failure, reliability evaluation in simple cases of exponential failures in series, paralleled and series-parallel device configurations, Redundancy and improvement factors evaluations. Introduction to Availability and Maintainability	**4**
	Introduction to Taguchi Method of Design of Experiments, Quality loss function.	**4**
	TOTAL	**40**

TEXT BOOK		
1	Introduction to Statistical Quality Control, Douglas C. Montgomery, 2nd Edition, Wiley.	1991
2	Charles E. Ebeling, An introduction to reliability and maintainability engineering, Tata McGraw-Hill Education.	2004
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Quality Planning and Analysis, J.M.Juran and F.M. Gryna, McGraw Hill	
2	Quality Control, Dale H. Besterfield, 8th Edition, Pearson/Prentice Hall	2008
3	Statistical Quality Control, E. L. Grant and Richard S. Leavenworth, Tata McGraw-Hill	2000
4	Fundamentals of Quality Control and Improvement, Amitava Mitra, 2nd Edition,Prentice Hall	1998
5	Design and Analysis of Experiments, 5th Edition, Douglas C. Montgomery, Wiley-India	2007

5ME5A: SOCIOLOGY AND ELEMENTS OF ECONOMICS FOR ENGINEERS

B.Tech. (Mechanical) 5th semester **Max. Marks: 100**

3L+0T **Exam Hours: 3**

UNIT	**CONTENTS**	**CONTACT HOURS**
I	Introduction to sociological concepts-structure, system, organization, social institutions, Culture social stratification (caste, class, gender, power).State & civil society.	**4**
	Social change in contemporary India: Modernization and globalization, Secularism and communalism, Nature of development,	**4**
II	Processes of social exclusion and inclusion, Changing nature of work and organization.	**4**
	Political economy of Indian society. Industrial, Urban, Agrarian and Tribal society; Caste, Class, Ethnicity and Gender; Ecology and Environment.	**5**
III	Basic Principles and Methodology of Economics. Demand/Supply – elasticity –. Theory of the Firm and Market Structure.	**4**
	Basic Macroeconomic Concepts (including GDP/GNP/NI/Disposable Income) and Identities for both closed and open economies. Aggregate demand and Supply (IS/LM). Price Indices (WPI/CPI), Interest rates, Direct and Indirect Taxes.	**4**
IV	Public Sector Economics –Welfare, Externalities, Labour Market. Components of Monetary and Financial System, Central Bank – Monetary Aggregates; Commercial Banks & their functions; Capital and Debt Markets.	**4**
	Monetary and Fiscal Policy Tools & their impact on the economy – Inflation and Phillips Curve	**4**
	Indian economy Brief overview of post independence period – plans.	**4**

V	Post reform Growth, Structure of productive activity. Issues of Inclusion – Sectors, States/Regions, Groups of people (M/F), Urbanization.	
	Employment–Informal, Organized, Unorganized, Public, Private. Challenges and Policy Debates in Monetary, Fiscal, Social, External sectors.	**4**
		40

TEXT BOOK		
1	Mankiw Gregory N., Principles of Economics, Thompson Asia	2002
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Giddens, A, Sociology, Polity, 6th edn.	2009
2	Haralambos M, RM Heald, M Holborn, Sociology, Collins	2000
3	Xaxa, V, State, Society and Tribes Pearson	2008
4	Chandoke, Neera & Praveen Priyadarshi , Contemporary India: Economy, Society and Politics, Pearson	2009
5	Mohanty, M, Class, Caste & Gender- Volume 5, Sage	2004
6	Ramaswamy, E.A. and Ramaswamy,U., Industry and Labour, OU Press	1981
7	Bhowmik, S (ed.) Street Vendors in the Global Urban Economy, Routledge	2010
8	Rao, M.S.A. (ed.) Urban Sociology, Orient Longmans	1974
9	V. Mote, S. Paul, G. Gupta, Managerial Economics, Tata McGraw Hill	2004
10	Misra, S.K. and Puri , Indian Economy, Himalaya	2009
11	Pareek Saroj , Textbook of Business Economics, Sunrise Publishers	2003

5ME6.1A: COMPUTER AIDED DESIGN AND GRAPHICS

B.Tech. (Mechanical) 5th semester **Max. Marks: 100**

3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**Overview of Computer Graphics:** Picture representation, Coordinate Systems, Raster Scan Display, DDA for line generation and Bresenham's algorithm for line and circle generation; Graphics standards: GKS, IGES, STEP, DXF. Different types of models.	**5**
	Parametric representation of plane curves: line, circle, ellipse, parabola and hyperbola.	**4**
II	**Parametric representation of Space Curves:** Cubic spline curve, Bezier Curve and B Spline Curves. Blending of Curves.	**4**
	Parametric representation of Surfaces: Hermite Bicubic surfaces, Bezier surfaces and Bspline surfaces.	**4**
III	**Solid Representation:** B-rep. and CSG. Comparison between three types of models.	**7**
IV	**Two and Three Dimensional Transformation of Geometric Models:** Translation, Scaling Reflection, Rotation and Shearing, Homogeneous Representation, Combined Transformation.	**4**
	Projection of Geometric models: Parallel and Perspective Projection.	**4**
V	**Clipping:** Point clipping, Line clipping, Cohen- Sutherland algorithm etc., Viewing transformation.	**4**
	Hidden line and surface removal: Techniques and Algorithms. Shading and Rendering.	**4**
	TOTAL	**40**

TEXT BOOK	

1	Zeid and Sivasubramanian,CAD/CAM: Theory and Practice, Tata McGraw Hill	
2	Rogers and Adams, Mathematical Elements for Computer Graphics, Tata McGraw Hill	
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Rao P.N., CAD / CAM Principles and Applications, McGraw Hill.	**2004**
2	Pao Y.C., Elements of Computer Aided Design and Manufacturing, John Wiley and Sons.	**1984**
3	Alavala C.R., CAD/CAM: Concepts and Applications, Prentice Hall of India.	**2008**
4	Xiang and Plastock, Computer Graphics, Schaum's Outlines, Tata McGraw Hill.	**2007**

5ME6.2A: AUTOMOBILE ENGINEERING

B.Tech. (Mechanical) 5th semester **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**Frame & Body:** Layout of chassis, types of chassis frames and bodies, their constructional features and materials.	**3**
	Clutches: single plate, multi-plate, cone clutch, semi centrifugal, electromagnetic, vacuum and hydraulic clutches. Fluid coupling. **Brakes:** Classification and function; Mechanical, hydraulic, vacuum air and self engineering brakes; Brake shoes and lining materials.	**5**
II	**Gear Boxes**: Sliding mesh, constant mesh, synchromesh and epicyclic gear boxes, Automatic transmission system; Hydraulic torque converter;	**4**
	Drives: Overdrive, Propeller shaft, Universal joints, Differential; Rear axle drives. Hotchkiss and torque tube drives; Rear axle types; Front wheel and All wheel drive.	**4**
III	**Wheels and Tyres**: Tyre types, Tyre construction; Tyre inflation pressure, Tyre wear and their causes; Re-treading of the tyre,	**2**
	Steering system: steering gear boxes, Steering linkages, Steering mechanism, Under and Over steering. Steering Geometry, Effect of camber, caster, king pin inclination, toe in and toe out; Power steering; Integral and linkage types	**3**
	Suspension system: objective and requirements, Suspension spring, front and rear suspension systems, Independent suspension system Shock absorbers.	**3**
IV	**Automotive Electrical System:** Battery construction, Charging and testing, battery types, Starting and Battery Charging System: Starter motor construction, types of drive, Alternator construction, regulation and rectification.	**4**
	Ignition System: Magneto and coil ignition systems, System components and requirements, Automotive lighting: Wiring systems Electrical instruments; head lamp, electric horn, fuel level indicator.	**4**
V	**Automotive Air Conditioning:** Introduction, Loads, Air conditioning system Components, Refrigerants, Fault Diagnosis.	**4**
	Automotive Safety: Safety requirements, Safety Devices, Air bags, belts, radio ranging, NVS (Night Vision System) GPS (Global Positioning	**4**

	System)	
	TOTAL	**40**

TEXT BOOK		
1	RP SHARMA,A Course in Automobile Engineering,Dhanpat Rai & Sons	
2	P S Gill,A Text book of Automobile Engineering,KATSON Books VOL 1&2	2010
3	Kirpal Singh,Automobile Engineering, Standard	2003
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	R K Rajpoot,A Text book of Automobile Engineering,Laxmi Publications	2007
2	Jornsen Reimpell, Helmut Stoll,The Automotive Chassis: Engineering Principles,Jurgen Betzler (P) Ltd,	2001

5ME6.3A: STATISTICS FOR DECISION MAKING

B.Tech. (Mechanical) 5th semester **Max. Marks: 100**

3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	Introduction - Statistical Terminology: Descriptive statistics or exploratory data analysis, inferential statistics, population, sample, variable, parameter, statistic, random sample.	3
	Collecting Data: Historical data, types of studies (comparative, descriptive or noncomparative, observational, experimental), sample surveys, sampling and nonsampling errors, bias, representative sample, judgment sampling, quota sampling, simple random samples, sampling rate, sampling frame, stratified random sampling, multistage cluster sampling, probability-proportional-to-size sampling, systematic sampling.	4
II	Summarizing and Exploring Data: Variable types (categorical, qualitative, nominal, ordinal, numerical, continuous, discrete, interval, ratio), summarizing categorical data (frequency table, bar chart, Pareto chart, pie chart), summarizing numerical data (mean, median), skewness, outliers, measures of dispersion (quantiles, range, variance, standard deviation, interquartile range, coefficient of variation) s tandardized z-scores, histogram, bivariate numerical data (scatter plot, simple correlation coefficient, sample covariance), straight line regression, summarizing time-series data, data smoothing, forecasting techniques.	4
	Basic Concepts of Inference: Estimation, hypothesis testing, point estimation, confidence interval estimation, estimator, estimate, bias and variance of estimator, mean square error, precision and standard error, confidence level and limits, null and alternative hypothesis, type I and II error, probabilities of type I and II error, acceptance sampling, simple and composite hypothesis, P-value, one-sided and two -sided tests.	4
III	Inference for Single Samples: Inference for the mean (large samples), confidence intervals for the mean, test for the mean, sample size determination for the z-interval, one-sided and two -sided z-test, inference for the mean (small samples), t distribution.	4
	Inference for Two Samples: Independent sample design, matched pair design, pros and cons of each design, side by side box plots, comparing means of two populations, large sample confidence interval for the difference of two means, large sample test of hypothesis for the difference of two means, inference for small samples (confidence	4

	intervals and tests of hypothesis).	
IV	Inference for Proportions and Count Data: Large sample confidence interval for proportion, sample size determination for a confidence interval for proportion,	**3**
	Large sample hypothesis test on proportion, comparing two proportions in the independent sample design (confidence interval and test of hypothesis), chi-square statistic	**4**
V	Simple Linear Regression and Correlation: Dependent and independent variables, probability model for simple linear regression, least squares fit, goodness of fit of the LS line, sums of squares, analysis of variance, prediction of future observation, confidence and prediction intervals,	**4**
	Multiple Linear Regression: Probability model for multiple linear regression, least squares fit, sums of squares. Use Excel, R, and MATLAB® in the class.	**4**
	TOTAL	**40**

TEXT BOOK		
1	Ajit Tamhane and Dorothy Dunlop "Statistics and Data Analysis: From Elementary to Intermediate" Prentice Hall	1999
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Richard Ivor Levin, David S. Rubin, Statistics for Managements, Prentice Hall International	1988
2	J. K. Sharma, Statistics for Management, Pearson Education India	

5ME7A: HEAT TRANSFER LAB.

B.Tech. (Mechanical) 5th Semester **Max. Marks: 125**
0L+0T+3P **Exam Hours: 3**

SN	NAME OF EXPERIMENT	CONTACT HOURS
1	To Determine Thermal Conductivity of Insulating Powders.	
2	To Determine Thermal Conductivity of a Good Conductor of Heat (Metal Rod).	
3	To determine the transfer Rate and Temperature Distribution for a Pin Fin.	
4	To Measure the Emissivity of the Test plate Surface.	
5	To Determine Stefan Boltzmann Constant of Radiation Heat Transfer.	
6	To Determine the Surface Heat Transfer Coefficient For Heated Vertical Cylinder in Natural Convection.	
7	Determination of Heat Transfer Coefficient in Drop Wise and Film Wise condensation.	
8	To Determine Critical Heat Flux in Saturated Pool Boiling.	
9	To Study and Compare LMTD and Effectiveness in Parallel and Counter Flow Heat Exchangers.	
10	To Find the Heat transfer Coefficient in Forced Convection in a tube.	
11	To study the rates of heat transfer for different materials and geometries	
12	To understand the importance and validity of engineering assumptions through the lumped heat capacity method.	

5ME8A: DYNAMICS OF MACHINES LAB. – II

B.Tech. (Mechanical) 5th Semester **Max. Marks: 75**

0L+0T+2P **Exam Hours: 2**

SN	NAME OF EXPERIMENT	CONTACT HOURS
1	To verify the torque relation for gyroscope.	
2	To plot force vs. radius and lift vs. speed curves for governors.	
3	To plot pressure distribution curves on a journal bearing.	
4	To perform wheel balancing.	
5	To perform static and dynamic balancing on balancing set up.	
6	To determine mass moment of inertia of a flywheel.	
7	Study of a lathe gear box.	
8	Study of a sliding mesh automobile gear box.	
9	Study of a planetary gear box.	

5ME9A: PRODUCTION ENGINEERING LAB.

B.Tech. (Mechanical) 5th Semester **Max. Marks: 100**
0L+0T+3P **Exam Hours: 3**

SN	NAME OF EXPERIMENT	CONTACT HOURS
1	Study of various measuring tools like dial gauge, micrometer, vernier caliper and telescopic gauges.	
2	Measurement of angle and width of a V-groove by using bevel protector..	
3	(a) To measure a gap by using slip gauges (b) To compare & access the method of small-bore measurement with the aid of spheres.	
4	Measurement of angle by using sine bar.	
5	(a) Measurement of gear tooth thickness by using gear tooth vernier caliper. (b) To check accuracy of gear profile with the help of profile projector.	
6	To determine the effective diameter of external thread by using three-wire method.	
7	To measure flatness and surface defects in the given test piece with the help of monochromatic check light and optical flat.	
8	To check the accuracy of a ground, machined and lapped surface - (a) Flat surface (b) Cylindrical surface.	
9	Find out Chip reduction co-efficient (reciprocal of chip thickness ratio) during single point turning.	
10	Forces measurements during orthogonal turning.	
11	Torque and Thrust measurement during drilling.	
12	Forces measurement during plain milling operation.	
13	Measurement of Chip tool Interface temperature during turning using thermocouple technique.	

5ME10A: PROFESSIONAL ETHICS AND DISASTER MANAGEMENT

B.Tech. (Mechanical) 5th Semester **Max. Marks: 50**
0L+0T+2P **Exam Hours: 2**

SN	CONTENTS	CONTACT HOURS
1	**Human values:** Effect of Technological Growth and Sustainable Development. Profession and Human Values: Values crisis in contemporary society. Nature of values. Psychological Values, Societal Values and Aesthetic Values. Moral and Ethical values.	
2	**Professional ethics:** Professional and Professionalism-Professional Accountability, Role of a professional, Ethic and image of profession; Engineering Profession and Ethics: Technology and society, Ethical obligations of Engineering professionals, Roles of Engineers in industry, society, nation and the world; Professional Responsibilities: Collegiality,	

	Loyalty, Confidentially, Conflict of Interest, Whistle Blowing.	
3	**Disaster management:** Understanding Disasters and Hazards and related issues social and environmental. Risk and Vulnerability. Types of Disasters, their occurrence/ causes, impact and preventive measures: Natural Disasters- Hydro-meteorological Based Disasters like Flood, Flash Flood, Cloud Burst, Drought, Cyclone, Forest Fires; Geological Based Disasters like Earthquake, Tsunami, Landslides, Volcanic Eruptions. **Man made Disasters:** Chemical Industrial Hazards, Major Power Break Downs, Traffic Accidents, Fire Hazards, Nuclear Accidents. Disaster profile of Indian continent. Case studies. Disaster Management Cycle and its components.	
4	In order to fulfill objectives of course, a) The institute shall be required to organize at least 3 expert lectures by eminent social workers/professional leaders. b) Each student shall compulsorily be required to: i. Visit a social institution/NGO for at least 7 days during the semester and submit a summary report. ii. II. Perform a case study of a disaster that has occurred in last decade and submit a summary report.	

REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Professional Ethics by R Subramanian, oxford publishers	
2	Engineering Ethics: Concepts and cases by Charles E. Harris, Jr., Michael S. Pritchard, Michael J. Rabins. CENGAGE Learning, Delhi	
3	Controlling Technology: Ethics and Responsible Engineers by Stephen H Unger. John Willey and Sons.	
4	Ethical Issues in Engineering, by Deborah Johnson. Prentice Hall	
5	Human Values in the engineering Profession, Moniograph by A N Tripathi. Published by IIM Calcutta.	
6	Towards Basics of Natural Disaster Reduction by Prof. D.K. Sinha. Researchco Book Center, Delhi.	
7	Understanding Earthquake Disasters by Amita Sinvhal. Tata McGraw Hill, New Delhi.	

6ME1A: DESIGN OF MACHINE ELEMENTS- II

B.Tech. (Mechanical) 6th Semester **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	**CONTENTS**	**CONTACT HOURS**
I	**Fatigue Considerations in Design:** Variable load, loading pattern, endurance stresses, Influence of size, surface finish, notch sensitivity and stress concentration.	**3**
	Goodman line, Soderberg line, Design of machine members subjected to combined, steady and alternating stresses.	**3**
	Design for finite life, Design of Shafts under Variable Stresses, Bolts subjected to variable stresses.	**2**
II	**Design of IC Engine components:** Piston, Cylinder, Connecting Rod and Crank Shaft.	**8**
III	Design of helical compression, tension, torsional springs, springs under variable stresses.	**4**
	Design of belt, rope and pulley drive system,	**4**

IV	**Design of gear teeth:** Lewis and Buckingham equations, wear and dynamic load considerations.	**4**
	Design and force analysis of spur, helical, bevel and worm gears, Bearing reactions due to gear tooth forces.	**4**
V	**Design of Sliding and Journal Bearing:** Methods of lubrication, hydrodynamic, hydrostatic, boundary etc. Minimum film thickness and thermal equilibrium.	**4**
	Selection of anti-friction bearings for different loads and load cycles, Mounting of the bearings, Method of lubrication.	**4**
	TOTAL	**40**

TEXT BOOK		
1	Design of Machine Elements, Bhandari V.B, 3rd Ed., Tata McGraw-Hill, New Delhi	2010
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Machine Design, Sharma and Aggarwal, Kataria and Sons, Delhi.	**1997**
2	Mechanical Engg Design, Shigley, Mischke, Budynas and Nisbett, Tata McGraw-Hill	**2002**
3	PSG Design Data Book, P.S.G. College of Technology, Coimbatore.	**1966**
4	A Text Book of Machine Design, Karwa A., Laxmi Publication.	**2002**
5	Machine Design, Hall, Holwenko and Laughlin, Schaum's Outlines Series, Tata McGraw Hill.	

6ME2A: NEWER MACHINING METHODS

B.Tech. (Mechanical) 6th semester **Max. Marks: 100**

3L+0T **Exam Hours: 3**

UNIT	**CONTENTS**	**CONTACT HOURS**
I	Introduction and classification of advanced machining process, consideration in process selection, difference between traditional and non-traditional process, Hybrid process.	**3**
	Abrasive finishing processes: AFM, MAF (for Plain and cylindrical surfaces).	**4**
II	**Mechanical advanced machining process**: Introduction, Mechanics of metal removal, process principle, Advantages, disadvantages and applications of AJM,USM,WJC.	**6**
III	**Thermo electric advanced machining process**: Introduction, Principle, process parameters,advantages, disadvantages and applications about EDM, EDG,	**4**
	LBM, PAM, EBM	**6**
IV	**Electrochemical and chemical advanced machining process**: ECM, ECG, ESD, Chemical machining,	**5**
	Anode shape prediction and tool design for ECM process. Tool (cathode) design for ECM Process.	**3**
V	Intorduction to Micro and nanomachining, Nanoscale Cutting, Diamond Tools in Micromachining, Conventional Processes: Microturning, Microdrilling and Micromilling, Microgrinding,	**5**
	Non-Conventional Processes: Laser Micromachining, Evaluation of Subsurface Damage in Nano and Micromachining, Applications of Nano and Micromachining in Industry.	**4**
	TOTAL	**40**

TEXT BOOK		
1	Modern Machining Process, Pandey and Shan, Tata McGraw Hill	1980
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Advance Machining Process, Jain V.K., Allied Publishers Ltd.	2002
2	Non Traditional Manufacturing Process, Gary F. Bevedict, Marcel Dekker Inc New York.	1987
3	Non-Conventional Machining Process, Mishra P.K., Narosa Publishing House	2006
4	Non-Conventional Machining Process, J.A. McGeough	1988
5	Nano and Micromachining, J. Paulo Davim, and Mark J. Jackson, Wiley-ISTE	2008

6ME3A: MECHATRONICS

B.Tech. (Mechanical) 6th Semester **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**Introduction:** Introduction, scope and applications of Mechatronics systems. Process control automation, FMS and CNC Machines.	5
	MEMS: Basics of Micro- and Nanotechnology, microprocessor-based controllers and Microelectronics	3
II	**Introduction to Sensors:** Linear and Rotational Sensors, Acceleration, Force, Torque, Power, Flow and Temperature Sensors, Light Detection, Image, and Vision Systems, Integrated Micro-sensors,	4
	Introduction to Actuators: Electro-mechanical Actuators, Electrical Machines, Piezoelectric Actuators, Hydraulic and Pneumatic Actuation Systems, MEMS: Micro-transducers Analysis, Design and Fabrication.	4
III	**Systems and Controls:** The Role of Controls in Mechatronics, Role of Modelling in Mechatronics Design, Signals and Systems: Continuous- and Discrete-time Signals, Z-Transforms and Digital Systems, Continuous- and Discrete-time State-space Models.	5
	Advanced Control Systems: Digital Signal Processing for Mechatronics Applications, Control System Design, Adaptive and Nonlinear Control Design, Neural Networks and Fuzzy Systems, Design Optimization of Mechatronics Systems.	3
IV	**Data Acquisition and related Instrumentation:** Introduction to Data Acquisition Measurement Techniques: Sensors and Transducers, Quantizing theory, Analog to Digital Conversion, Digital to Analog (D/A) conversation, Signal Conditioning.	4
	Real time Instrumentation: Computer-Based Instrumentation Systems, Software Design and Development, Data Recording and Logging.	4
V	**Design of Mechatronics systems:** Introduction of mechatronics systems: Home appliances, ABS (anti-lock braking system) and other areas in automotive engineering, Elevators and escalators, Mobile robots and manipulator arms, Sorting and packaging systems in production lines, Computer Numerically Control (CNC) production machines, Aeroplanes and helicopters, Tank fluid level and temperature control systems.	8
	TOTAL	40
TEXT BOOK		
1	Bolton, W., "Mechatronics: Electronic Control Systems in Mechanical and Electrical Engineering", Pearson Education	2011

REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Mechatronics, HMT Hand Book, Tata McGraw Hill	**2000**
2	Alciatore and Histand, "Introduction to Mechatronics and Measurement Systems", Tata McGraw Hill	**2011**
3	Smaili and Mrad, "Mechatronics: Integrated Technologies for Intelligent Machines", Oxford	**2008**
4	Mahalik N.P., "Mechatronics: Principles, Concepts and applications", Tata McGraw Hill.	**2003**

6ME4A: VIBRATION ENGINEERING

B.Tech. (Mechanical) 6th semester **Max. Marks: 100**
3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**Introduction to Sound:** Frequency dependent human response to sound, Sound pressure dependent human response, Relationship among sound power, sound intensity and sound pressure level.	**2**
	Introduction to Noise: Auditory and Non auditory effects of Noise, Major sources of the noise, Industrial noise sources, Industrial noise control strategies.	**3**
	Introduction to Vibration: Importance and scope of vibrations, terminology and classification, Concept of Degrees of freedom, Harmonic motion, vectorial representation, complex number representation, addition.	**3**
II	**Undamped Single Degree of Freedom System:** Derivation of equation of motion for one dimensional longitudinal, transverse and torsional vibrations without damping using Newton's second law, D' Alembert's principle and Principle of conservation of energy, Compound pendulum and centre of percussion.	**3**
	Damped vibrations of single degree of freedom systems: Viscous damping, under-damped, critically damped and over-damped systems, Logarithmic decrement.	**3**
	Vibration characteristics of Coulomb damped system and Vibration characteristics of Hysteretic damped systems.	**2**
III	**Forced Vibrations of Single Degree of Freedom Systems:** Forced vibration with constant harmonic excitation, Steady state and transient parts, Frequency response curves and phase angle plot, Forced vibration due to excitation of support.	**4**
	Vibration Isolation and Transmissibility: Force transmissibility, Motion transmissibility, Forced vibration with rotating and reciprocating unbalance, Materials used in vibration isolation.	**4**
IV	**System with Two Degrees of Freedom:** principle mode of vibration, Mode shapes, Undamped forced vibrations of two degrees of freedom system with harmonic excitation, Vibration Absorber, Undamped dynamic vibration absorber and centrifugal pendulum absorber	**5**
	Critical Speed of Shaft: Critical speed of a light shaft without damping, critical speed of shaft having multiple discs, secondary critical speed.	**3**
V	**Many Degrees of Freedom Systems (Exact analysis)**: Equation of Motion, The matrix method, Eigen Values and Eigen Vectors, Method of influence Coefficients and Maxwell's reciprocal theorem. Torsional vibrations of multi rotor system, vibrations of geared system, Generalized coordinates and coordinate coupling **Many Degrees of Freedom Systems (approximate methods**): Rayleigh's, Dunkerley's, Stodola's and Holzer's methods	**5**

	Vibrations of continuous systems: Transverse vibration of a string, Longitudinal vibration of a bar, Torsional vibration of a shaft.	**3**
	TOTAL	**40**

TEXT BOOK		
1	Rao S.S., "Mechanical Vibrations", Pearson Education, 2nd Indian reprint.	2004
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Ambekar A.G., "Mechanical Vibrations and Noise Engineering", Prentice-Hall of India Pvt. Ltd.	2006
2	Kelly, S.G., "Mechanical Vibrations, Theory and Applications, Cengage Learning	2013
3	Thomson, W.T., and Dahleh, M.D., Padmanabhan, C., "Theory of Vibrations with Applications", Pearson Education.	2014
4	Meirovitch, L., "Elements of Vibration Analysis", Tata McGraw-Hill	2006
5	Tongue, B.H., "Principles of Vibration", Oxford Publication	2007

6ME5A: STEAM ENGINEERING

B.Tech. (Mechanical) 6th Semester **Max. Marks: 100**
3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**Steam generators:** Classification of Boilers, water and fire tube boilers, High pressure boilers, Advantages of high pr. Boilers, Natural and forced circulation boilers, Water wall.	**4**
	Steam drum internal, steam super heaters, Economizers, air preheater, induced, forced and balanced draught boilers, Fluidized bed boilers	**4**
II	Definition and type of nozzle and diffuser equation of continuity, sonic velocity, mach no. and stagnation properties, the steady flow energy equation for nozzles, momentum energy equation for flow through steam nozzles nozzle efficiency, effect of friction, nozzle for uniform pressure drop, throat pressure for maximum discharge or chock flow, critical pressure ratio, design of nozzle and diffuser.	**8**
III	**Steam Turbines:** Principle and working of steam turbines, type of turbines, compounding for pressure and velocity. Overview and difference of various type of turbine, different types of governing of turbines.	**3**
	Impulse turbine: The effect of blade friction on velocity diagram. Force, work and power, Blade or diagram efficiency, Gross stage efficiency, steam speed to blade, speed ratio for optimum performance, turbine performance at various loads	**5**
IV	**Impulse reaction turbine:** Velocity diagram and work done , degree of reaction, Parson turbine, blade efficiency, gross stage efficiency comparison of enthalpy drop in various stages, size of blades in impulse reaction turbines for various stages of impulse reaction and impulse turbine.	**5**
	Regenerative Feed Heating Cycles: Introduction, Ideal regenerative feed heating cycle, Regenerative heating cycles and their representation on T-s and h-s Diagram, Representation of actual process on T-s and h-s Diagram Regenerative cycles, types of feed heating arrangements, Optimum feed water temperature and saving in Heat Rate. direct contact and surface heaters.	**4**
V	**Reheating of steam**: Practical reheating and Non- reheating cycles, advantage and disadvantages of reheating, reheat regenerative cycle,	**4**

	regenerative water extraction cycles.	
	Process heat and by product power cycle, pass out turbine, Binary vapour cycle. Condensers.	**3**
		40

TEXT BOOK		
1	Steam, Gas Turbine and Power Plant Engineering, Yadav R., CPH Allahabad	
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	A Practical Guide to Steam Turbine, Heinz P. Bloch, McGraw Hill Publication	**1995**
2	Steam Turbines: Design Application and Rerating, Heinz P. Bloch, McGraw Hill Publication.	**1996**
3	Steam Turbine: Theory and Design, Shlykhin P., University press of Pacific.	**2006**
4	Steam Turbine: Theory and Construction, Wilde and Salter, Merchant Books.	**2007**
5	Power Plant Engineering, Nag P.K., Tata McGraw-Hill, New Delhi.	**1992**
6	Thermal Science & Engineering, Kumar D.S., S.K.Kataria & Sons	**2006**
7	Engineering Thermodynamics, Nag P.K., Tata McGraw-Hill, New Delhi	**1998**
8	Fundamentals of Classical Thermodynamics, Gordan J Van Wylen, Willey Eastern Ltd.	**1959**
9	Engineering Thermodynamics, Cengel & Boles, Tata McGraw-Hill, New Delhi.	**2006**
10	Engineering Thermodynamics, Chottopadhyay P., Oxford University Press.	2009

6ME6.1A: NON DESTRUCTIVE EVALUATION AND TESTING

B.Tech. (Mechanical) 6th semester **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**Introduction**: An Overview, Factors influencing the Reliability of NDE, Defects in materials, Defects in composites. NDT methods used for evaluation of materials and composites.	**3**
	Visual Inspection: Basic Principle and Applications.	**2**
	Liquid Penetrant Testing: Principle, Procedure and Test Parameters, Materials, Limitations and Applications.	**3**
II	**Radiographic Inspection:** Principles of X – ray radiography, equipment, Absorption, Scattering, X-ray film processing, General radiographic procedures, Reading and Interpretation of Radiographs, Industrial radiographic practice, Limitations and Applications, Welding defects detection. Gamma ray radiography.	**8**
III	**Ultrasonic Testing:** Principle of wave propagation, Ultrasonic equipment, Variables affecting an ultrasound test, Basic methods: Pulse Echo and Through Transmission, Types of scanning.	**5**
	Applications of UT: Testing of products, Welding Inspection, Tube Inspection, Thickness Measurement, Elastic Constant Determination, Ultrasonic testing of composites.	**3**
IV	**Magnetic Particle Inspection:** Methods of generating magnetic field, Demagnetization of materials, Magnetic particle test: Principle, Test Equipment and Procedure, Interpretation and evaluation.	**5**
	Introduction to Accostic Emission Testing and Thermography.	**3**
V	**Eddy Current Testing:** Principle of eddy current, Factors affecting eddy currents, Test system and test arrangement, Standardization and	**5**

	calibration, Application and effectiveness.	
	Comparison and Selection of NDT Methods, Codes and Standards	**3**
	TOTAL	**40**

TEXT BOOK		
1	Baldev Raj, T. Jay Kumar, M. Thavasimuthu, Practical Non-Destructive Testing, Narosa.	
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Loius Cartz, Non Destructive Testing, ASM International	**1995**
2	J PRASAD, C G K NAIR, NDT & Evaluation Of Materials, Tata McGraw Hill	**2008**
3	R. Halmshaw, Introduction to the Non-Destructive Testing of Welded Joints,	**1997**
4	American Metals Society, Non-Destructive Examination and Quality Control, Metals Hand Book, Vol.17, 9th Ed.	**1989**

6ME6.2A: DESIGN AND MANUFACTURING OF PLASTIC PRODUCTS

B.Tech. (Mechanical) 6th semester **Max. Marks: 100**

3L+0T **Exam Hours: 3**

UNIT	**CONTENTS**	**CONTACT HOURS**
I	Plastics Materials: An Overview, Classification, Thermoplastics, Thermosets, Crystalline, Amorphous, and Liquid, Crystalline Polymers, Copolymers, Alloys, Elastomers, Additives, Reinforcements, and Fillers, Physical Properties and Terminology.	**5**
	Mechanical Properties, Thermal Properties, Electrical Properties, Environmental Considerations.	**3**
II	**Design Considerations for Injection-Molded Parts**: Injection Molding Process, Design Strategy, Efficient and Functional Design, Material Selection,	**2**
	Nominal Wall Thickness, Normal Ranges of Wall Thickness, Structural Requirements of the Nominal Wall,	**2**
	Insulation Characteristics of the Nominal Wall, Impact Response of the Nominal Wall, Draft, Structural Reinforcement, Ribs, Other Geometric Reinforcement, Bosses, Coring, Fillets and Radii, Undercuts	**4**
III	Polymer processing techniques such as extrusion, compression and transfer moulding.	**4**
	Injection moulding, blow moulding, thermoforming, rotational moulding, calendaring.	**4**
IV	Assembly: General Types of Assembly Systems, Molded-In Assembly Systems, Snap-Fit Assembly, Molded-In Threads, Press-Fits, Chemical Bonding Systems, Solvent Welding, Adhesive Bonding, Thermal Welding Methods.	**4**
	Spin Welding, Radio Frequency (RF) Welding, Electromagnetic or Induction Welding, Assembly with Fasteners, Bolted Assembly, Threaded Metal Inserts, Self-Tapping Screws, Riveted Assembly, Sheet Metal Nuts, Specialty Plastic Fasteners	**4**
V	Machining of Plastics: Drilling and Reaming,Thread Tapping, Sawing, Milling, Turning, Grinding.	**4**
	Finishing and Decorating of Plastics: Painting, Vacuum Metallizing and Sputter Plating, Electroplating, Flame Spraying/Arc Spraying, Hot Stamping	**4**
	TOTAL	**40**

TEXT BOOK		
1	Design and Manufacture of Plastic Parts, R.L.E. Brown, John Wiley and Sons, New York	1980
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Designing with Plastics, Gerhard, Hanser Verlag	
2	Handbook of Plastics Joining: a practical guide, PDL handbook series, Plastics Design Library, William Andrew	
3	Modern Plastics Handbook, McGraw Hill handbooks, Modern plastics series, Charles A. Harper, McGraw-Hill Professional	1997
4	Industrial Plastics: theory and applications, Erik Lokensgard and Terry L. Richardson, 4th Edition, Cengage Learning	2000

6ME6.3A: MAINTENANCE MANAGEMENT

B.Tech. (Mechanical) 6th semester **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	Introduction -Fundamentals of Maintenance Engineering. Maintenance Engineering its importance in material & energy conservation, inventory control, productivity, safety, pollution control etc.	3
	Safety Regulations, pollution problems, human reliability, total quality management (TQM), total productivity maintenance (TPM), environmental issues in maintenance, ISO 9000.	4
II	Maintenance Management - types of maintenance strategies, Planned and unplanned maintenance, breakdown, preventive & predictive maintenance. Their comparison, advantages & disadvantages. Limitations.	4
	Computer aided maintenance, maintenance scheduling, spare part management, inventory control, organisation of maintenance department.	4
III	Tribology in Maintenance, friction wear and lubrication, friction & wear mechanisms, prevention of wear, types of lubrication mechanisms, lubrication processes.	3
	Lubricants - types, general and special purpose, additives, testing of lubricants, degradation of lubricants, seal & packings.	3
	Repair methods for basic machine elements: Repair methods for beds, slideways, spindles, gears, lead screws and bearings–Failure analysis–Failures and their development–Logical fault location methods–Sequential fault location.	3
IV	Machine Health Monitoring - Condition based maintenance, signature analysis, oil analysis, vibration, noise and thermal signatures, on line & off line techniques,	4
	Instrumentation & equipment used in machine health monitoring. Instrumentation in maintenance, signal processing, data acquisition and analysis, application of intelligent systems, data base design.	4
V	Reliability, availability & maintainability (RAM) Analysis - Introduction to RAM failure mechanism, failure data analysis, failure distribution, reliability of repairable and non repairable systems.	4
	Improvement in reliability, reliability testing, reliability prediction, utilisation factor, system reliability by Monte Carlo Simulation Technique.	4
	TOTAL	**40**

TEXT BOOK		
1	Anthony Kelly, Strategic Maintenance Planning, Butterworth-Heinemann	2006
2	R. C. Mishra, K. Pathak ,Maintenance Engineering and Management, PHI Learning Pvt. Ltd	2012
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Higgins L.R., "Maintenance Engineering Hand book", McGraw Hill	1988
2	Maintenance & Spare parts Management Gopal Krishnan	
3	Srivastava S.K., "Industrial Maintenance Management", S. Chand and Co	1981
4	Hand book of Condition Monitoring CNR Rao	
5	White E.N., "Maintenance Planning", I Documentation, Gower Press	1979
6	Armstrong, "Condition Monitoring", BSIRSA	1988
7	Davies, "Handbook of Condition Monitoring", Chapman &Hall,	1996

6ME7A: MACHINE DESIGN SESSIONAL-II

B.Tech. (Mechanical) 6th Semester **Max. Marks: 125**
0L+0T+3P **Exam Hours: 3**

SN	SESSIONAL WORK	CONTACT HOURS
	Problems on:	
1	Fatigue loading.	
2	Helical compression, tension and torsional springs design.	
3	Curved Beams.	
4	Preloaded bolts and bolts subjected to variable stresses.	
5	Belt, Rope and Chain drive system.	
6	Gear Design.	
7	Sliding contact bearing design.	
8	Anti-friction bearing selection	

6ME8A: INDUSTRIAL ENGINEERING LAB-I

B.Tech. (Mechanical) 6th Semester **Max. Marks: 75**
0L+0T+2P **Exam Hours: 2**

SN	SESSIONAL WORK	CONTACT HOURS
1	Case study on X bar charts and process capability analysis	
2	P Chart: (a)Verify the Binomial Distribution of the number of defective balls by treating the balls with a red colour to be defective. (b) Plot a P-chart by taking a sample of n=20 and establish control limits	
3	To plot C-chart using given experimental setup	
4	Operating Characteristics Curve: (a) Plot the operating characteristics curve for single sampling attribute plan for n = 20 ; c = 1 , 2 , 3 Designate the red ball to defective. (b) Compare the actual O.C. curve with theoretical O.C. curve using approximation for the nature of distribution	
5	Distribution Verification: (a) Verification of Normal Distribution. (b) To find the distribution of numbered cardboard chips by random drawing one at a time with replacement. Make 25 subgroups in size 5 and 10 find the type of distribution of sample average in	

	each case. Comment on your observations	
6	Verification of Poisson distribution	
7	Central Limit Theorem: (a) To show that a sample means for a normal universe follow a normal distribution (b) To show that the sample means for a non normal universe also follow a normal Distribution.	
8	Solve problems using available Statistical Process Control software in lab	

6ME9A: MECHATRONICS LAB

B.Tech. (Mechanical) 6th Semester **Max. Marks: 75**
0L+0T+2P **Exam Hours: 2**

SN	SESSIONAL WORK	CONTACT HOURS
Perform any ten experiments from the list given below		
1	Study the following devices (a) Analog & digital multimeter (b) Function/ Signal generators (c) Regulated d. c. power supplies (constant voltage and constant current operations)	
2	Displacement Measurement using Capacitive & inductive Pick –ups.	
3	Study of Speed Measurement System: (a) Magnetic Pick-up (b) Stroboscope	
4	Study of Load Measurement System Load Cell	
5	Measurement of temperature using thermocouple, thermistor and RTD	
6	Measurement of displacement using POT, LVDT & Capacitive transducer	
7	Torque measurement using torque measuring devices	
8	Strain Measurement using strain gauge	
9	Frequency to Voltage Converter and vice versa	
10	Position and velocity measurement using encoders	
11	Study on the application of data acquisition system for industrial purposes	
12	Speed control of DC motor using PLC.	
13	Study of Load Measurement System Load Cell	

6ME10A: VIBRATION ENGINEERING LAB.

B.Tech. (Mechanical) 7th Semester **Max. Marks: 75**
0L+0T+2P **Exam Hours: 2**

SN	NAME OF EXPERIMENT	CONTACT HOURS
1	To verify relation $T = 2\pi\sqrt{(l/g)}$ for a simple pendulum.	
2	To determine radius of gyration of compound pendulum.	
3	To determine the radius of gyration of given bar by using bifilar suspension.	
4	To determine natural frequency of a spring mass system.	
5	Equivalent spring mass system.	
6	To determine natural frequency of free torsional vibrations of single rotor system. i. Horizontal rotor ii. Vertical rotor	
7	To verify the Dunkerley's rule.	
8	Performing the experiment to find out damping co-efficient in case of free damped torsional vibration	
9	To conduct experiment of trifler suspension.	
10	Harmonic excitation of cantilever beam using electro-dynamic shaker and determination of resonant frequencies.	

11	Study of Vibration measuring instruments.	
12	Perform study of the following using Virtual Lab http://www.vlab.co.in/	
13	Forced Vibration of a Cantilever Beam with a Lumped Mass at Free End: To calculate the natural frequency and damping ratio for forced vibration of a single DOF cantilever beam system, experimentally; and compare the results with theoretical values.	
14	Harmonicaly Excited Forced Vibration of a Single DOF System: To analyze the forced vibration response of a single DOF system at different damping ratio and frequency ratio.	
15	Perform study of the following using Virtual Lab http://www.vlab.co.in/	
16	Forced Vibration of a Cantilever Beam with a Lumped Mass at Free End: To calculate the natural frequency and damping ratio for forced vibration of a single DOF cantilever beam system, experimentally; and compare the results with theoretical values.	
17	Harmonicaly Excited Forced Vibration of a Single DOF System: To analyze the forced vibration response of a single DOF system at different damping ratio and frequency ratio.	

7ME1A: FINITE ELEMENT METHODS

B.Tech. (Mechanical) 7th semester **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	Introduction to FEM and its applicability, Review of :Matrix algebra, Gauss elimination method, Uniqueness of solution, Banded symmetric matrix and bandwidth.	**4**
	Structure analysis: Two-force member element, Local stiffness matrix, coordinate transformation, Assembly, Global stiffness matrix, imposition of Boundary conditions, Properties of stiffness matrix	**4**
II	One-dimensional Finite Element Analysis: Basics of structural mechanics, stress and strain tensor, constitutive relation, Principle of minimum Potential, General steps of FEM, Finite element model concept / Discretization, Derivation of finite elements, equations using potential energy approach for linear and quadratic 1-D bar element,	**5**
	shape functions and their properties, Assembly, Boundary conditions, Computation of stress and strain.	**3**
III	Two Dimensional Finite Element Analysis: Finite element formulation using three nodded triangular (CST) element , Plane stress and Plain strain problems,	**4**
	Shape functions, node numbering and connectivity, Assembly, Boundary conditions, Isoparametric formulation of 1-D bar elements,	**2**
	Numerical integration using gauss quadrature formula, computation of stress and strain.	**2**
IV	Finite Element Formulation from Governing Differential Equation: Method of Weighted Residuals, Collocation, Sub domain method, Least Square method and Galerkin's method,	**5**
	Application to one dimensional problems, one-dimensional heat transfer, etc. introduction to variational formulation (Ritz Method.)	**3**
V	Higher Order Elements: Lagrange's interpolation formula for one and two independent variable, Convergence of solution, compatibility, element continuity, static condensation, p and h methods of mesh refinement, Aspect ratio and element shape,	**5**
	Application of FEM, Advantages of FEM, Introduction to concept of element mass matrix in dynamic analysis.	**3**
	TOTAL	**40**

TEXT BOOK		
1	Seshu P.,"Text Book of Finite Element Analysis", Prentice Hall India	2003
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Dixit, U. S., "Finite Element Methods for Engineers" Cengage Learning	2003
2	Finite Element Procedure in Engineering Analysis, Bathe K.J., Prentice Hall India.	2001
3	An Introduction to the Finite Element Method, Reddy J.N., Tata McGraw-Hill, New Delhi	1993
4	Concepts & Applications of Finite Element Analysis, Cook and Plesha, Willey India New Delhi.	2007
5	Introduction to Finite Elements in Engineering, Chandupatla and Belegundu, Prentice Hall India.	1999

7ME2A: REFRIGERATION AND AIR CONDITIONING

B.Tech. (Mechanical) 7th Semester **Max. Marks: 100**

3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**Introduction:** Refrigeration and second law of Thermodynamics, Refrigeration effect and unit of Refrigeration, Heat pump, reversed Carnot cycle. **Vapour Compression Refrigeration System:** Analysis of simple vapour compression Refrigeration cycle by p-h and T-S diagram. Effect of operating conditions	5
	Multiple Evaporator and compressor system: Application, air compressor system, Individual compressor, compound compression, cascade system. Application, air compressor systems, individual compressor, compound compression, cascade system.	3
II	**Gas Cycle Refrigeration:** Limitation of Carnot cycle with gas, reversed Brayton cycle, Brayton cycle with regenerative heat exchanger.	4
	Air cycle for air craft: Necessity of cooling of air craft, Basic cycle, boot strap, regenerative type air craft refrigeration cycle.	4
III	**Other refrigeration systems** (description only): Vapour absorption refrigeration system, Electrolux refrigerator, Lithium Bromide - Water system, Water vapour refrigeration system, Vortex tube refrigeration system, thermo electric refrigeration system.	4
	Refrigerants: Classification, Nomenclature, selection of Refrigerants, global warming potential of CFC Refrigerants. Refrigeration Equipments: Compressor, condenser, evaporator, expansion devices, types & working.	4
IV	**Psychrometry:** Psychrometric properties, psychometric relations, pyschrormetric charts, psychrometric processes, cooling coils, By-pass factor, Apparatus Dew point temperature and air washers.	5
	Human Comfort: Mechanism of body heat losses, factors affecting human comfort, effective temperature, comfort chart.	3
V	**Cooling load calculations:** Internal heat gain, system heat gain, RSHF, ERSHF, GSHF, cooling load estimation, heating load estimation, psychrometric calculation for cooling.	5
	Selection of air conditioning: Apparatus for cooling and dehumidification, Air conditioning system, year round air conditioning.	3
	TOTAL	**40**

TEXT BOOK		
1	Arora, C.P., Refrigeration and Air Conditioning, Tata McGraw Hill	
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Stoecker W.F., "Refrigeration & Air Conditioning" McGraw Hill Publication.	**2000**
2	Andrew D. Althouse., "Modern Refrigeration & Air Conditioning" GoodHeart-Willcox Co.	**2002**
3	Jorden & Priester, Refrigeration & Air Conditioning, Prentice Hall of India.	**2003**
4	Roy J. Dossat, Principal of Refrigeration, Pearson Education, New Delhi.	**2014**
5	Edward G. Pita, Air Conditioning Principles and Systems, Pearson Education, New Delhi.	**2003**
6	Jain V.K., Refrigeration & Air Conditioning, Tata McGraw Hill New Delhi.	2004

7ME3A: OPERATIONS RESEARCH

B.Tech. (Mechanical) 7th semester **Max. Marks: 100**
3L+1T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**Overview of Operations Research**	**1**
	Linear Programming: Applications and model formulation, Graphical method, Simplex method, duality and Sensitivity analysis.	**4**
	Transportation Model and Assignment Model including travelling salesman problem.	**4**
II	**Integer Linear Programming:** Enumeration and cutting Plane solution concept, Gomory's all integer cutting plane method, Branch and Bound Algorithms, applications of zero-one integer programming.	**5**
	Replacement Models: Capital equipment replacement with time, group replacement of items subjected to total failure.	**3**
III	**Queuing Theory**: Analysis of the following queues with Poisson pattern of arrival and exponentially distributed service times, Single channel queue with infinite customer population, Multichannel queue with infinite customer population,	**3**
	Competitive Situations and Solutions: Game theory, two person zero sum game, saddle point, minimax (maximin) method of optimal strategies, value of the game. Solution of games with saddle points, dominance principle. Rectangular games without saddle point – mixed strategy, approximate solution, and simplified analysis for other competitive situations. Application of linear programming	**4**
IV	Theory of Decision making: Decision making under certainty, risk and uncertainty. Decision trees.	**5**
	Deterministic Inventory control models: functional role of inventory, inventory costs, model building, Single item inventory control model without shortages, with shortage and quantity discount. Inventory control model with uncertain demand, service level, safety stock, P and Q systems, two bin system. Single period model. Selective Inventory control techniques.	**4**
V	**Probabilistic Inventory control models:** Instantanoues demand without setup cost and with setup cost, Continuous demand without setup cost	**4**
	Simulation: Need of simulation, advantages and disadvantages of simulation method of simulation. Generation of Random numbers, Generation of Normal Random numbers. Use of random numbers for	**4**

	system simulation. , Monte Carlo simulation, simulation language ARENA, Application of simulation for solving queuing Inventory Maintenance, Scheduling and other industrial problems	
	TOTAL	**40**

TEXT BOOK		
1	Operations Research, Ravindran, Phillips and Solberg, Wiley India.	
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Introduction to Operations Research, Hillier F.S. and Lieberman G.J., CBS Publishers.	
2	Operations Research, Taha H.A., Pearson Education	
3	Linear Programming and Network Flows, Bazaraa, Jarvis and Sherali, Wiley India.	
4	Principles of Operations Research, Wagner H.M., Prentice Hall of India.	
5	Operations Research, Gupta and Heera, S. Chand Publications.	

7ME4A: TURBOMACHINES

B.Tech. (Mechanical) 7th Semester **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**Basic Concepts of Turbo Machines:** Definition & classification of Turbo machine, Basic laws and governing equations: continuity equation, steady flow energy equation(1st law of thermodynamics),2nd law of thermodynamics applied to turbo machines, Newton's 2nd law of motion applied to turbomachines - Euler's pump equation and Euler's turbine equation	**4**
	Dimensional analysis applied to hydraulic machines, power coefficient, flow coefficient, head coefficient, non-dimensional specific speed, Range of specific speeds for various turbo machines, Dimensional analysis applied to compressible flow machines, pressure ratio as a Function of temperature ratio, mass flow rate parameter and speed parameter	**4**
II	**Centrifugal Compressors and Fans:** Components and description, velocity iagrams, slip factor, energy transfer, power input factor, stage pressure rise and loading coefficient, pressure coefficient, degree of reaction, Centrifugal compressor characteristic, surging, rotating Stall and Choking	**3**
	Axial Flow Compressors and Fans: Basic constructional features, Advantages of axial flow compressors, working principle, velocity triangle, elementary theory, stage work, work done factor, stage loading, degree of reaction; vortex theory, simple design calculations, introduction to blade design, cascade test, compressibility effects, operating characteristics	**3**
	Reciprocating Compressors: Basic constructional features, working principle, work done calculation, single and double acting compressors	**2**
III	**Centrifugal Pumps:** Main parts, work done and velocity triangles, slip and slip factor, pump losses and efficiencies, minimum starting speed, net positive suction head, performance curve.	**3**
	Axial Flow Pumps: Description, velocity triangles, work done on the fluid, energy transfer, axial pump characteristics, cavitation.	**3**
	Reciprocating Pumps: Classification, component and working, single acting and double acting, discharge, work done and power required, coefficient of discharge, indicator diagram, slip, effect of friction and	**2**

	acceleration, theory of air vessels.	
IV	**Gas power cycles**: Ideal and practical gas turbine cycle, heat exchange cycle, reheat cycle, intercooled cycle, Comparison of various cycles.	**4**
	Thermodynamic Cycles: Advantages, disadvantages and performance characteristics of Ram jet engine, pulse jet engine, turbo prop engine, turbo jet engine, turbo fan engine, Calculation of specific thrust and efficiency	**4**
V	**Gas Turbines:** impulse and reaction type gas turbines, Velocity triangles and calculation of work done, efficiency etc.	**8**
	TOTAL	**40**

TEXT BOOK		
1	Gas turbines, V. Ganesan, Tata McGraw-Hill	
2	Subramanya, K., Hydraulic Machine, Tata McGraw Hill	2013
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Principle of Turbo Machinery, Turton R.K., Springer Publication	**1994**
2	Fundamentals of Turbo Machinery, William W., John Wiley and Sons.	**2008**
3	Turbo Machinery Basic Theory and Application, Logan E.J.	**1981**
4	Principles of Turbo Machinery, Shepherd Dennis G., Mac Millan Publisher, New York.	1956
5	TurboMachines, A Valan Arasu, Vikas Publishing House Pvt. Ltd.	**2009**
7	Gas turbine theory, Cohen and Saravanamutto, Pearson Educational Publication	**2009**
8	Hydraulic Machine: Turbines and Pumps, Nazarov N.T., Springer New York.	**2003**
9	Gas Turbine Theory, Cohen and Roger, Pearson Education.	
10	Hydraulic Machinery, Jagdish Lal, Metropolitan Books.	

7ME5A: OPERATIONS MANAGEMENT

B.Tech. (Mechanical) 8th semester **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	Introduction to operations management (OM), the scope of OM; Historical evolution of OM; Trends in business; the management process. Operations Strategy, Competitiveness and Productivity	**3**
	Demand Forecasting: components of forecasting demand, Approaches to forecasting: forecasts based on judgment and opinion, Time series data. Associative forecasting techniques, Accuracy and control of forecasts, Selection of forecasting technique.	**4**
II	Product and Service design, Process selection, Process types, Product and process matrix, Process analysis.	**3**
	Capacity Planning: Defining and measuring capacity, determinants of effective capacity, capacity strategy, steps in capacity planning process, determining capacity requirements, Capacity alternatives, Evaluation of alternatives; Cost-Volume analysis.	**2**
III	Facility Location: Need for location decisions, factors affecting location, qualitative and quantitative techniques of location. Facilities layout: Product, Process, Fixed position, combination and cellular layouts; Designing product and process layout, line balancing. Material Handling	**4**
	Planning levels: long range, Intermediate range and Short range	**4**

	planning, Aggregate planning: Objective, Strategies, and techniques of aggregate planning. Master scheduling; Bill of materials, MRP; inputs processing and outputs, and overview of MRPII , use of MRP to assist in planning capacity requirements, Introduction to ERP	
IV	Production Control: Capacity control and priority control, production control functions; Routing, scheduling, dispatching, expediting and follow up. Techniques of production control in job shop production, batch production and mass production systems. sequencing: priority rules, sequencing jobs through two work centers, scheduling services	**4**
	Introduction to Just-in-time (JIT) and Lean Operations: JIT production, JIT scheduling, synchronous production, Lean operations system	**4**
V	Supply Chain Management (SCM): Need of SCM, Bullwhip effect, Elements of SCM, Logistics steps in creating effective supply chain, Purchasing and supplied management.	**3**
	Project Management: Nature of projects, project life cycle, Work breakdown structure, PERT and CPM, Time-Cost trade-offs: Crashing. Resource allocation, leveling	**5**
	TOTAL	**40**

TEXT BOOK		
1	Stevenson, Operations Management, Tata McGraw Hill.	2009
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Roberta S. Russell, Bernard W. Taylor, Operations Management, John Wiley & Sons	2010
2	Joseph S. Martinich, Production And Operations Management, John Wiley & Sons	2008
3	S.N. Chary, Production and Operations Management, Tata McGraw Hill	2009
4	Norman Gaither, Greg Frazier, Operations Management, Thomson Learning	2002
5		

7ME6.1A MICRO AND NANO MANUFACTURING

B.Tech. (Mechanical) 7th semester **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	Nanoscale Cutting:- Introduction, Material representation and microstructure, Atomic interaction; Nonomachining:- Introduction, Nanometric machining, Theoretical basis of machining;	**4**
	Meso-micromcahining:- Introduction, size effects in micromachining, mechanism for large plastic flow, origin of the size effect, Meso-machining processes. Product quality in micromachining, Burr formation in micromachining operations.	**4**
II	Microturning:- Characteristic features and applications, Microturning tools and tooling systems, Machine tools for microturning	**2**
	Microdrilling: Characteristic features and applications, Microdrills and tooling systems, Machine tools for microdrilling Micromilling:- Characteristic features and applications, Micromills and	**2**

	tooling systems, Machine tools for micromilling,	
	Micro machining high aspect ratio microstructures, micromolding, micromolding processes, micromolding tools, micromold design, micromolding applications, limitations of micromolding.	**3**
III	**Microgrinding and Ultra-precision Processes:** Introduction, Micro and nanogrinding, Nanogrinding apparatus, Nanogrinding procedures, Nanogrinding tools, Preparation of nanogrinding wheels, Bonding systems, Vitrified bonding	**4**
	Non-Conventional Processes: Laser Micromachining:- Introduction, Fundamentals of lasers, Stimulated emission, Types of lasers, Laser microfabrication, Nanosecond pulse microfabrication, Shielding gas, Effects of nanosecond pulsed microfabrication, Picosecond pulse microfabrication, Femtosecond pulse microfabrication, Laser nanofabrication.	**4**
IV	**Diamond Tools in Micromachining:** Introduction, Diamond technology, Hot Filament CVD (HFCVD), Preparation of substrate, Selection of substrate material, Pre-treatment of substrate, Modified HFCVD process.	**4**
	Deposition on complex substrates, Diamond deposition on metallic (molybdenum) wire, Deposition on WC-Co microtools, Diamond deposition on tungsten carbide, (WC-Co) microtool, Performance of diamond-coated microtool	**4**
V	**Evaluation of Subsurface Damage in Nano and Micromachining:** Introduction, Destructive evaluation technologies, Cross-sectional microscopy, Preferential etching, Angle lapping/angle polishing, X-ray diffraction, Micro-Raman spectroscopy.	**4**
	Applications of Nano and Micromachining in Industry: Introduction, Typical machining methods, Diamond turning, Shaper/planner machining, Applications in optical manufacturing, Aspheric lens, Fresnel lens, Microstructured components, Semiconductor wafer production.	**5**
	TOTAL	**40**

TEXT BOOK		
1	Micro and Nano manufacturing by Marks J. Jackson springer	2008
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	J. Paulo Davim, Mark J. Jackson, Nano and Micromachining ISTE Ltd John Wiley & Sons, Inc.	2009

7ME6.2A: ROBOTICS

B.Tech. (Mechanical) 7th semester **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**Introduction to Robotics:** Evolution of Robots and Robotics, Laws of Robotics, What is and What is not a Robot, Progressive Advancement in Robots.	**3**
	Robot Anatomy, Human Arm Characteristics, Design and Control Issues, Manipulation and Control, Sensors and Vision, Programming Robots, The Future Prospects, Notations.	**5**
II	**Robot End Effectors:** Classification of end effectors, drive system for grippers, Mechanical, Magnetic, Vaccum, Adhesive grippers, Hooks,	**4**

	Scoops, Miscellaneous devices, Gripper force analysis and Design, Active and Passive Gripeers	
	Coordinate Frames, Mapping and Transforms: Coordinate Frames, Description of Objects in Space, Transformation of Vectors, Inverting a Homogeneous Transform, Fundamental Rotation Matrices.	**4**
III	**Symbolic Modeling of Robots:** Direct Kinematic Model, Mechanical Structure and Notations, Description of Links and Joints, Kinematic Modeling of the Manipulator,	**3**
	Denavit – Hartenberg Notation, Kinematic Relationship between Adjacent Links, Manipulator Transformation Matrix. Introduction to Inverse Kinematic model, Solvability of Inverse Kinematics model, Solution techniques.	**5**
IV	**Robotic Sensors:** The Meaning of Sensing, Sensors in Robotics, Kinds of Sensors used in Robotics, Choosing the right sensors	**3**
	Robotic vision: Introduction to Robotic Vision, Industrial Applications of Vision-Controlled Robotic Systems, Process of Imaging, Architecture of Robotic Vision Systems, Image Acquisition, Image Representation and Image Processing	**5**
V	**Robot Applications:** Industrial Applications, Material Handling, Processing Applications, Assembly Applications, Inspection Application, Principles for Robot Application and Application Planning, Justification of Robots, Robot Safety, Non-Industrial Applications.	**4**
	Robot Programming: Robot languages, Classification of Robot language, Computer control and robot software, VAL system and language	**4**
	TOTAL	**40**

TEXT BOOK		
1	Mittal R.K., Nagarath, I.K., Robotics and Control, Tata Mc Graw Hill,	**2007**
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Deb S.R., Robotics Technology and Flexible Automation, Tata McGraw Hill	**2010**
2	Ghoshal, A., Robotics Fundamental Concepts and Analysis, Oxford University Press	**2010**
3	Craig JJ, Introduction to Robotics, Mechanics and Control, Addison-Wesley, 2nd Ed.	**2004**
4	Fu, K.S., Gonzales, R.C. and Lee, C.S.G., Robotics: Control, Sensing, Vision and Intelligence, McGraw Hill	**1987**
5	Groover, M. P., Wiess, M., Nagel, R. N. and Odery, N. G. Industrial Robotics- Technology, Programming and Applications, McGraw Hill Inc. Singapore	**2000**
9		

7ME6.3A: CNC MACHINES AND PROGRAMMING

B.Tech. (Mechanical) 7th semester **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	**CONTENTS**	**CONTACT HOURS**
I	Introduction: Definition of NC, Applications of NC ,Historical Developments in Automation, Classification of NC Systems, Comparison of NC and Conventional Machines, Advantages of NC	**8**
II	NC Hardware: Architecture of NC Systems, Design Considerations, Mechanical Elements, Structure, Guideways and Slides, Guideway Elements, Transmission Systems, Spindle Unit, Coolant system, Lubrication System, Tool and work Changing Mechanisms, Electrical	**8**

	Elements, Drives, Sensors, Control Loops, Computing Elements/ Firmware, Interpolators	
III	NC Software: Introduction, Manual Part Programming, Computer-Assisted Part Programming, Language Based , Geometric Modeling Based, Automatic Part Program Generation,	**8**
IV	CAPP Systems , 5 Axis Programming, Post-Processing, Programming Robots and CMMs	**4**
	NC Simulation, Kinematic simulation, Volumetric simulation, Applications of Volumetric NC Simulation, Verification	**4**
V	Advanced Topics:, Adaptive Control, Off-line adaptive control, Various optimisation criteria, Hardware Based AC, Software Based AC, Tooling and Instruments for NC Special Considerations in High Speed Cutting (HSC) and Die Sinking, Rapid Product Development, CAM, FMS, CIM	**8**
	TOTAL	**40**

TEXT BOOK		
1	Krar S. and Gill A., CNC: Technology and Programming, McGraw Hill	1990
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Koren Y., Computer Control of Manufacturing Systems, Tata McGraw Hill.	1983
2	Pressman R.S. and Williams J.E., Numerical Control and Computer-Aided Manufacturing, John Wiley & Sons	1977,
3	Jones B.L., Introduction to Computer Numerical Control, John Wiley & Sons.	1986
4	Kral I.H., , Numerical Control Programming in APT, Prentice-Hall	1986
5	Chang C.H. and Melkanoff M.A., ,NC Machine Programming and Software Design, Prentice-Hall	1986

7ME7A: THERMAL ENGINEERING LAB-II

B.Tech. (Mechanical) 7th Semester **Max. Marks: 100**
0L+0T+3P **Exam Hours: 3**

SN	**LABORATORY WORK/NAME OF EXPERIMENT**	**CONTACT HOURS**
1	To perform constant speed load test on a single cylinder diesel engine and to plot performance curves: indicated thermal efficiency, brake thermal efficiency, mechanical efficiency Vs. Brake power, and heat balance sheet.	
2	To estimate the Indicated Power, Friction Power and Mechanical Efficiency of a multi-cylinder Petrol Engine. (Morse Test)	
3	Analysis of engine exhaust gases using Orsat apparatus / gas analyzer.	
4	To study refrigeration cycle, determination of coefficient of performance of cycle and tonnage capacity of refrigeration unit.	
5	To determine the COP and tonnage capacity of a Mechanical heat pump.	
6	To study various controls used in Refrigeration and Air conditioning system.	
7	Determination of dryness fraction of steam.	
8	Study and Performance of Simple Steam Turbine	
9	Performance characteristics of Pelton wheel turbine.	
10	Performance characteristics of Francis turbine.	
11	Performance characteristics of Kaplan turbine.	
12	Performance characteristics of variable speed centrifugal pump.	
13	Performance characteristics of rated speed centrifugal pump.	

7ME8A: FINITE ELEMENT LAB.

B.Tech. (Mechanical) 7th Semester **Max. Marks: 100**
0L+0T+3P **Exam Hours: 3**

SN	LABORATORY WORK/NAME OF EXPERIMENT	CONTACT HOURS
1	Laboratory work for the solution of solid mechanics problems, heat transfer problems, and free vibration problems **A: by using FE packages such as NASTRAN/ ANSYS/ SIMULIA/ ABAQUS**	
2	Introduction of GUI of the software in the above mentioned areas realistic problems.	
3	Analysis of beams and frames (bending and torsion problems)	
4	Plane stress and plane strain analysis problems	
5	Problems leading to analysis of axisymmetric solids	
6	Problems leading to analysis of three dimensional solids (a) Heat transfer problems (b) Modal analysis problem	
	B: by writing own code for finite element analysis using MATLAB for:	
7	Plane stress and plane strain analysis problems	
8	Modal Analysis problem	

8ME1A: COMPUTER INTEGRATED MANUFACTURING SYSTEMS

B.Tech. (Mechanical) 8th semester **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**Introduction to CIM:** Overview of Production Systems, the product cycle, Automation in Production Systems, computer's role in manufacturing, sources and types of data used in manufacturing. The Beginning of CAM: Historical Background,	2
	Numerical Control (NC): Basic components of an NC system, coordinate system and motions control systems. Computer Numerical Control (CNC): features of CNC, machine control unit, CNC software. Direct Numerical Control and Distributed Numerical Control. Applications, advantages and disadvantages of NC. Adaptive control of machining system.	3
II	NC Part programming: Manual and computer assisted part programming, Part programming with APT. NC part programming using CAD/CAM software. NC cutter path verification.	8
III	Computer Aided Process Planning: Traditional Process Planning, Retrieval process planning system, Generative Process Planning, Machinability data systems, computer generated time standards.	4
	Group Technology: Introduction, part families, part classification and coding, coding system and machining cells.	4
IV	Computer Aided Production Management Systems: Introduction to computer aided PPC, Introduction to computer aided inventory management, manufacturing resource planning (MRPII), computer process monitoring and shop floor control, computer process control.	6
	Computer Aided Quality Control; Computer in quality control, contact inspection methods, Non contact inspection methods, optical and non optical computer aided testing.	2
V	Computer Aided Material Handling; Computer control on material handling, conveying, picking. Ware house control, computerized material handling for automated inspection and assembly.	

	Computer Integrated Manufacturing Systems: Introduction, types special manufacturing systems, flexible manufacturing systems (FMS).	**5**
	Collaborative Engineering; Introduction, Faster Design throughput, Web based design, Changing design approaches, extended enterprises, concurrent engineering, Agile and lean manufacturing.	**3**
	TOTAL	**40**

TEXT BOOK		
1	Mikell P. Groover, , Automation, Production Systems, and Computer-Integrated Manufacturing, 3rd ed., Pearson/Prentice Hall,	2008
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	James A. Rehg and Henry W. Kraebber, 2005, Computer-Integrated Manufacturing, 3rd ed., Pearson/Prentice Hall,	
2	Nanua Singh, 1996, Systems Approach to Computer-Integrated Design and Manufacturing, John Willey & Sons.	
3	Computer Aided Manufacturing, Chang, Wysk and Wang, Pearson Education	
4	CAD/CAM: Principles and Applications, P.N. Rao, McGraw Hill	
5	Computer Control of Manufacturing Systems,Y. Koren, McGraw Hill	
6	Computer aided Manufacturing, Rao, Tiwari and Kundra, Tata McGraw Hill.	
7	Computer Numerical Control: Machining and Turning Centres, Quesada and Jeyepoovan, Pearson Education	

8ME2A: LAWS FOR ENGINEERS

B.Tech. (Mechanical) 8th semester **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	**CONTENTS**	**CONTACT HOURS**
	Constitutional Law*:* The Preamble; Fundamental Rights; Directive principles of State policy; Fundamental Duties; Emergency provisions – kinds, legal requirements and legal effects.	**5**
I	**General Principles of Contract under Indian Contract Act, 1872:** General principles of contract – Sec. 1 to 75 of Indian Contract Act and including Government as contracting party, Kinds of government contracts and dispute settlement, Standard form contracts; nature, advantages, unilateral character, principles of protection against possibility of exploitation, judicial approach to such contracts, exemption clauses, clash between two standard form contracts.	**4**
II	**Introduction to Human Rights:** Theoretical foundation, Historical development of human rights; Human Rights in Indian tradition and Western tradition; Covenant on Civil & Political Rights 1966 including Optional Protocol – I (Individual Complaint Mechanism) & Optional Protocol – II (Abolition of Death Penalty); Covenant on Economic, Social and Cultural Rights 1966 including Optional Protocol – I (2002);	**4**
	Enforcement of Human Rights in India including Supreme Court, High Courts, Statutory Commissions – NHRC, NCW, NCM, NC-SC/ST etc.	**4**
	Labour Laws: Industrial Disputes Act, 1947; Collective bargaining; Industrial Employment (Standing Orders) Act, 1946; Workmen"s Compensation Act, 1923.	**3**
III	**Right to Information Act, 2005:** Evolution and concept; Practice and procedures; Official Secret Act, 1923; Indian Evidence Act, 1872;	**3**

	Information Technology – legislation and procedures, Cyber crimes – issues and investigations.	
	Law relating to Intellectual property: Introduction–meaning of intellectual property, main forms of IP, Copyright, Trademarks, Patents and Designs, Secrets; International instruments on IP – Berne convention, Rome convention, TRIPS, Paris convention and international organizations relating IPRs, WTO etc;	**4**
IV	Law relating to Copyright in India, Meaning of copyright – literary, dramatics and musical works, sound records and cinematographic films, computer programs, Ownership of copyrights, Criteria of infringement, Piracy in Internet – Remedies and procedures in India;	**1**
	Law relating to Trademarks under Trademark Act, 1999 including Rationale of protection of trademarks as Commercial aspect and Consumer rights, Trademarks, registration, procedures, Distinction between trademark and property mark, Doctrine of deceptive similarity, Passing off an infringement and remedies;	**2**
	Law relating to Patents under Patents Act, 1970, Patentable inventions with special reference to biotechnology products, Patent protection for computer programs, Process of obtaining patent –application, examination, opposition and sealing of patents, Patent cooperation treaty and grounds for opposition, Rights and obligations of patentee, Duration of patents – law and policy considerations, Infringement and related remedies.	**3**
V	**Corporate Law**: Meaning of corporation; Law relating to companies, public and private (Companies Act, 1956) general provisions; Law and multinational companies – International norms for control, FEMA 1999, Corporate liability, civil and criminal.	**4**
	Election provisions under Indian Constitution (Art.324–329): Representation of Peoples Act and Prevention of Corruption Act, 1988; Superintendence, directions and control of elections to be vested in Election Commission; Election to the house of people and to the legislative assemblies of States to be on the basis of adult suffrage. Candidate electoral rights.	**3**
	TOTAL	**40**

REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	D.D. Basu, Shorter Constitution of India, Prentice Hall of India	1996
2	M.P. Jain, Indian Constitutional Law, Wadhwa & Co.	2005
3	S.K. Awasthi & R.P. Kataria, Law relating to Protection of Human Rights, Orient Publishing	2006
4	S.K. Kapur, Human Rights under International Law and Indian Law, Central Law Agency	2001
5	Avtarsingh, Law of Contract, Eastern Book Co	2002
6	Wadhera , Intellectual Property Rights, Universal Law Publishing Co	2004
7	T. Ramappa, Intellectual Property Rights Law in India, Asia Law House	2010
8	O.P. Malhotra, Law of Industrial Disputes, N.M. Tripathi Publishers	

8ME3A: POWER GENERATION

B.Tech. (Mechanical) 8th Semester **Max. Marks: 100**
3L+1T **Exam Hours: 3**

UNIT	**CONTENTS**	**CONTACT HOURS**
I	**Introduction to economics of power generation:** Load duration	**7**

	curves, location of power plants, power plant economics.	
II	**Analysis of Steam Power Plants (SPP):** Components of steam power plants, Effect of variations, variation of steam condition on thermal efficiency of steam power plant. Typical layout of SPP. Efficiencies in a SPP.	**9**
III	**Analysis of Hydroelectric Power Plants (HEPP):** Components of HEPP, Typical layout of HEPP, Performance of turbines and comparison.	**4**
	Analysis of Diesel and Gas Turbine Power Plants: General layout of Diesel and Gas Turbine power plants, Performance of Diesel and Gas Turbine power plants, comparison with other types of power plants.	**4**
IV	**Wind Energy:** Wind energy potential measurement, general theories of wind machines, basic laws and concepts of aerodynamics, aerofoil design; wind mill and wind electric generator. Description and performance of the horizontal–axis wind machines. Description and performance of the vertical–axis wind machines. The generation of electricity by wind machines,	**8**
V	**Solar radiation:** its measurement and prediction. Flat plate collectors, liquid and air type. Theory of flat plate collectors, advanced collectors, optical design of concentrators, selective coatings, solar water heating, thermal storage. Conversion of heat into mechanical energy. Solar cells, photovoltaic effect, performance of a solar cell, P-V material, performance of solar cells, P-V modules. Solar P-V plants, Economies of solar photovoltaic's.	**8**
		40

TEXT BOOK		
1	P.K.Nag, Power Plant Engineering, Tata McGraw Hill	2008
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Hau E., Wind Turbines: Fundamentals, Technologies, Application and Economics, Springer	**2000**
2	Mathew S.,Wind Energy: Fundamentals, Resource Analysis and Economic s, Springer	2006
3	Burton T. Sharpe D. Jenkins N. and Bossanyi E.,Wind Energy Handbook, John Wiley	2001
4	Jiandong T. (et al.) , Mini Hydropower, John Wiley	
5	Duffie J. A. and Beckman W. A. ,Solar Engineering of Thermal Processes, John Wiley	1997
6	Goswami D. Y. Kreith F. and Kreider J. F. Principles of Solar Engineering, Taylor and Francis	2006
7	Garg H. P. and Prakash S.Solar Energy: Fundamental and Application, Ta ta McGraw Hill	1999
8	Green M.,Third Generation Photovoltaics: Advance Solar Energy, Springer	1997
9	Tiwari G. N.,Solar Energy: Fundamentals, Design, Modeling and Applicati ons, Narosa	2005
10	Johnson G. L.Wind Energy Systems (Electronic Edition), Prentice Hall	2002
11	Wagner H. and Mathur J. Introduction to Hydro energy Systems : Basics, Technology and Operation, Springer	2006
12	Nayak J. K. and Sukhatme S.P. Solar Energy: Principles of Thermal Colle ction and Storage, Tata McGraw Hill	2011
13	Solanki C. S. Solar Photovoltaics: Fundamentals, Technologies and Applications, Prentice Hall India	2006
14	F.T.Morse, D.Van.Nostran, Power Plant Engineering, Newyork,	2009
15	Johnson G. L.Wind Energy Systems (Electronic Edition), Prentice Hall	1953

16	Wagner H. and Mathur J. Introduction to Hydro energy Systems : Basics, Technology and Operation, Springer	2006
17	M.M.EI- Wakil, Power Plant Technology, McGraw Hill	1984

8ME4.1A: PRODUCT DEVELOPMENT AND LAUNCHING

B.Tech. (Mechanical) 8th semester **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**Importance of New Product:** Definition-importance-Development Process, Importance of new product for growth of enterprise, Definition of product and new product,	**2**
	Responsibility for new product development, Demands on product development team, Classification of products from new product development point of view- Need based/Market pull products, Tech. push, Platform based, Process based and customized products,	**3**
	New product development process and organization, Generic product development process for Market Pull Products, Modification of this process for other types of products.	**3**
II	**Need Analysis:** Problem Formulation Establishing economic existence of need, Need Identification and Analysis, Engineering Statement of Problem, Establishing Target Specification.	**8**
III	**Generation of Alternatives and Concept Selection:** Concept generation- a creative process, Creativity, Road Elects to creative thinking-Fear of criticism and Psychological set,	**4**
	Tools of creativity like brain storming, Analogy, Inversion etc., Creative thinking Process, Concept feasibility and Concept Selection, Establishing Engineering Specification of Products.	**4**
IV	**Preliminary and Detailed Design:** Design Review Preliminary design-Identification of subsystems, Subsystem specifications, Compatibility, Detailed design of subsystems, component design,	**6**
	Preparation of assembly drawings, Review of product design from point of view of Manufacturing, Ergonomics and aesthetics.	**2**
V	**Management of New Product:** Development and Launch New Product Management's Challenges, Maintaining focus, Promotion of Right Culture, Management of Creativity, Top Management attention, Design Team Staffing and Organization, Setting key mile stone, Identification of Risk Areas, Project Execution and Evaluation Product Launch Strategies,	**8**
	TOTAL	**40**

TEXT BOOK		
1	Product Design and Manufacturing, Chitale and Gupta. McGraw Hill.	
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Product Design and Development, Ulrich and Eppinger, McGraw Hill	**2003**
2	Project Management in New Product Development, Barkley B.T., Tata McGraw Hill.	**2008**
3	Product Management, Anandan C., McGraw Hill.	**2009**
4	Engineering Design Methods, Cross, Nigel, John Wiley and Sons.	**1995**
5	Product Design and Manufacture, Lindbeck, J.R., Prentice Hall of India.	**1995**

8ME4.2: COMPUTATIONAL FLUID DYNAMICS

B.Tech. (Mechanical) 8th semester **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	**Introduction to Computational Fluid Dynamics and Principles of Conservation**: Conservation of mass, linear momentum: Navier-Stokes equation, Conservation of Energy, General scalar transport equation, Reynolds transport theorem,	4
	Classification of Partial Differential Equations and Physical Behaviour: Elliptic, parabolic and hyperbolic partial differential equations	2
	Approximate Solutions of Differential Equations: Error Minimization Principles, Approximate solutions of differential equations, variational approach, Weighted residual approach: trial function and weighting function, Essential and natural boundary conditions, Least square method, Galerkin's method, Rayleigh-Ritz method	4
II	**Fundamentals of Discretization:** Pre-processing, Solution, Post-processing, Finite Element Method, Finite difference method, Well posed boundary value problem, Conservativeness, Boundedness, Transportiveness, Finite volume method (FVM), 1-D steady state heat conduction without and with constant source term	3
	Finite Volume Method: FV Discretization of a 1-D steady state diffusion type problem, Composite material with position dependent thermal conductivity, Source term linearization, Implementation of boundary conditions, 1-D unsteady state diffusion problems: implicit, fully explicit and Crank-Nicholson scheme	4
III	**Solution of Systems of Linear Algebraic Equations:** Solution techniques for systems of linear algebraic equations: Elimination, Iteration and Gradient Search method, L-U decomposition technique, Tridiagonal matrix algorithm (TDMA): Thomas algorithm	4
	Iteration methods: Generalized analysis of the iterative methods, Sufficient condition for convergence, Scarborough criteria of for convergence Relaxation methods, Preferential characteristics of iterative methods, Multigrid method, Line by line TDMA, Alternating direction implicit method, Gradient search methods: Steepest descent method, Conjugate gradient method	4
IV	**Discretization of Convection-Diffusion Equations: A Finite Volume Approach:** Central difference scheme, Upwind scheme, Exponential scheme and Hybrid scheme, Power law scheme, Generalized convection-diffusion formulation, The concept of false diffusion, QUICK scheme.	5
	Discretization of Navier Stokes Equations: Discretization of the Momentum Equation: Stream Function-Vorticity approach and Primitive variable approach, Staggered grid and Collocated grid, SIMPLE Algorithm, SIMPLER Algorithm	3
V	**Introduction to Turbulence Modeling:** Vorticity transport equation, Homogeneous turbulence and isotropic turbulence, Reynolds average Navier stokes (RANS) equation, Necessity of turbulence modeling, Turbulence model: Eddy viscosity, Mixing length, The κ-ε model, RNG κ-ε model, κ-ω model, Reynolds stress model (RSM),Large eddy Simulation (LES),Direct numerical simulation (DNS)	4
	The basic structure of a CFD code: Pre-processor, Solver and Post-processor, User-defined-subroutines, Solution to some basic problems in heat transfer and fluid flow	3
	TOTAL	**40**

TEXT BOOK	

1	Computational Fluid Dynamics, John Anderson, McGraw Hill Publication	
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Computational Fluid Dynamics, Jiynan Tu, Butter Worth Henman.	**1998**
2	Computational Fluid and Heat Transfer, Anderson & Tannehill, Taylor & Francis Publication.	**1997**
3	Computational Methods for Fluid Dynamics, Joel H. Ferziger, Springer Publication.	**2009**
4	Computational Heat Transfer, Jaluria Y., Taylor and Francis Publication.	**1996**
5	Computational Heat Transfer and Fluid Flow, Murlidhar and T. Sunder Rajan, Narosa Publications	**2011**

8ME4.3A: TOTAL QUALITY MANAGEMENT

B.Tech. (Mechanical) 8th semester **Max. Marks: 100**
3L+0T **Exam Hours: 3**

UNIT	CONTENTS	CONTACT HOURS
I	Introduction to TQM: Definition, Basic approach, Guru's of TQM, TQM framework, benefits.	**2**
	Leadership: Characteristics of Quality Leadership, Leadership Concepts, The 7 Habits of Highly Effective People, The Deming Philosophy, The Role of TQM Leaders, Quality Council, Core Values, Concepts, and Framework, Quality Statements, Strategic Planning Communications, Decision Making.	**3**
	Customer Satisfaction: Introduction, Customer Perception of Quality, Feedback, Using Customer Complaints, Service Quality, Translating Needs into Requirements, Customer Retention.	**3**
II	Continuous Process Improvement: Introduction, Process, The Juran Trilogy, Improvement Strategies, Types of Problems PDSA Cycle, Problem-Solving Method, DMAIC, Kaizen, Reengineering.	**3**
	Supplier Partnership: Principles of Customer/Supplier Relationship Partnering, Sourcing Supplier, Selection ,Supplier Certification Supplier Rating, Relationship Development.	**2**
	Performance Measures: Basic Concepts, Strategy, performance measure presentation, Cost of Quality, Malcolm Baldrige and Rajiv Gandhi National Quality Award, Balanced Score Card	**3**
III	Lean Enterprise: Historical Review, Lean Fundamentals, Value Stream Map, Implementing Lean, Benefits.	**3**
	Six Sigma: Statistical Aspects, Improvement Methodology, Organizational Structure Benefits.	**3**
	Benchmarking: Benchmarking Defined, Reasons to Benchmark, Process, deciding what to benchmark, Pitfalls and Criticisms.	**2**
IV	Quality Management Systems: Benefits of ISO Registration, ISO Series of Standards, Sector-specific Standards, ISO 9001 Requirements, Implementation, Documentation, Writing the Documents, Internal Audits, Registration.	**2**
	Environmental Management Systems: ISO 14000 Series Standards, Concepts of ISO 14001, ISO 14001, Requirements, Benefits, Integrating QMS and EMS. Other EMS Systems, Relationship to Health and Safety	**2**
	Quality Function Deployment: The QFD Team, Benefits, the voice of the Customer, Organization of Information, House of Quality, Building a House of Quality, QFD Process.	**2**
	Total Productive Maintenance: The Plan, Learning the New Philosophy, Promoting the Philosophy, Training, Improvement Needs, Goal,	**2**

	Developing Plans, Autonomous Work Groups	
V	Management Tools: Forced Field Analysis, Nominal Group Technique, Affinity Diagram, Interrelationship Digraph, Tree Diagram, Matrix Diagram, Prioritization Matrices, Process Decision Program Chart, Activity Network Diagram	2
	Experimental Design: Introduction, Basic Statistics, Hypothesis, t Test F Test. One Factor at a Time Orthogonal Design, Point and Interval Estimate, Two Factors Full Factorials.	3
	Taguchi's Quality Engineering: Introduction, Loss Function, Orthogonal Arrays, Signal-to-Noise Ratio, Parameter Design, Tolerance Design, Case study	3
	TOTAL	**40**

TEXT BOOK		
1	D. H. Besterfield, G. H Besterfield, Hemant Urdhwareshe, Total Quality Management: Revised Third Edition, Pearson Higher Education	2013
REFERENCE BOOKS		
SN	**Name of Authors /Books /Publisher**	**Year of Pub.**
1	Total Quality Management: text with cases, John S Oakland, Butterworth-Heinemann	2003
2	Total Quality Management for Engineers, Zaire, M., Wood Head Publishing Ltd.	1991
3	Total Quality Control, Feigenbaum. Armand V., McGraw Hill	1991
4	The Management and Control of Quality,(5th Edition), James R.Evans and William M.Lidsay, South-Western (Thomson Learning)	2002
5		

8ME5A: CAM LAB.

B.Tech. (Mechanical) 8th Semester **Max. Marks: 75**
0L+0T+2P **Exam Hours: 2**

SN	NAME OF EXPERIMENT	CONTACT HOURS
1	To prepare part programming for plain turning operation.	
2	To prepare part programming for turning operation in absolute mode.	
3	To prepare part program in inch mode for plain turning operation.	
4	To prepare part program for taper turning operation.	
5	To prepare part program for turning operations using turning cycle.	
6	To prepare part program for threading operation.	
7	To prepare part program for slot milling operation.	
8	To prepare part program for gear cutting operation.	
9	To prepare part program for gear cutting using mill cycle.	
10	To prepare part program for drilling operation.	
11	To prepare part program for multiple drilling operation in Z-axis.	
12	To prepare part program for multiple drilling in X-axis.	
13	To prepare part program for multiple drilling in X and Z axis using drilling cycle.	

8ME6A: CAD LAB.

B.Tech. (Mechanical) 8th Semester **Max. Marks: 100**
0L+0T+3P **Exam Hours: 3**

SN	NAME OF EXPERIMENT	CONTACT

		HOURS
1	Introduction and different features of the CAD Software.	
2	2-D Drafting.	
3	3-D Modeling.	
4	3-D Advanced Modeling.	
5	Assembly modeling.	
6	Feature Modification and Manipulation	
7	Detailing.	
8	Sheet Metal Operations.	
9	Surface Modeling	
10	One Dimensional problems of Finite Element Method Note: (These exercises may be performed by any of the following Advanced CAD Software such as Pro E /Unigraphics/ AotoCAD Inventor)	

8ME7A: INDUSTRIAL ENGINEERING LAB-II

B.Tech. (Mechanical) 8th Semester **Max. Marks: 75**

0L+0T+2P **Exam Hours: 2**

SN	NAME OF EXPERIMENT	CONTACT HOURS
1	Determination of time standard for a given job using stopwatch time-study.	
2	Preparation of flow process chart, operation process chart and man-machine charts for an existing setup and development of an improved process.	
3	Study of existing layout of a workstation with respect to controls and displays and suggesting improved design from ergonomic viewpoint.	
4	To carry out a work sampling study.	
5	To conduct process capability study for a machine in the workshop.	
6	To design a sampling scheme based on OC curve.	
7	To conduct Shewart's experiments on known population	
8	Generation of random numbers for system simulation such as facility planning, job shop scheduling etc.	