



POORNIMA
COLLEGE OF ENGINEERING

An autonomous institution approved by RTU, AICTE & UGC • NAAC A+ Accredited



1.1.2 Remedial Classes Time Tables

Department of Electronics & Communication Engineering

(2023-24)

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Poornima College of Engineering
Department of Electronics & Communication Engineering
NOTICE

Date: 01/12/2023

Remedial classes for B.Tech II Year III Semester and III Year V Semester will commence from 05/12/23. All the student are requested to attend the same as per their regular time table.

Dr. Garima Mathur
HOD EC

Poornima College of Engineering
Department of Electronics & Communication Engineering
NOTICE

Date: 25/05/2024

Remedial classes for B.Tech II Year IV Semester and III Year VI Semester will commence from 29/05/24. All the student are requested to attend the same as per their regular time table.

Dr. Garima Mathur

HOD EC

POORNIMA COLLEGE OF ENGINEERING

Remedial Classes Time Table (SESSION 2023-2024)

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Class:- II Year III Sem. Venue: (CT-13) Tutor- Dr. Nitesh Mudgal

DAY/TIME	08:30-9:30	9:30-10:30	10:30-11:30	11.30-12.00	12:00-1:00	1:00- 2:00	2:00-3:00	3:00-4:00
MONDAY	3EC2-01/AEM/SD	3EC1-03/MEFA/KS	3EC4-04/DSD/NM	11.30-12.00 LUNCH	3EC4-23/SP Lab/A1AT-20(JJ)		3EC4-07/ED/MN	PTS/ NSP/ Activity
TUESDAY	3EC4-06/ NT/SH	3EC4-22/DSD LAB/A1 / AT-01(NM)			3EC4-07/ED/MN	3EC4-04/DSD/NM	3EC2-01/AEM/SD	PTS/ NSP/ Activity
WEDNESDAY	3EC4-06/NT/SH	3EC4-07/ED/MN	3EC4-05/A1/SS/JJ		3EC4-07/ED/MN	3EC2-01/AEM/SD	3EC1-03/MEFA/KS	PTS/ NSP/ Activity
THRUSDAY	3EC4-04/DSD/NM	3EC4-06/ NT/SH	3EC4-05/A1/SS/JJ		3EC3-24/CP Lab/A1 /AT-20(AB)		3EC4-05/A1/SS/JJ	PTS/ NSP/ Activity
FRIDAY	3EC4-05/A1/SS/JJ	3EC4-21/ED Lab/A1/ AT-02 (SH)			3EC4-04/DSD/NM	3EC4-06/ NT/SH	3EC2-01/AEM/SD	PTS/ NSP/ Activity
SATURDAY	I3 Class				I3 Class			
Dr. Payal Bansal, TT Coordinator		Dr. Garima Mathur, HoD, ECE	Mr. Pankaj Dhemla, Vice Principal, PCE				Dr. Mahesh Bundele, Director, PCE	
Subject Abbreviations				Faculty Abbreviations				
ED - Electronics Devices				GM-Dr Garima Mathur				
NT - Network Theory				SH-Dr. Surender Hans				

DSD - Digital System Design	PB-Dr. Payal Bansal
SS - Signal & Systems	NM- Dr. Nitesh Mudgal
AEM - Advanced Engineering Mathematics-I	MN-Dr. Meetu Nag
MEFA- Managerial Economics and Financial Accounting	JJ- Ms. Jyotsna Joshi
SP Lab- Signal Processing Lab	SD- Dr. Shuchi dave
CP Lab - Computer Programming Lab-I	AB- Ms. Apporva Bansal
ED Lab - Electronics Devices Lab	KS - Ms. Kalpna Sharma
DSD Lab - Digital System Design Lab	

POORNIMA COLLEGE OF ENGINEERING								
Remedial Classes Time Table (SESSION 2023-2024)								
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING								
Class:- III Year V Sem. Venue: (CT-04) Tutor- Dr. Meetu Nag								
DAY/TIME	08:30-9:30	9:30-10:30	10:30-11:30	11.30-12.00	12:00-1:00	1:00- 2:00	2:00-3:00	
MONDAY	SC (5ECE5-14)	EW5EC4-02	CA (5EC3-01)	LUNCH	MTT(5EC4-05)	CS(5EC4-03)	DSP (5EC4-04)	
TUESDAY	DSP (5EC4-04)	EW5EC4-02	CS(5EC4-03)		MW Lab (5EC4-23)		SC (5ECE5-14)	
WEDNESDAY	SC (5ECE5-14)	DSP (5EC4-04)	CA (5EC3-01)		EW5EC4-02	MTT(5EC4-05)	CS(5EC4-03)	
THRUSDAY	EW5EC4-02	RF Simulation Lab (AT-20B)			MTT(5EC4-05)	DSP (5EC4-04)	CS(5EC4-03)	
FRIDAY	SC (5ECE5-14)	DSP Lab/5EC4-22/NM (AT-20B)			MTT(5EC4-05)	CA (5EC3-01)		
SATURDAY								
Dr. Meetu Nag, TT Coordinator		Dr. Garima Mathur, HoD, ECE		Mr. Pankaj Dhemla, Vice Principal, PCE			Dr. Mahesh Bundele, Director, PCE	
Subject Abbriviations				Faculty Abberiviations				
EW - Electronics Waves				GM-Dr Garima Mathur				
ST - Satellite Communication				SH-Dr. Surender Hans				
DSP - Digital Signal Pprocessing				PB-Dr. Payal Bansal				
CA - Computer Archtiecture				NM- Dr. Nitesh Mudgal				
CS- Control Syystem				MN-Dr. Meetu Nag				
MTT- Microwave Theory & Techniques				JJ- Ms. Jyotsna Joshi				
DSP Lab- Digital Signal Processing Lab								
MW Lab - Microwave Lab								
RF Lab- Simulation lan								
IT- Industrial traning & Seminar								

